Pch -45V -6A Power MOSFET

$V_{DSS}$	-45V
R <sub>DS(on)</sub> (Max.)	36mΩ
I <sub>D</sub>	±6.0A
P <sub>D</sub>	2.0W

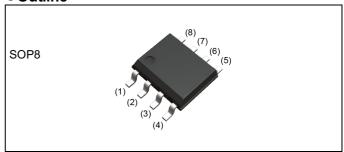
# ● Features

- 1) Low on-resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free
- 5) Sn100% plating
- 6) AEC-Q101 Qualified

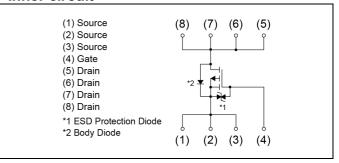
# Application

Switching

### Outline



## ●Inner circuit



Packaging specifications

	Packing	Embossed Tape			
	Reel size (mm)	330			
Туре	Tape width (mm)	12			
	Quantity (pcs)	2500			
	Taping code	ТВ			
	Marking	RSS060P05			

# ullet Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-45	V
Continuous drain current	I <sub>D</sub>	±6.0	А
Pulsed drain current	I <sub>DP</sub> *1	±24	А
Gate - Source voltage	$V_{GSS}$	±20	V
Down discipation	P <sub>D</sub> *2	2.0	W
Power dissipation	P <sub>D</sub> *3	1.4	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

# ●Thermal resistance

Deremeter	Cumb of	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
Thermal registance innetion, ambient	R <sub>thJA</sub> *2	1	1	62.5	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	ı	1	89.2	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions			Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-45	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-50	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -45V, V <sub>GS</sub> = 0V	-	-	-1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	-	-2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	3.3	-	mV/°C	
		V <sub>GS</sub> = -10V, I <sub>D</sub> = -6.0A	-	26	36		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	$V_{GS} = -4.5V, I_D = -6.0A$	-	35	49	mΩ	
		$V_{GS} = -4.0V, I_D = -6.0A$	-	38	53		
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	3.6	1	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	$V_{DS} = -10V, I_{D} = -6.0A$	8.0	-	-	S	

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a Cu board (40×40×0.8mm)

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2700	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	360	-	pF
Reverse transfer capacitance	$C_{rss}$	f = 1MHz	-	230	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq -25V, V_{GS} = -10V$	-	25	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -3.0A	-	28	-	no
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 8.3\Omega$	-	100	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	28	-	

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ a	,				
Parameter	ter Symbol Conditions -			Values	_	Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Qg*4	V <sub>DD</sub> ≃ <b>-</b> 25V.	-	23.0	32.2	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \approx -25V,$ $I_{D} = -6.0A,$ $V_{GS} = -5V$	-	6.6	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	V <sub>GS</sub> = -5V	-	8.0	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol	Conditions	Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	-1.6	Α	
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	-24	Α	
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = -6.0A	-	-	-1.2	V	

### • Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

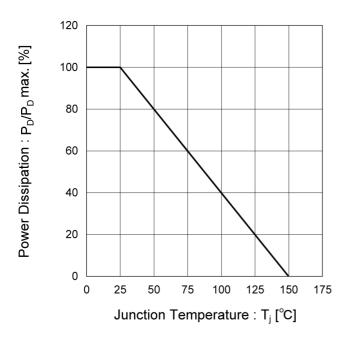
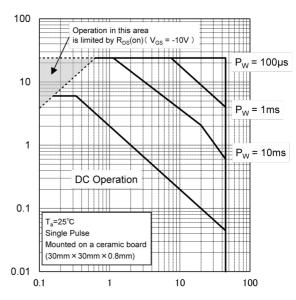


Fig.2 Maximum Safe Operating Area



Drain Current: -l<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

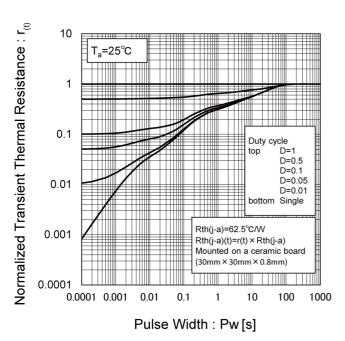
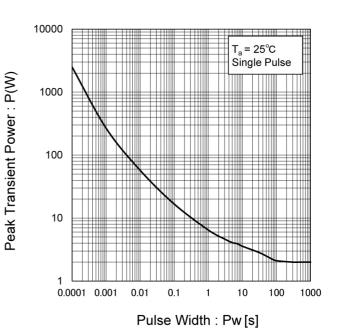


Fig.4 Single Pulse Maximum Power dissipation



### Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

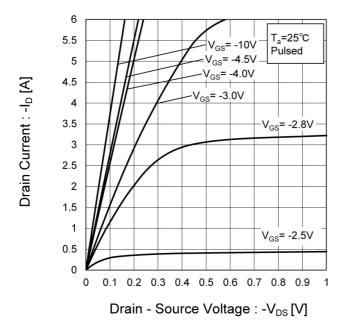


Fig.6 Typical Output Characteristics(II)

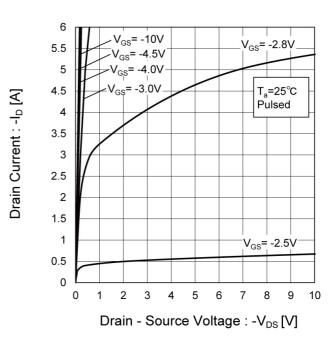


Fig.7 Breakdown Voltage vs.

Junction Temperature

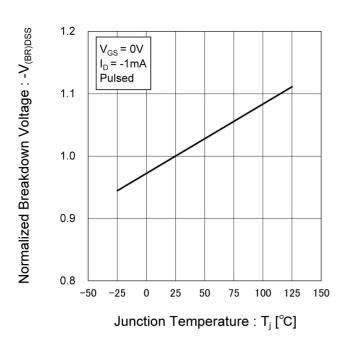
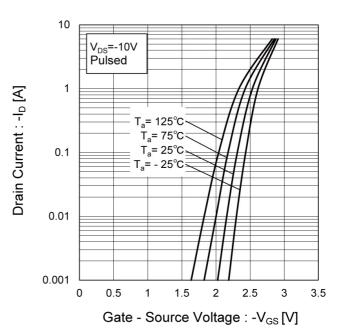


Fig.8 Typical Transfer Characteristics



RSS060P05HZG

## • Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

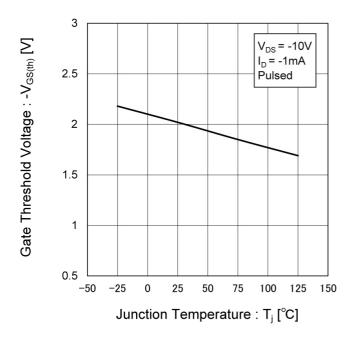
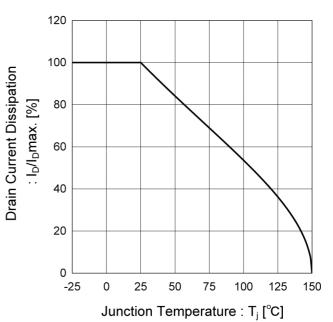


Fig.10 Drain Current Derating Curve



RSS060P05HZG

## • Electrical characteristic curves

Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

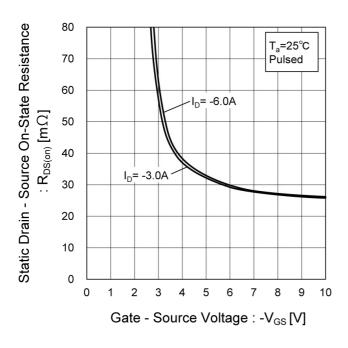


Fig.12 Static Drain - Source On - State Resistance vs. Junction Temperature

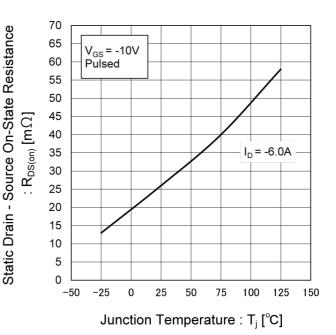
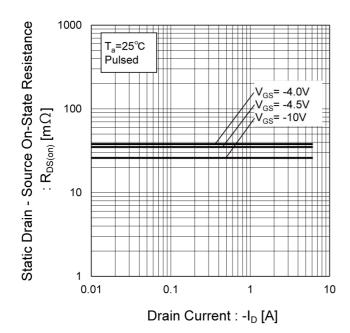


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current (I)



## • Electrical characteristic curves

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (II)

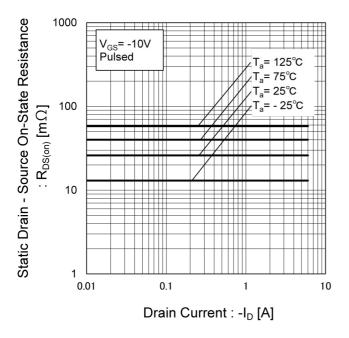


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (III)

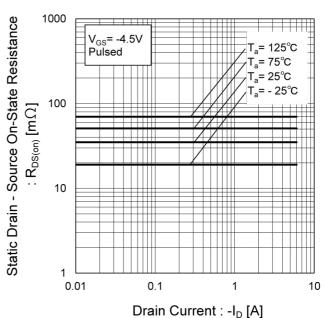
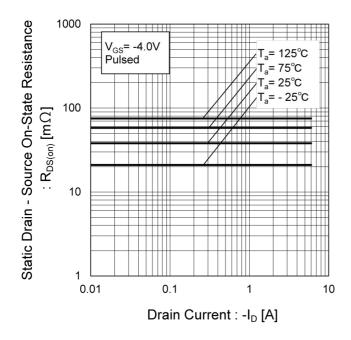


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (IV)



## • Electrical characteristic curves

Fig.17 Typical Capacitance vs.

Drain - Source Voltage

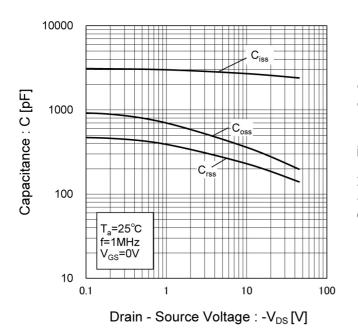


Fig.18 Switching Characteristics

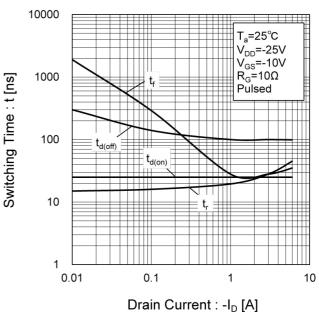


Fig.19 Dynamic Input Characteristics

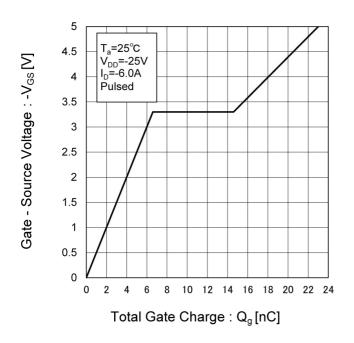
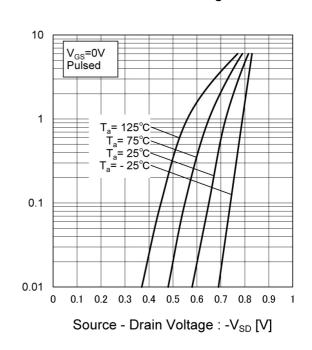


Fig.20 Source Current vs.

Source Drain Voltage



Source Current : -Is [A]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

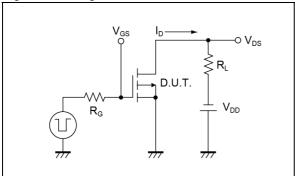


Fig.2-1 Gate Charge Measurement Circuit

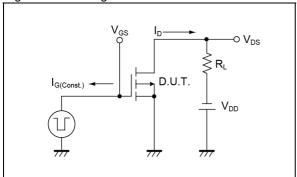


Fig.1-2 Switching Waveforms

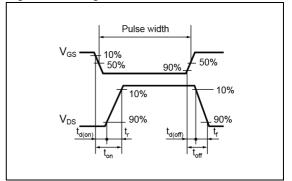
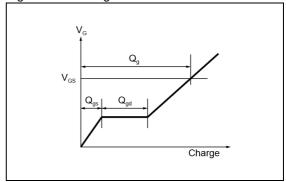
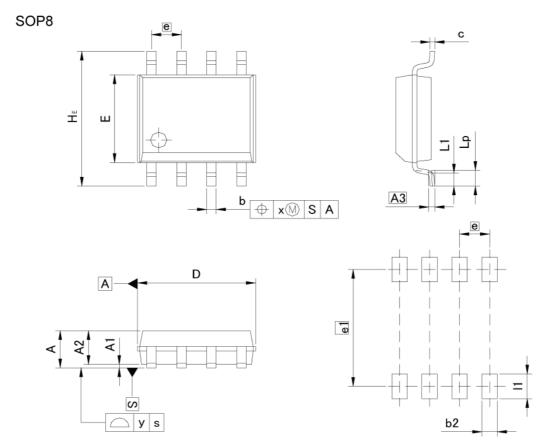


Fig.2-2 Gate Charge Waveform



## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	_	1.75	-	0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.	25	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1,	27	0.0	50
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
х	0.	15	0.0	06
У	0.	10	0.004	
DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.65		0.026	
e1	5.	15	0.2	03	
11	-,	1.15	- 1	0.045	

Dimension in mm/inches



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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
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WMJ80N60C4 BXP2N20L BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR DMNH15H110SK3-13
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