# 4V Drive Nch MOS FET RSS095N05

#### Structure

Silicon N-channel MOS FET

#### Features

- 1) Built-in G-S Protection Diode.
- 2) Small Surface Mount Package (SOP8).

### Applications

Power switching , DC / DC converter , Inverter

# Packaging dimensions

Package	Taping
Code	TB
Basic ordering unit(pieces)	2500

# ● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol		Limits	Unit	
Drain-source voltage	$V_{DSS}$		45	V	
Gate-source voltage	$V_{GSS}$		20	V	
Drain current	Continuous	$I_D$		±9.5	Α
Diam current	Pulsed	$I_{DP}$	*1	±38	Α
Source current	Continuous	Is		1.6	Α
(Body diode)	Pulsed	$I_{SP}$	*1	38	Α
Total power dissipation	$P_{D}$	*2	2	W	
Chanel temperature	$T_{ch}$		150	°C	
Range of Storage temp	$T_{stg}$		-55 to +150	°C	

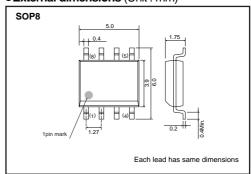
<sup>\*1</sup> PW≤10μs, Duty cycle≤1%

# ●Thermal resistance

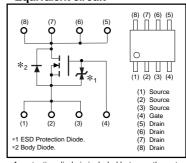
Parameter	Symbol	Limits	Unit
Chanel to ambient	R <sub>th(ch-a)</sub> *	62.5	°C/W

<sup>\*</sup> Mounted on a ceramic board

## ●External dimensions (Unit : mm)



# ●Equivalent circuit



<sup>\*</sup> A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use.Use a protection circuit when the fixed voltage are exceeded.

<sup>\*2</sup> Mounted on a ceramic board

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	10	μΑ	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	45	_	_	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	IDSS	_	_	1	μΑ	V <sub>DS</sub> = 45V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	1.0	_	2.5	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain-source on-state resistance		_	11	16	mΩ	I <sub>D</sub> = 9.5A, V <sub>GS</sub> = 10V
	R <sub>DS (on)</sub> *	-	14	20	mΩ	I <sub>D</sub> = 9.5A, V <sub>GS</sub> = 4.5V
		-	15	21	mΩ	I <sub>D</sub> = 9.5A, V <sub>GS</sub> = 4V
Forward transfer admittance	Y <sub>fs</sub> *	10.0	_	_	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 9.5A
Input capacitance	Ciss	-	1830	_	pF	V <sub>DS</sub> = 10V
Output capacitance	Coss	_	410	_	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	Crss	-	210	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	-	20	_	ns	V <sub>DD</sub> ≒ 25V
Rise time	tr *	-	35	_	ns	ID= 5.0A
Turn-off delay time	td (off) *	-	78	_	ns	V <sub>GS</sub> = 10V R <sub>L</sub> =5Ω
Fall time	t <sub>f</sub> *	-	31	_	ns	R <sub>G</sub> =10Ω
Total gate charge	Qg *	_	18.9	26.5	nC	V <sub>DD</sub> =25V V <sub>GS</sub> =5V
Gate-source charge	Q <sub>gs</sub> *	_	4.9	_	nC	I <sub>D</sub> = 9.5A
Gate-drain charge	Q <sub>gd</sub> *	-	7.2	_	nC	R <sub>L</sub> =2.6Ω R <sub>G</sub> =10Ω

<sup>\*</sup>Pulsed

# Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp*	_	_	1.2	V	I <sub>S</sub> = 9.5A, V <sub>GS</sub> =0V

<sup>\*</sup>Pulsed

#### Electrical characteristic curves

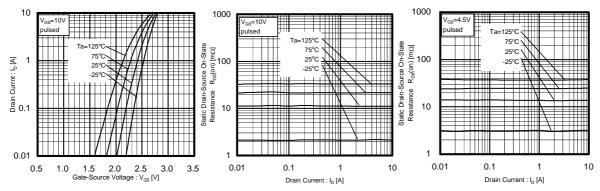
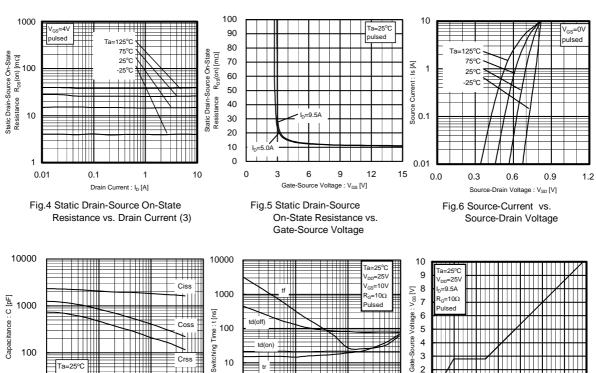


Fig.1 Typical Transfer Characteristics

Fig.2 Static Drain-Source On-State Resistance vs. Drain Current (1)

Fig.3 Static Drain-Source On-State Resistance vs. Drain Current (2)



Capacitance : C [pF] 0001 Switching Time : t [ns] 10 f=1MHz 10 0.01 0.1 10 100 1 Drain-Source Voltage : V<sub>DS</sub> [V]

Fig.7 Typical capacitance vs. Source-Drain Voltage

Fig.8 Switching Characteristics

Fig.9 Dynamic Input Characteristics

10 15 20 25 Total Gate Charge : Qg [nC]

4 3

2

0

0

10

#### Measurement circuits

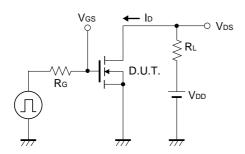


Fig.10 Switching Time Test Circuit

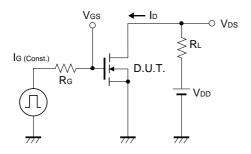


Fig.12 Gate Charge Test Circuit

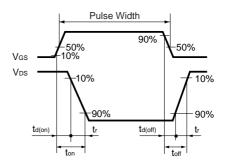


Fig.11 Switching Time Waveforms

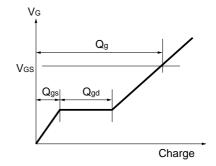


Fig.13 Gate Charge Waveform

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