### 2.5V Drive Pch MOS FET

## RTE002P02

## - Structure

Silicon P-channel MOS FET

## -Features

1) Low On-resistance.
2) Small package (EMT3).
3) 2.5 V drive .

## -Applications

Switching

## -Package specifications

| Type | Package | Taping |
| :--- | :--- | :---: |
|  | Code | TL |
|  | Basic ordering unit (pieces) | 3000 |
| RTE002P02 |  | $\bigcirc$ |

- External dimensions (Unit : mm)



## - Inner circuit



- Absolute maximum ratings ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| Parameter |  | Symbol | Limits | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Drain-source voltage |  | V ${ }_{\text {dss }}$ | -20 | V |
| Gate-source voltage |  | VGss | $\pm 12$ | V |
| Drain current | Continuous | ID | $\pm 0.2$ | A |
|  | Pulsed | ldP *1 | $\pm 0.4$ | A |
| Total power dissipation |  | PD *2 | 0.15 | W |
| Channel temperature |  | Tch | 150 | ${ }^{\circ} \mathrm{C}$ |
| Range of storage temperature |  | Tstg | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

*1 Pw $\leq 10 \mu \mathrm{~s}$, Duty cycle $\leq 1 \%$
*2 Each terminal mounted on a recommended land

## -Thermal resistance

| Parameter | Symbol | Limits | Unit |
| :---: | :---: | :---: | :---: |
| Channel to ambient | Rth(ch-a)* | 833 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

[^0]
## - Electrical characteristics $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Gate-source leakage | Igss | - | - | $\pm 10$ | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{GS}}= \pm 12 \mathrm{~V}, \mathrm{~V}_{\text {dS }}=0 \mathrm{~V}$ |
| Drain-source breakdown voltage | $\mathrm{V}_{\text {(BR) }} \mathrm{DSS}$ | -20 | - | - | V | $\mathrm{I}_{\mathrm{D}}=-1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |
| Zero gate voltage drain current | Idss | - | - | -1 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{DS}}=-20 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |
| Gate threshold voltage | $\mathrm{VGS}_{\text {( }}$ (h) | -0.7 | - | -2.0 | V | $V_{D S}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-1 \mathrm{~mA}$ |
| Static drain-source on-state resistance | Rds (on)* | - | 1.0 | 1.5 | $\Omega$ | $\mathrm{ID}=-0.2 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=-4.5 \mathrm{~V}$ |
|  |  | - | 1.1 | 1.6 | $\Omega$ | $\mathrm{ID}=-0.2 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=-4 \mathrm{~V}$ |
|  |  | - | 2.0 | 3.0 | $\Omega$ | $\mathrm{ID}=-0.15 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=-2.5 \mathrm{~V}$ |
| Forward transfer admittance | $\mid \mathrm{Y}_{\text {fs }}$ \| $^{*}$ | 0.2 | - | - | S | $\mathrm{V} \mathrm{DS}=-10 \mathrm{~V}, \mathrm{ld}=-0.15 \mathrm{~A}$ |
| Input capacitance | Ciss | - | 50 | - | pF | $\begin{aligned} & \mathrm{V}_{\mathrm{DS}}=-10 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V} \\ & \mathrm{f}=1 \mathrm{MHz} \end{aligned}$ |
| Output capacitance | Coss | - | 5 | - | pF |  |
| Reverse transfer capacitance | Crss | - | 5 | - | pF |  |
| Turn-on delay time | $\mathrm{td}_{\text {( }}^{\text {on) }}$ * | - | 9 | - | ns | $\begin{aligned} & \mathrm{VDD} \fallingdotseq-15 \mathrm{~V} \\ & \mathrm{ld}=-0.15 \mathrm{~A} \\ & \mathrm{VGS}=-4.5 \mathrm{~V} \\ & \mathrm{RL}=100 \Omega \\ & \mathrm{RG}=10 \Omega \\ & \hline \end{aligned}$ |
| Rise time | tr * | - | 6 | - | ns |  |
| Turn-off delay time | td (off) * | - | 35 | - | ns |  |
| Fall time | $\mathrm{tf}_{f}$ * | - | 45 | - | ns |  |

-Body diode characteristics (Source-drain) ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Forward voltage | Vsd | - | - | -1.2 | V | $\mathrm{Is}=-0.1 \mathrm{~A}, \mathrm{~V} \mathrm{~V}=0 \mathrm{~V}$ |

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[^0]:    * Each terminal mounted on a recommended land

