2.5V Drive Pch MOSFET RTL030P02

Structure

Silicon P-channel MOSFET

Features

- 1) Low on-resistance. (90mΩ at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

Applications

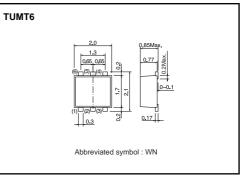
DC-DC converter

Packaging specifications

	Package	Taping		
Туре	Code	TR		
	Basic ordering unit (pieces)	3000		
RTL030P02	0			

•Dimensions (Unit : mm)

Equivalent circuit



(6) (5) (4) *2 *2 *1 (1) Drain (2) Drain (3) Gate *1 ESD PROTECTION DIODE *2 BODY DIODE (6) Drain (6) Drain (6) Drain (7) Drain (7) Drain (7) Drain (8) Drain (6) Drain (6) Drain (7) Drain (7) Drain (7) Drain (8) Drain (8) Drain (9) Drain

●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol		Limits	Unit
Drain-source voltage		VDSS		-20	V
Gate-source voltage		V _{GSS}		±12	V
Droin ourrent	Continuous	ID		±3	А
Drain current	Pulsed	I _{DP}	*1	±12	А
Source current	Continuous	ls		-0.8	А
(Body diode)	Pulsed	Isp	*1	-12	А
Total power dissipation	PD	*2	1	W	
Channel temperature	Tch		150	°C	
Range of Storage temperature		Tstg		-55 to +150	°C
A Decision - Decisional a static					

*1 Pw≤10µs, Duty cycle≤1% *2 Mounted on a ceramic board

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	125	°C / W
* Mounted on a ceramic board.			

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•Electrical characteristics (Ta=25°C)

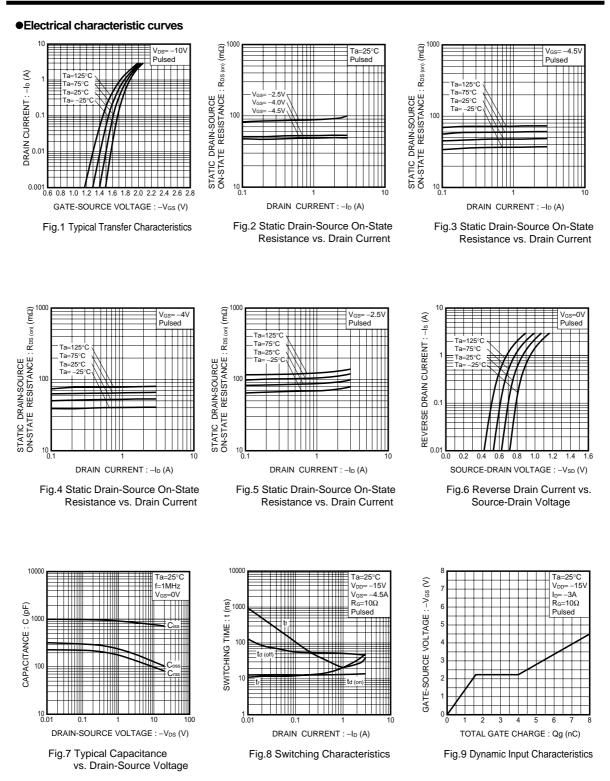
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	lgss	-	-	±10	μA	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V(BR) DSS	-20	-	-	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	-	-1	μΑ	V_{DS} = -20V, V_{GS} =0V
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	$V_{DS} = -10V, I_{D} = -1mA$
		-	50	70	mΩ	I _D = -3.0A, V _{GS} = -4.5V
Static drain-source on-state	RDS (on)	-	55	77	mΩ	I _D = -3.0A, V _{GS} = -4V
resistance		-	90	125	mΩ	I _D = -1.5A, V _{GS} = -2.5V
Forward transfer admittance	Y _{fs} *	2.0	_	-	S	V _{DS} = -10V, I _D = -1.5A
Input capacitance	Ciss	-	760	_	pF	V _{DS} =-10V
Output capacitance	Coss	-	125	-	рF	V _G s=0V
Reverse transfer capacitance	Crss	-	100	-	рF	f=1MHz
Turn-on delay time	t _{d (on)} *	-	12	-	ns	ID= -1.5A
Rise time	tr *	-	25	-	ns	$V_{DD} = -15V$
Turn-off delay time	td (off) *	-	50	-	ns	VGs= –4.5V R∟=10Ω
Fall time	t _f *	-	22	-	ns	Rg=10Ω
Total gate charge	Qg *	-	8.0	-	nC	V _{DD} ≒−15V RL=5Ω
Gate-source charge	Q _{gs} *	-	1.5	-	nC	V _{GS} =-4.5V R _G =10Ω
Gate-drain charge	Q _{gd} *	_	2.5	_	nC	I _D =-3A

•Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd	-	-	-1.2	V	Is= -0.8A, V _{GS} =0V

RTL030P02

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Transistors

Measurement circuits

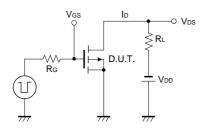


Fig.10 Switching Time Measurement Circuit

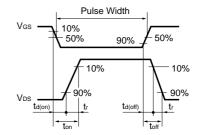


Fig.11 Switching Waveforms

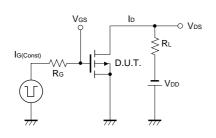


Fig.12 Gate Charge Measurement Circuit

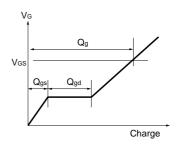


Fig.13 Gate Charge Waveforms

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