DC-DC Converter (-20V, -4.0A)

RTQ040P02

Features

- 1) Low on-resistance. (110m Ω at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

Applications

DC-DC converter

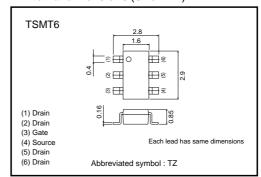
●Structure

Silicon P-channel MOS FET

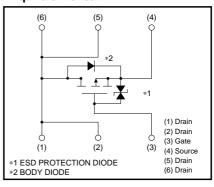
Packaging specifications

	Package	Taping		
Type	Code	TR		
	Basic ordering unit (pieces)	3000		
RTQ040P02	0			

●External dimensions (Unit : mm)



●Equivalent circuit



●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	-20	V	
Gate-source voltage		V_{GSS}	±12	V	
Drain current	Continuous	I_D	±4.0	Α	
	Pulsed	I_{DP}	±16	A *1	
Source current	Continuous	Is	-1	A *1	
(Body diode)	Pulsed	I _{SP}	-16	Α	
Total power dissipation		PD	1.25	W *2	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	_	±10	μΑ	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V _(BR) DSS	-20	_	_	٧	I _D = -1mA, V _G S=0V
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	V _{DS} = -20V, V _{GS} =0V
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	V _{DS} = -10V, I _D = -1mA
Static drain-source on-state resistance		_	35	50	mΩ	I _D = -4A, V _G S= -4.5V *
	R _{DS} (on)	_	40	55	mΩ	I _D = -4A, V _G S= -4V *
		_	60	85	mΩ	I _D = -2.0A, V _G S= -2.5V *
Forward transfer admittance	Yfs	3.5	-	_	S	$V_{DS} = -10V, I_{D} = -2.0A$ *
Input capacitance	Ciss	_	1350	_	рF	V _{DS} = -10V
Output capacitance	Coss	_	210	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	150	_	pF	f=1MHz
Turn-on delay time	td (on)	_	15	_	ns	I _D = -2.0A *
Rise time	tr	_	35	_	ns	VDD≒ -15V *
Turn-off delay time	t _{d (off)}	_	60	_	ns	$V_{GS} = -4.5V$ $R_{L} = 7.5\Omega$
Fall time	tf	_	30	_	ns	RGs= 10Ω
Total gate charge	Qg	_	12.2	_	nC	V _{DD} ≒−15V R _L ≒3.75Ω
Gate-source charge	Qgs	_	2.6	-	nC	V _{GS} = -4.5V R _{GS} =10Ω
Gate-drain charge	Q _{gd}	_	3.4	_	nC	I _D = -4.0A

Body diode characteristics (source-drain characteristics)

Forward voltage	VSD	_	_	-1.2	V	I _S = -1A, V _{GS} =0V

•Electrical characteristic curves

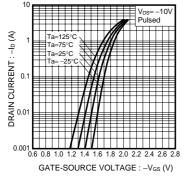


Fig.1 Typical Transfer Characteristics

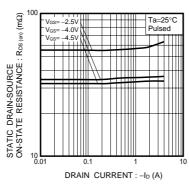


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

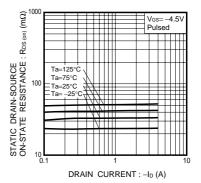


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

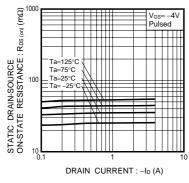


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

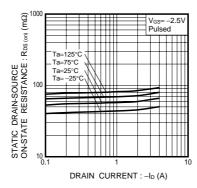


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

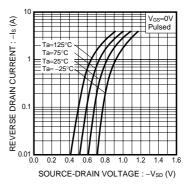


Fig.6 Reverse Drain Current vs. Source-Drain Voltage

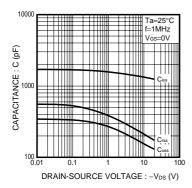


Fig.7 Typical Capacitance vs. Drain-Source Voltage

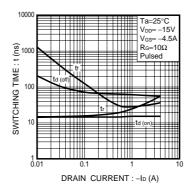


Fig.8 Switching Characteristics

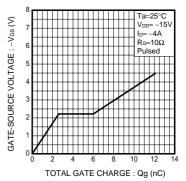


Fig.9 Dynamic Input Characteristics

●Measurement circuits

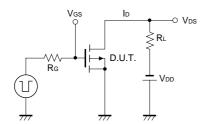


Fig.10 Switching Time Measurement Circuit

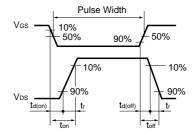


Fig.11 Switching Waveforms

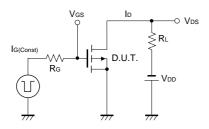


Fig.12 Gate Charge Measurement Circuit

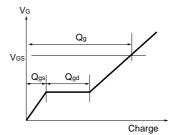


Fig.13 Gate Charge Waveforms

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