2.5V Drive Pch MOS FET

RTR020P02

Structure

Silicon P-channel MOS FET

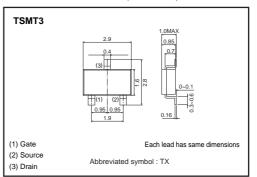
Features

- 1) Low On-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).

Application

Power switching, DC / DC converter.

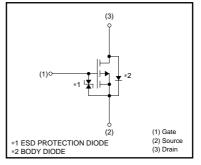
•External dimensions (Unit : mm)



Packaging specifications

	Package	Taping		
Туре	Code	TL		
	Basic ordering unit (pieces)	3000		
RTR020P02		0		

Equivalent circuit



●Absolute maximum ratings (Ta=25°C)

	•	,			
Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	-20	V	
Gate-source voltage		Vgss	±12	V	
Drain autrent	Continuous	lь	±2.0	А	
Drain current	Pulsed	I _{DP} *1	±8.0	А	
Source current (Body diode)	Continuous	ls	-0.8	А	
	Pulsed	I _{SP} *1	-3.2	А	
Total power dissipation		P _D *2	1.0	W	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55 to +150	°C	
A Division of Distance is static					

*1 Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a) *	125	°C / W
· Manuala data a successful sound			

* Mounted on a ceramic board.



Transistors

•Electrical characteristics (Ta=25°C)

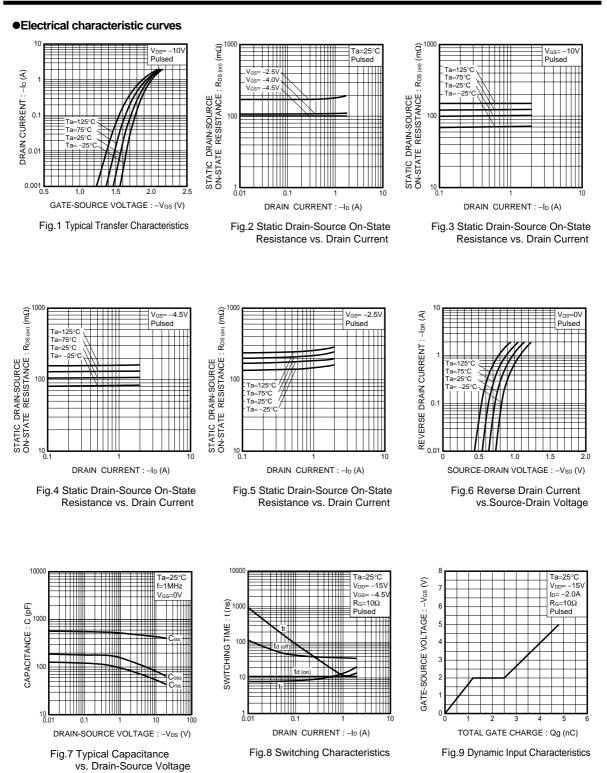
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	lgss	-	-	±10	μA	Vgs=±12V, Vds=0V	
Drain-source breakdown voltage	V(BR) DSS	-20	-	-	V	I _D = -1mA, V _{GS} =0V	
Zero gate voltage drain current	IDSS	-	-	-1	μΑ	V _{DS} = -20V, V _{GS} =0V	
Gate threshold voltage	VGS (th)	-0.7	-	-2.0	V	V _{DS} = -10V, I _D = -1mA	
		-	100	135	mΩ	I _D = -2.0A, V _{GS} = -4.5V	
Static drain-source on-state resistance	$R_{DS(on)}^*$	-	110	150	mΩ	I _D = -2.0A, V _{GS} = -4.0V	
resistance		-	180	250	mΩ	I _D = -1.0A, V _{GS} = -2.5V	
Forward transfer admittance	Y _{fs} *	1.2	-	-	S	V_{DS} = -10V, I_{D} = -1.0A	
Input capacitance	Ciss	-	430	-	pF	V _{DS} =-10V	
Output capacitance	Coss	-	80	-	pF	Vgs=0V	
Reverse transfer capacitance	Crss	-	55	_	pF	f=1MHz	
Turn-on delay time	td (on) *	-	11	_	ns	ID=-1.0A	
Rise time	tr *	-	13	_	ns	VDD≒ -15V VGs= -4.5V R∟=15Ω RG=10Ω	
Turn-off delay time	td (off) *	-	38	_	ns		
Fall time	t _f *	-	12	_	ns		
Total gate charge	Qg	-	4.9	-	nC	V _{DD} ≒−15V	
Gate-source charge	Qgs	-	1.2	-	nC	V _{GS} =-4.5V	
Gate-drain charge	Q _{gd}	-	1.3	-	nC	ID=-2.0A	

•Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd	-	_	-1.2	V	Is= -0.8A, Vgs=0V

RTR020P02

Transistors



3/4

Transistors

Measurement circuits

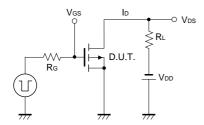


Fig.10 Switching Time Test Circuit

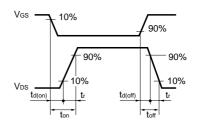


Fig.11 Switching Time Waveforms

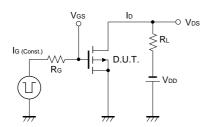


Fig.12 Gate Charge Test Circuit

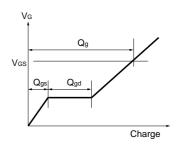


Fig.13 Gate Charge Waveform

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