## Pch -20V -200mA Small Signal MOSFET

$V_{DSS}$	-20V
R <sub>DS(on)</sub> (Max.)	1.2Ω
I <sub>D</sub>	±200mA
$P_D$	150mW

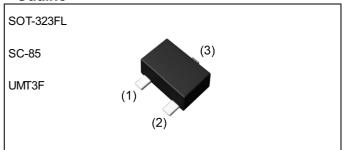
# ●Features

- 1) Low on-resistance
- 2) Low voltage drive(1.2V drive)

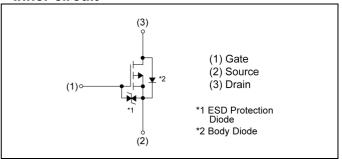
# Application

Switching

## Outline



## •Inner circuit



Packaging specifications

i deliaging opermediation						
	Packing	Embossed Tape				
	Reel size (mm)	180				
Туре	Tape width (mm)	8				
	Basic ordering unit (pcs)	3000				
	Taping code	TCL				
	Marking	YK				

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-20	V
Continuous drain current	I <sub>D</sub>	±200	mA
Pulsed drain current	I <sub>DP</sub> *1	±800	mA
Gate - Source voltage	V <sub>GSS</sub>	±10	V
Power dissipation	P <sub>D</sub> *2	150	mW
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Parameter	Cymphol	Values			Lleit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R <sub>thJA</sub> *2	-	1	833	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol Conditions		Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-20	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-21.9	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V	-	-	-1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 10V$ , $V_{DS} = 0V$	1	-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = -10V, I_{D} = -100\mu A$	-0.3	-	-1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	2.4	-	mV/°C	
		$V_{GS} = -4.5V, I_D = -200mA$	-	0.8	1.2		
		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -100mA	-	1.0	1.5		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *3	$V_{GS} = -1.8V, I_D = -100mA$	1	1.3	2.2	Ω	
		$V_{GS} = -1.5V, I_D = -40mA$	ı	1.6	3.5		
		$V_{GS} = -1.2V, I_D = -10mA$	ı	2.4	9.6		
Forward Transfer Admittance	Y <sub>fs</sub>  *3	V <sub>DS</sub> = -10V, I <sub>D</sub> = -200mA	200	-	-	mS	

<sup>\*1</sup> Pw≦10µs, Duty cycle≦1%

<sup>\*2</sup> Each terminal mounted on a reference land.

<sup>\*3</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumphal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Urill	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	115	-	_	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	10	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	6	-		
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} \simeq -10V, V_{GS} = -4.5V$	-	6	-		
Rise time	t <sub>r</sub> *3	I <sub>D</sub> = -100mA	-	4	-	no	
Turn - off delay time	t <sub>d(off)</sub> *3	$R_L \simeq 100\Omega$	-	17	-	ns	
Fall time	t <sub>f</sub> *3	$R_G = 10\Omega$	-	17	-		

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol Condition	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*3}$	V <sub>DD</sub> ≃ -10V,	-	1.4	-	
Gate - Source charge	$Q_{gs}^{*3}$	$V_{DD} \simeq -10V$ , $I_D = -200$ mA,	-	0.3	-	nC
Gate - Drain charge	Q <sub>gd</sub> *3	V <sub>GS</sub> = -4.5V	-	0.3	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol	Conditions	Values			l lait	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Continuous forward current	Is	T - 25°C	-	-	-100	mA	
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25℃	-	-	-800	mA	
Forward voltage	V <sub>SD</sub> *3	$V_{GS} = 0V, I_{S} = -200 \text{mA}$	-	-	-1.2	V	

Fig.1 Power Dissipation Derating Curve

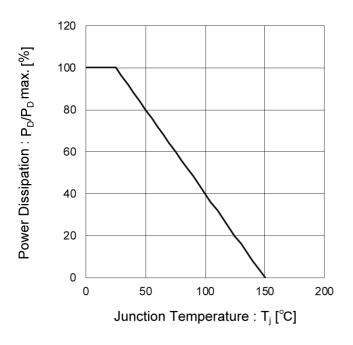
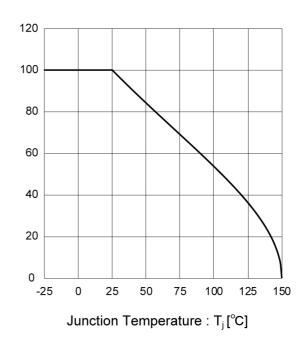


Fig.2 Drain Current Derating Curve



Drain Current Dissipation: I<sub>D</sub>/I<sub>D</sub> max. [%]

Fig.3 Typical Output Characteristics(I)

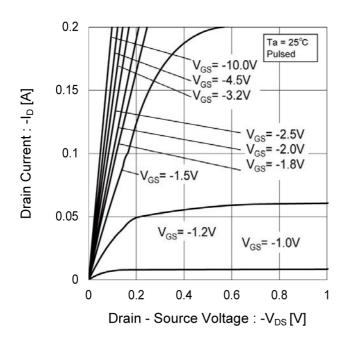


Fig.4 Typical Output Characteristics(II)

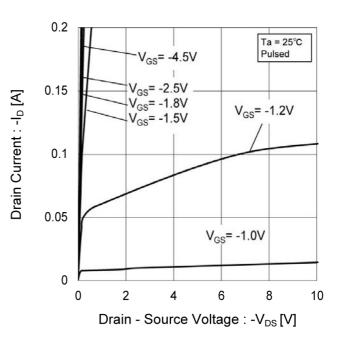


Fig.5 Breakdown Voltage vs.

Junction Temperature

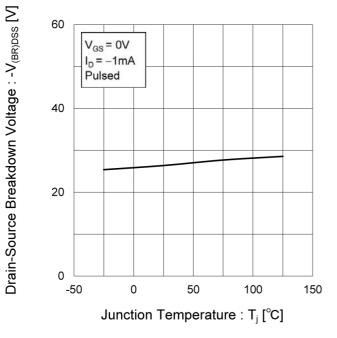


Fig.6 Typical Transfer Characteristics

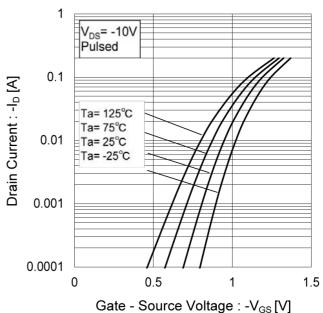


Fig.7 Gate Threshold Voltage vs.
Junction Temperature

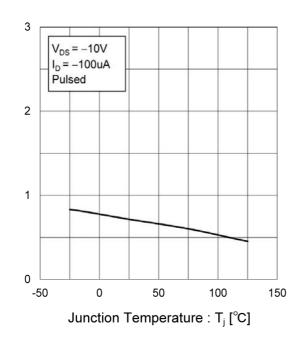
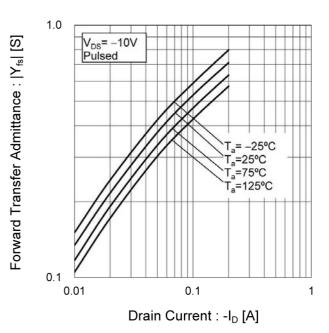


Fig.8 Forward Transfer Admittance vs.
Drain Current



Gate Threshold Voltage: -V<sub>GS(th)</sub> [V]

Fig.9 Static Drain - Source On - State
Resistance vs. Gate Source Voltage

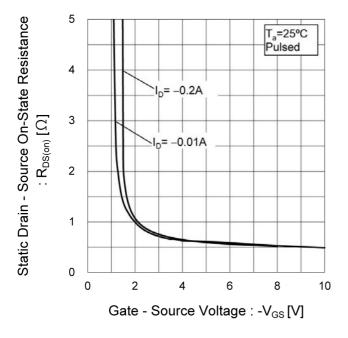


Fig.10 Static Drain - Source On - State Resistance vs. Junction Temperature

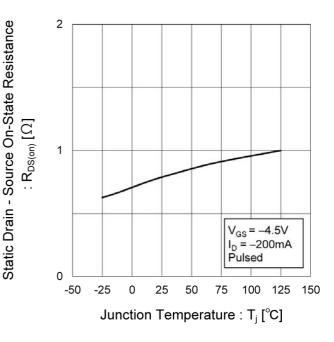


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current (I)

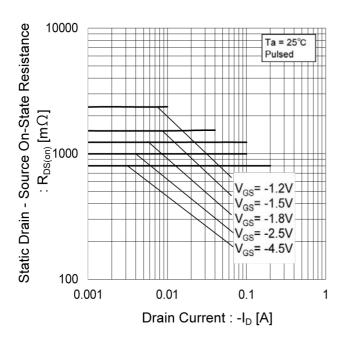


Fig.12 Static Drain - Source On - State Resistance vs. Drain Current (II)

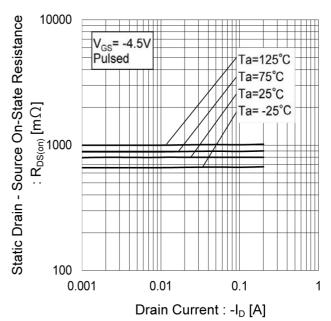


Fig.13 Static Drain - Source On - State
Resistance vs. Drain Current (III)

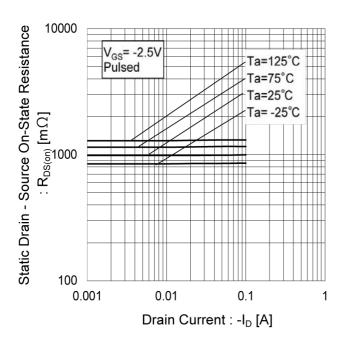


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (IV)

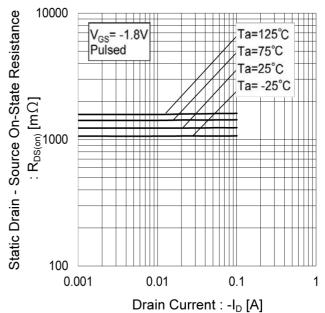


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (V)

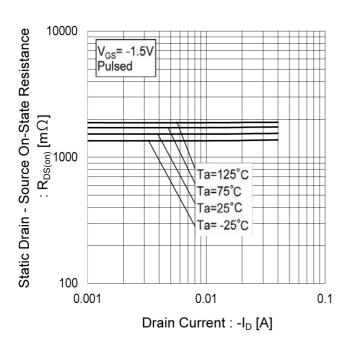


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (VI)

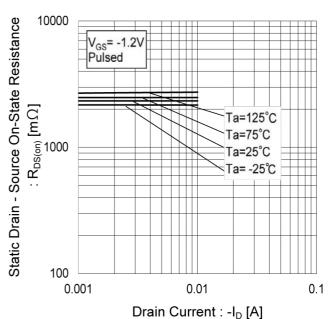


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

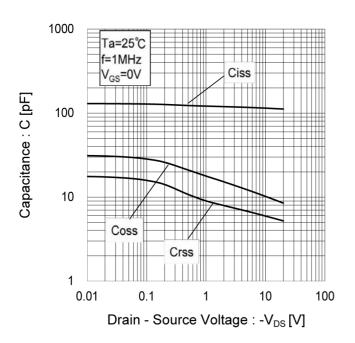


Fig.18 Switching Characteristics

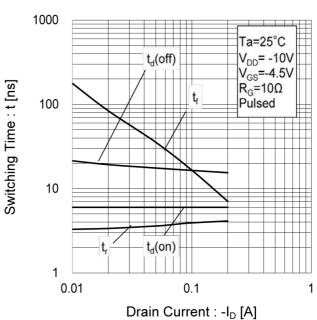
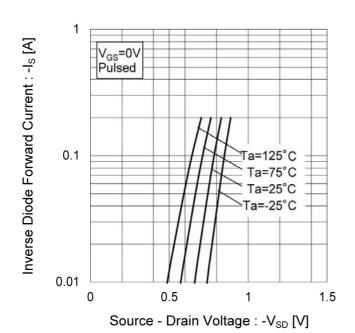


Fig.19 Source Current vs.

Source Drain Voltage



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### Measurement circuits

Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

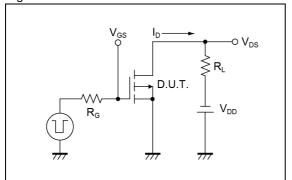


Fig. 2-1 GATE CHARGE MEASUREMENT CIRCUIT

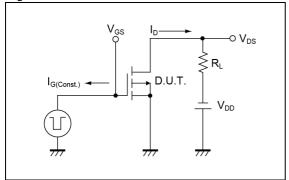


Fig. 1-2 SWITCHING WAVEFORMS

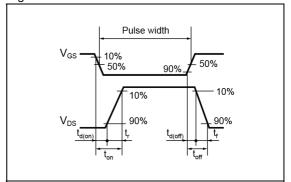
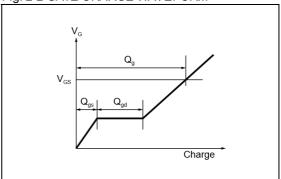


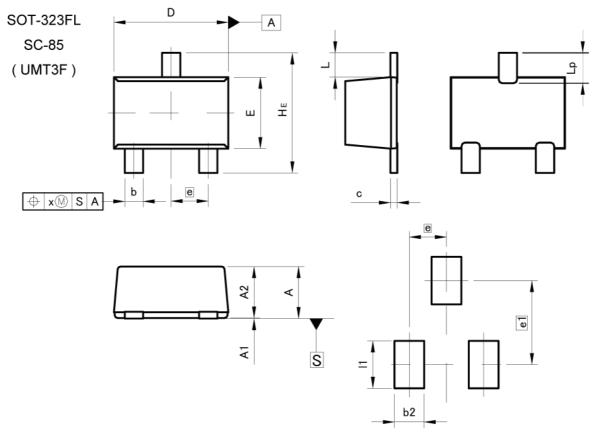
Fig. 2-2 GATE CHARGE WAVEFORM



### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	0.85	1.05	0.033	0.041	
A1	0.00	0.10	0.000	0.004	
A2	0.80	1.00	0.031	0.039	
b	0.27	0.42	0.011	0.017	
С	0.08	0.18	0.003	0.007	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.0	65	0.026		
HE	2.00	2.20	0.079	0.087	
L	0.425		0.425	0.0	17
Lp	0.43	0.63	0.017	0.025	
х	=:	0.10	-	0.004	

DIM	MILIME	TERS	INCH	HES
DIM	MIN	MAX	MIN	MAX
b2	<del>-</del> s	0.52	-	0.020
e1	1.47		0.0	58
11	<del></del> k	0.83	-	0.033

Dimension in mm/inches



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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSIII
CLASSIV	CLASSIII	CLASSIII	CLASSIII

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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### **Precaution for Mounting / Circuit board design**

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

#### **Precaution for Product Label**

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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When disposing Products please dispose them properly using an authorized industry waste company.

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