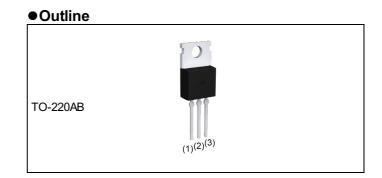


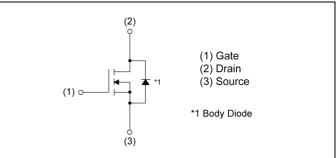
# RX3L07BBG

Nch 60V 105A Power MOSFET

V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	4.6mΩ
Ι <sub>D</sub>	±105A
P <sub>D</sub>	89W



## Inner circuit



## Packaging specifications

	Packing	Tube
T	Quantity (pcs)	1000
Туре	Taping code	C16
	Marking	RX3L07BBG

## • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit
Drain - Source voltage		V <sub>DSS</sub>	60	V
O attinue desire accordent	Silicon limit (V <sub>GS</sub> =10V)	۱ <sub>D</sub> *1	±105	Α
Continuous drain current	T <sub>c</sub> = 25°C (V <sub>GS</sub> =10V)	I <sub>D</sub> *2	±70	Α
Pulsed drain current		ا <sub>DP</sub> *3	±420	Α
Gate - Source voltage		V <sub>GSS</sub>	±20	V
Avalanche current, single pulse		I <sub>AS</sub> *4	33	Α
Avalanche energy, single p	ulse	$E_{AS}^{*4}$	85	mJ
Power dissipation		P <sub>D</sub> *2	89	W
Junction temperature		Tj	150	°C
Operating junction and storage temperature range		T <sub>stg</sub>	-55 to +150	°C

•Features

- 1) Low on resistance.
- 2) High power package (TO-220AB)
- 3) Pb-free lead plating ; RoHS compliant
- 4) Halogen free

Application

Switching

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## •Thermal resistance

Parameter	Symbol	Values			Unit
Falameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}^{*2}$	-	-	1.40	°C/W

## •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Currence of	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	UTIIL	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{i}} I_{D} = 1 mA$ referenced to 25°C		-	38.9	-	mV/°C	
Zero gate voltage drain current	$I_{DSS}$ $V_{DS}$ = 60V, $V_{GS}$ = 0V		-	-	2	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$I_{GSS}$ $V_{GS} = \pm 20V, V_{DS} = 0V$		-	±200	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{GS(th)}$ $V_{DS} = V_{GS}, I_D = 1mA$		-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$			-4.7	-	mV/°C	
Static drain - source	D *5	V <sub>GS</sub> = 10V, I <sub>D</sub> = 70A	-	3.5	4.6		
on - state resistance	${\sf R}_{\sf DS(on)}^{*5}$	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 35A	-	4.7	6.5	mΩ	
Gate resistance	R <sub>G</sub>	R <sub>G</sub> -		0.9	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  * <sup>5</sup>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 35A	49	-	-	S	

\*1 Limited by silicon chip capability.

\*2 T<sub>c</sub>=25°C, Limited only by maximum temperature allowed.

\*3 Pw  $\leq$  10µs , Duty cycle  $\leq$  1%

\*4 L  $\simeq$  0.1mH, V<sub>DD</sub> = 30V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25°C Fig.3-1,3-2

\*5 Pulsed





## • Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Cumphal	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2950	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30V	-	750	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	55	-		
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 30V, V_{GS}$ = 10V	-	21	-		
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 35A	-	12	-	-	
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L \simeq 0.85\Omega$	-	72	-	ns	
Fall time	t <sub>f</sub> *5	R <sub>G</sub> = 10Ω	-	27	-		

## • Gate charge characteristics (T<sub>a</sub> = 25°C)

Deremeter	Currence of	Symbol Conditions		Values			L lucit	
Parameter	Зупрог			Min.	Тур.	Max.	Unit	
Tatal water all sums	O *5		V <sub>GS</sub> = 10V	-	47.0	-		
Total gate charge	$Q_g^{*5}$		$V_{DD} \simeq 30V$		-	23.0	-	nC
Gate - Source charge	$Q_{gs}^{*5}$	I <sub>D</sub> = 50A	V <sub>GS</sub> = 4.5V	-	8.8	-	nc	
Gate - Drain charge	Q <sub>gd</sub> *5			-	7.9	-		

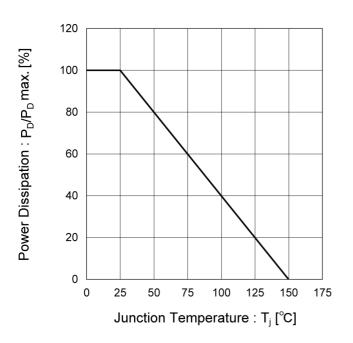
## •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Deremeter	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ <sub>s</sub>	T <sub>a</sub> = 25℃	-	-	70	А
Pulse forward current	$I_{SP}^{*3}$	$T_a = 25 C$	-	-	420	А
Forward voltage	$V_{SD}^{*5}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 70A	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub> *5	I <sub>S</sub> = 50A, V <sub>GS</sub> =0V	-	49	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 100A/µs	-	64	-	nC



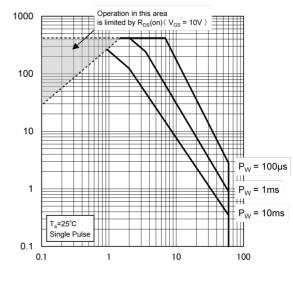
3/10

## • Electrical characteristic curves



## Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage :  $V_{DS}$  [V]

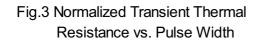
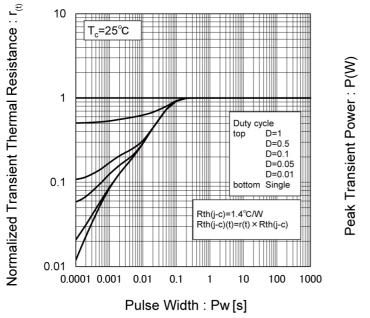
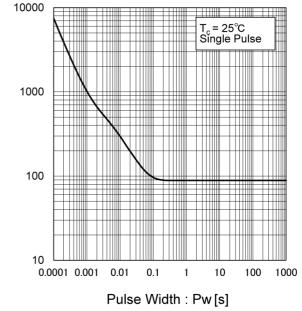


Fig.4 Single Pulse Maximum Power Dissipation





70

#### • Electrical characteristic curves



#### Fig.5 Typical Output Characteristics(I)

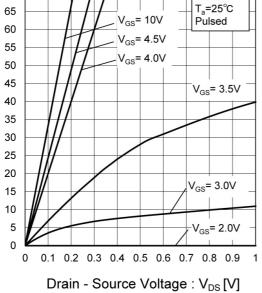


Fig.7 Breakdown Voltage vs.

Junction Temperature

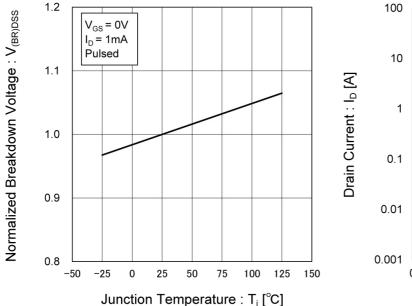
65 T<sub>2</sub>=25°C <sub>GS</sub>= 10V Pulsed 60 V<sub>GS</sub>= 4.5V 55 V<sub>GS</sub>= 4.0V 50 45 V<sub>GS</sub>= 3.0V 40 V<sub>GS</sub>= 3.5V 35 30 25 20 15 10 V<sub>GS</sub>= 2.0V 5 0 3 5 0 2 4 6 10 1 7 8 9 Drain - Source Voltage : V<sub>DS</sub> [V]

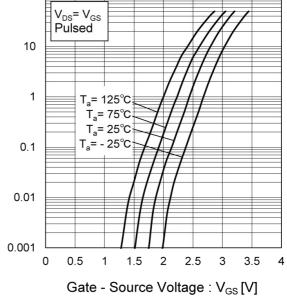
## Fig.6 Typical Output Characteristics(II)

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Drain Current : I<sub>D</sub> [A]

Fig.8 Typical Transfer Characteristics

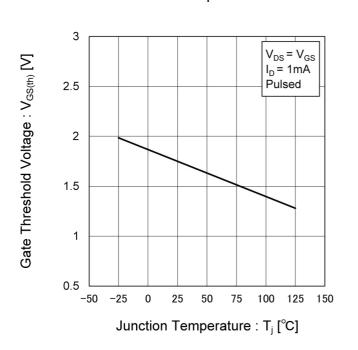




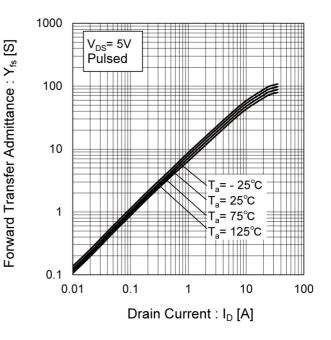
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## • Electrical characteristic curves

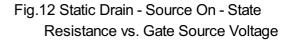


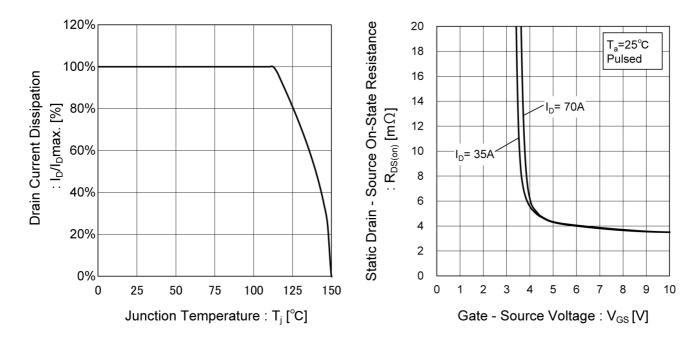
#### Fig.9 Gate Threshold Voltage vs. Junction Temperature



## Fig.10 Forward Transfer Admittance vs. Drain Current

Fig.11 Drain Current Derating Curve





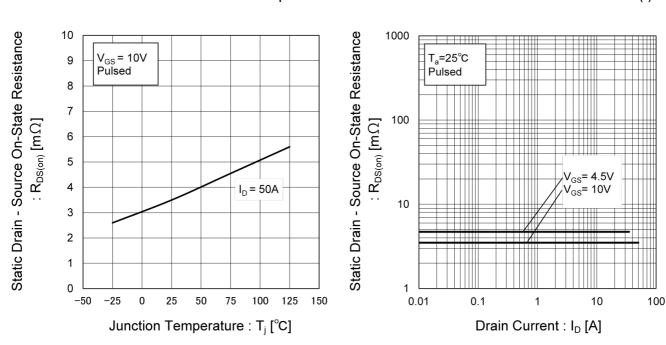


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

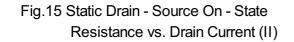
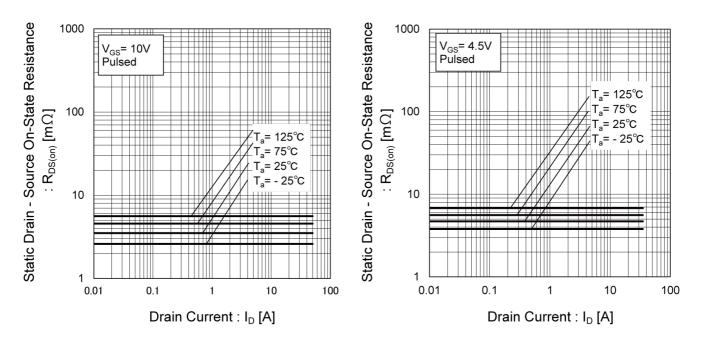
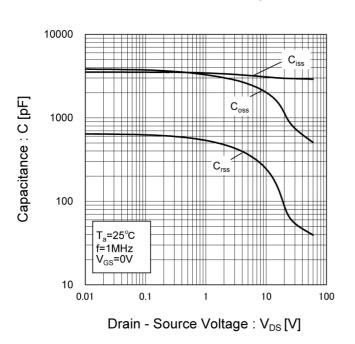


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



## • Electrical characteristic curves



## Fig.17 Typical Capacitances vs. Drain - Source Voltage

Fig.18 Switching Characteristics

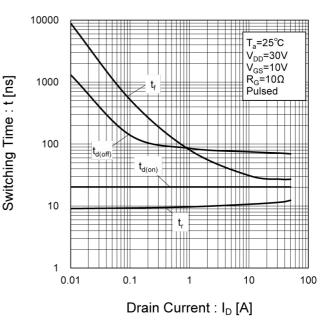


Fig.19 Typical Gate Charge

Gate - Source Voltage : V<sub>GS</sub> [V]

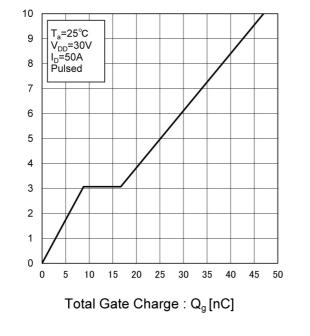
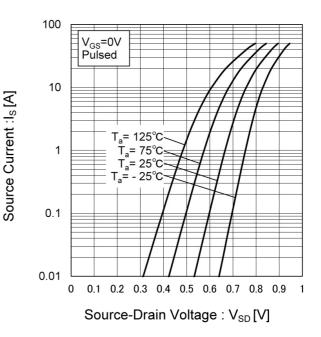
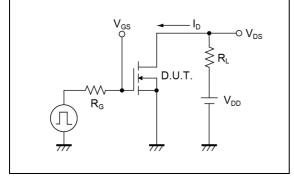


Fig.20 Source Current vs. Source Drain Voltage

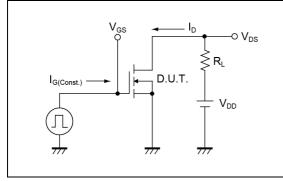


#### Measurement circuits

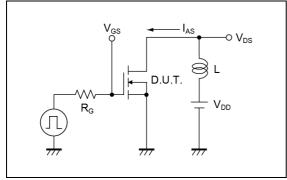
Fig.1-1 Switching Time Measurement Circuit



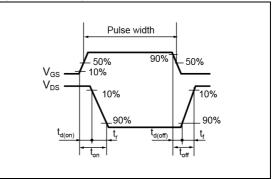
#### Fig.2-1 Gate Charge Measurement Circuit



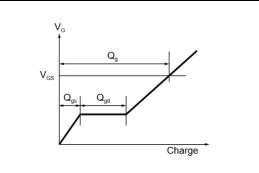
#### Fig.3-1 Avalanche Measurement Circuit



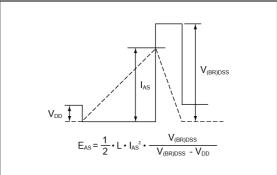
#### Fig.1-2 Switching Waveforms



#### Fig.2-2 Gate Charge Waveform

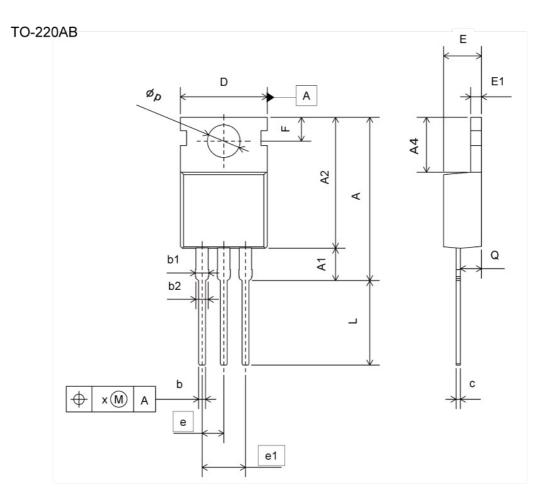


#### Fig.3-2 Avalanche Waveform



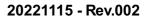


### Dimensions



	MILIME	TERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
A	18.30	20.00	0.720	0.787	
A1	3.60	4.00	0.142	0.157	
A2	14.70	16.00	0.579	0.630	
A4	6.30	6.60	0.248	0.260	
b	0.65	0.95	0.026	0.037	
b1	1.20	1.75	0.047	0.069	
b2	1.20	1.70	0.047	0.067	
с	0.35	0.65	0.014	0.026	
D	9.96	10.36	0.392	0.408	
E	4.24	4.64	0.167	0.183	
E1	1.14	1.40	0.045	0.055	
е	2.	2.54		00	
e1	5.	08	0.200		
F	2.60	3.00	0.102	0.118	
L	9.47	10.37	0.373	0.408	
φp	3.69	3.99	0.145	0.157	
Q	2.30	2.70	0.091	0.106	
х	-	0.38	-	0.015	

Dimension in mm/inches



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---

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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