

RX3L18BBG

Nch 60V 240A Power MOSFET

V _{DSS}	60V
R _{DS(on)} (Max.)	1.84mΩ
I _D	±240A
P_D	192W

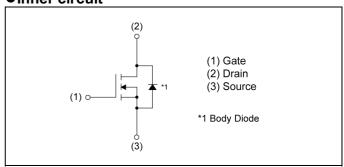
Outline



Features

- 1) Low on resistance.
- 2) High power package (TO-220AB)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen free

•Inner circuit



Application

Switching

Packaging specifications

	Packing	Tube
T. //p.o	Quantity (pcs)	1000
Type	Taping code	C16
	Marking	RX3L18BBG

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parar	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	60	V	
Continuous drain current	Silicon limit (V _{GS} =10V)	I _D *1	±240	Α
Continuous drain current	$T_c = 25^{\circ}C (V_{GS} = 10V)$	I _D *2	±180	Α
Pulsed drain current	I _{DP} *3	±900	Α	
Gate - Source voltage	V _{GSS}	±20	V	
Avalanche current, single pu	I _{AS} *4	70	Α	
Avalanche energy, single pu	E _{AS} *4	380	mJ	
Power dissipation	P _D *2	192	W	
Junction temperature	Tj	150	°C	
Operating junction and stora	T _{stg}	-55 to +150	°C	

●Thermal resistance

Parameter	Symbol	Values			Llmit
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *2	-	-	0.65	°C/W

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymah ol	Conditions	Values			Lleit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	38.9	-	mV/°C	
Zero gate voltage drain current	I_{DSS} $V_{DS} = 60V, V_{GS} = 0V$		-	-	5	μA	
Gate - Source leakage current	I _{GSS}	I_{GSS} $V_{GS} = \pm 20V$, $V_{DS} = 0V$		-	±200	nA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-4.7	-	mV/°C	
Static drain - source	D *5	V _{GS} = 10V, I _D = 90A	-	1.41	1.84	O	
on - state resistance	R _{DS(on)} *5	V _{GS} = 4.5V, I _D = 90A	-	1.73	2.43	mΩ	
Gate resistance	R _G -		-	8.0	-	Ω	
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 5V, I _D = 90A	90	-	-	S	

^{*1} Limited by silicon chip capability.

^{*2} T_c =25°C, Limited only by maximum temperature allowed.

^{*3} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*4} L \simeq 0.1mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

Daramatar	Symbol	Conditions		Unit			
Parameter	Symbol Conditions		Min.	Тур.	Max.	Offic	
Input capacitance	C _{iss}	V _{GS} = 0V	-	11000	-		
Output capacitance	C _{oss}	V _{DS} = 30V	-	2540	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	130	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 30V, V_{GS} = 10V$	-	49	-		
Rise time	t r*5	I _D = 50A	1	31	-	20	
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 0.6\Omega$	-	230	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	140	-		

● Gate charge characteristics (T_a = 25°C)

Davanastav	0	Conditions			Values		l lait	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Total gate charge	O *5		V _{GS} = 10V	-	160	-		
Total gate charge	Q_g^{*5}	\mathbf{Q}_{g}	V _{DD} ≃ 30V		-	80	-	»C
Gate - Source charge	Q _{gs} *5	I _D = 50A	V _{GS} = 4.5V	-	30	-	nC	
Gate - Drain charge	Q _{gd} *5			-	23	-		

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Darameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	mbol Conditions		Тур.	Max.	Offic
Continuous forward current	I _S	T = 25°C	-	-	160	Α
Pulse forward current	I _{SP} *3	T _a = 25°C	-	-	900	Α
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 90A	-	-	1.2	V
Reverse recovery time	t _{rr} *5	I _S = 50A, V _{GS} =0V	-	85	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	175	-	nC

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Fig.1 Power Dissipation Derating Curve

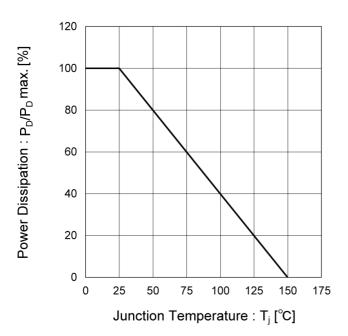
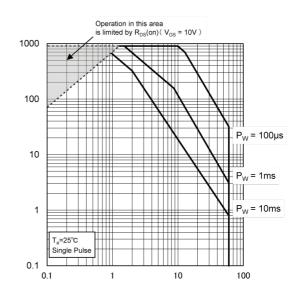


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

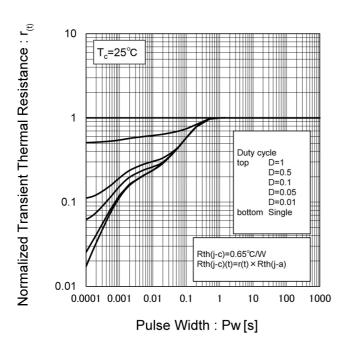


Fig.4 Single Pulse Maximum Power Dissipation

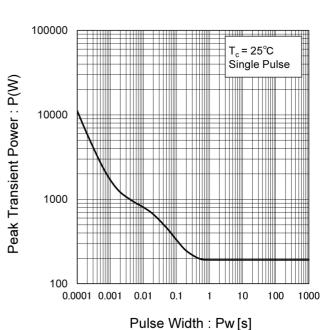


Fig.5 Typical Output Characteristics(I)

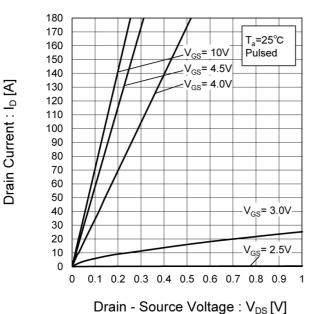
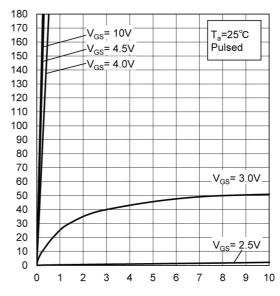


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

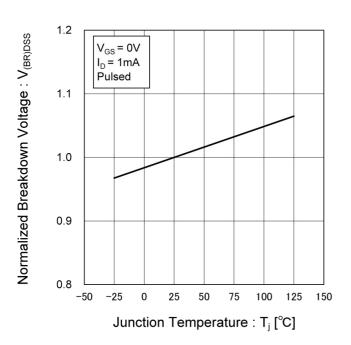


Fig.8 Typical Transfer Characteristics

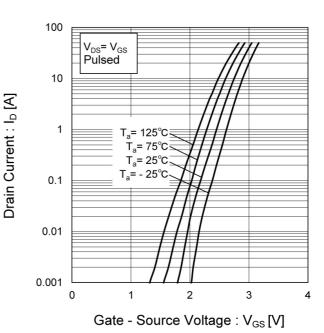


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

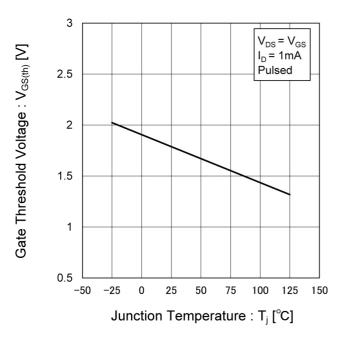


Fig.10 Forward Transfer Admittance vs.
Drain Current

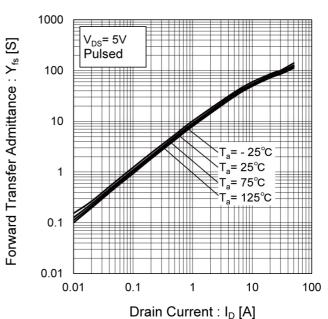


Fig.11 Drain Current Derating Curve

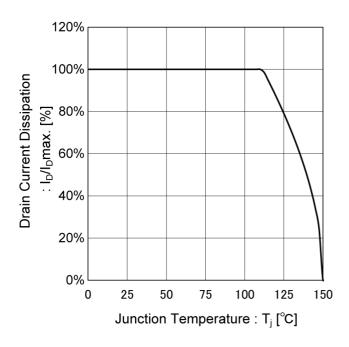


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

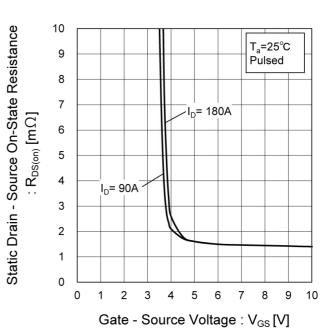


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Static Drain - Source On-State Resistance V_{GS} = 10V Pulsed 2.5 2 $R_{\text{DS(on)}} \, [\text{m}\Omega]$ 1.5 $I_{\rm D} = 50A$ 1 0.5 0 75 -50 -25 0 25 50 100 125 150 Junction Temperature : T_j [$^{\circ}$ C]

Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

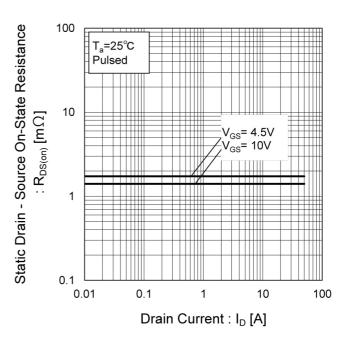


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

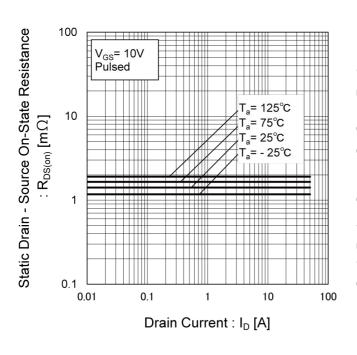


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

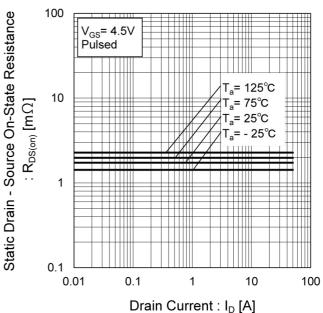
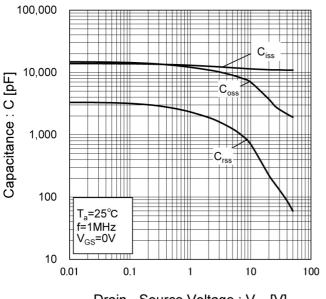


Fig.17 Typical Capacitances vs.

Drain - Source Voltage



Drain - Source Voltage: V_{DS} [V]

Fig.18 Switching Characteristics

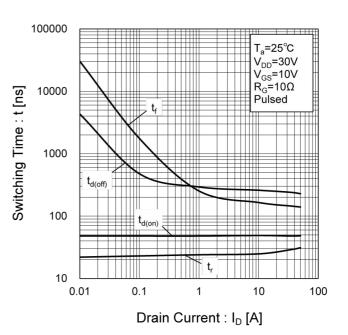


Fig.19 Typical Gate Charge

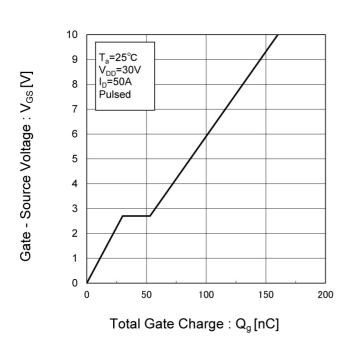
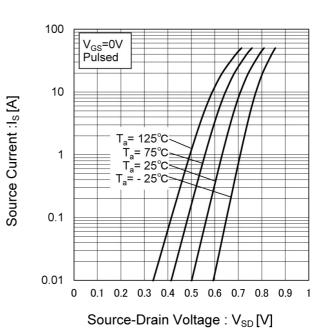


Fig.20 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

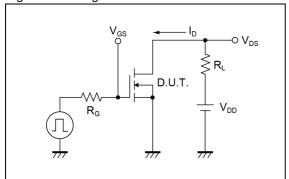


Fig.1-2 Switching Waveforms

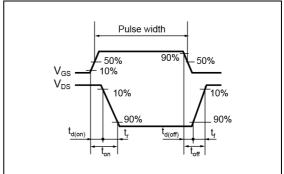


Fig.2-1 Gate Charge Measurement Circuit

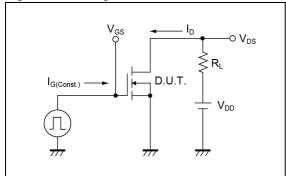


Fig.2-2 Gate Charge Waveform

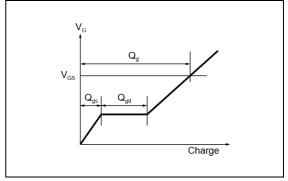


Fig.3-1 Avalanche Measurement Circuit

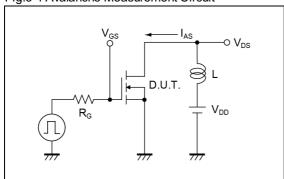
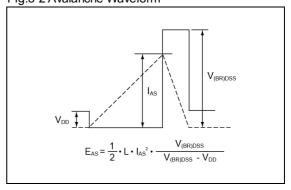
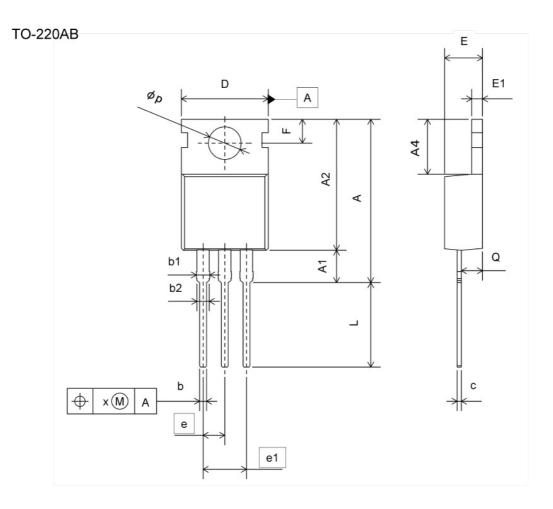


Fig.3-2 Avalanche Waveform



Dimensions



DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	18.30	20.00	0.720	0.787
A1	3.60	4.00	0.142	0.157
A2	14.70	16.00	0.579	0.630
A4	6.30	6.60	0.248	0.260
b	0.65	0.95	0.026	0.037
b1	1.20	1.75	0.047	0.069
b2	1.20	1.70	0.047	0.067
С	0.35	0.65	0.014	0.026
D	9.96	10.36	0.392	0.408
E	4.24	4.64	0.167	0.183
E1	1.14	1.40	0.045	0.055
е	2.	54	0.1	00
e1	5.	08	0.200	
F	2.60	3.00	0.102	0.118
L	9.47	10.37	0.373	0.408
ϕ p	3.69	3.99	0.145	0.157
Q	2.30	2.70	0.091	0.106
х	-0	0.38	_	0.015

Dimension in mm/inches



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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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