

SCT2H12NY

N-channel SiC power MOSFET

V _{DSS}	1700V
R _{DS(on)} (Typ.)	1.15Ω
I _D	4A
P _D	44W

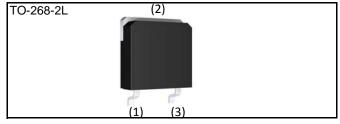
Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Long creepage distance with no center lead
- 4) Simple to drive
- 5) Pb-free lead plating ; RoHS compliant

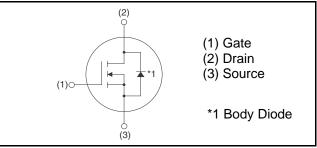
Application

- Auxilialy power supplies
- •Switch mode power supplies

Outline



Inner circuit



Packaging specifications

	Packing	Embossed tape
	Reel size (mm)	330
Tuno	Tape width (mm)	24
Туре	Basic ordering unit (pcs)	400
	Taping code	ТВ
	Marking	SCT2H12NY

●Absolute maximum ratings (T_a = 25°C)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V _{DSS}	1700	V
Continuous drain surrent	$T_c = 25^{\circ}C$	Ι _D ^{*1}	4	А
Continuous drain current	$T_c = 100^{\circ}C$	Ι _D ^{*1}	2.9	А
Pulsed drain current		I _{D,pulse} *2	10	А
Gate - Source voltage (DC)		V _{GSS}	-6 to 22	V
Gate - Source surge voltage (t _{surge} <300nsec)		V_{GSS_surge} *3	-10 to 26	V
Power dissipation $(T_c = 25^{\circ}C)$		P _D	44	W
Junction temperature		Tj	175	°C
Range of storage temperature		T _{stg}	-55 to +175	°C

•Thermal resistance

Parameter	Symbol		Unit		
	Symbol	Min.	Тур.	Max.	Onit
Thermal resistance, junction - case	R_{thJC}	-	2.65	3.45	°C/W

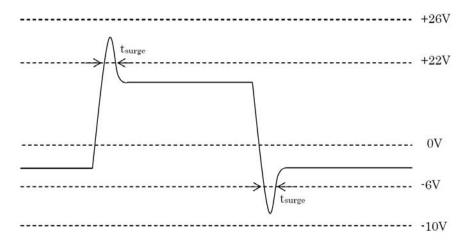
•Electrical characteristics ($T_a = 25^{\circ}C$)

Parameter	Symbol	mbol Conditions -		Values		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 1mA$	1700	-	-	V
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 1700V, V_{GS} = 0V$ $T_j = 25^{\circ}C$ $T_j = 150^{\circ}C$	-	0.1 0.2	10 -	μΑ
Gate - Source leakage current	I_{GSS^+}	$V_{GS} = +22V, V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -6V, V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	$V_{GS (th)}$	$V_{DS} = V_{GS}, I_{D} = 0.41 \text{mA}$	1.6	2.8	4.0	V

*1 Limited only by maximum temperature allowed.

*2 PW \leq 10 $\mu s,$ Duty cycle \leq 1%

*3 Example of acceptable Vgs waveform



*4 Pulsed

•Electrical characteristics ($T_a = 25^{\circ}C$)

Deremeter	Symbol	ool Conditions		Values			
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
		$V_{GS} = 18V, I_{D} = 1.1A$					
Static drain - source on - state resistance	R _{DS(on)} *4	$T_j = 25^{\circ}C$	-	1.15	1.5	Ω	
		T _j = 125°C	-	1.71	-		
Gate input resistance	R _G	f = 1MHz, open drain	-	64	-	Ω	
Transconductance	g _{fs} *4	$V_{DS} = 10V, I_D = 1.1A$	-	0.4	-	S	
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	184	-		
Output capacitance	C _{oss}	V _{DS} = 800V	-	16	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	6	-		
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V V _{DS} = 0V to 800V	-	17	-	pF	
Turn - on delay time	t _{d(on)} *4	$V_{DD} = 500V, I_{D} = 1.1A$	-	16	-		
Rise time	t _r *4	V _{GS} = 18V/0V	-	21	-		
Turn - off delay time	t _{d(off)} *4	$R_L = 455\Omega$	-	35	-	ns	
Fall time	t _f *4	$R_G = 0\Omega$	-	74	-		
Turn - on switching loss	E _{on} *4	$V_{DD} = 800V, I_{D} = 1.1A$ $V_{GS} = 18V/0V$	-	57	-		
Turn - off switching loss	E _{off} *4	R _G = 0Ω, L=2mH *E _{on} includes diode reverse recovery	-	32	-	μJ	

•Gate Charge characteristics ($T_a = 25^{\circ}C$)

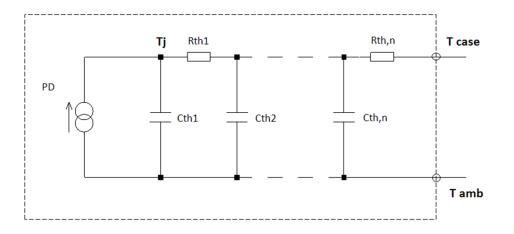
Parameter	Symbol	Conditions		Unit		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*4}	V _{DD} = 500V	-	14	-	
Gate - Source charge	Q_{gs}^{*4}	I _D = 1A	-	4	-	nC
Gate - Drain charge	Q_{gd}^{*4}	V _{GS} = 18V	-	5	-	
Gate plateau voltage	V _(plateau)	$V_{DD} = 500V, I_D = 1A$	-	10.5	-	V

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions		Unit			
Faranielei	Symbol Conditions –		Min.	Тур.	Max.	Unit	
Inverse diode continuous, forward current	ا _S *1	T _c = 25°C	-	-	4	А	
Inverse diode direct current, pulsed	I_{SM}^{*2}	T _c = 25 C	-	-	10	A	
Forward voltage	V_{SD} *4	$V_{GS} = 0V, I_{S} = 1.1A$	-	4.3	-	V	
Reverse recovery time	t _{rr} *4		-	21	-	ns	
Reverse recovery charge	Q _{rr} ^{*4}	I _F = 1.1A, V _R = 800V di/dt = 300A/μs	-	13	-	nC	
Peak reverse recovery current	^{*4}		-	1.1	-	А	

•Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	493m		C _{th1}	378µ	
R _{th2}	1601m	K/W	C _{th2}	1.42m	Ws/K
R _{th3}	556m		C _{th3}	65.6m	



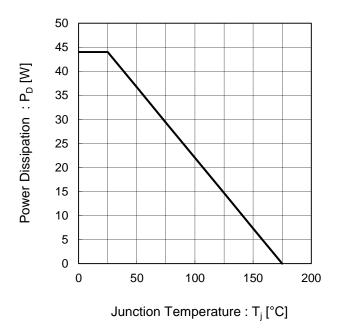
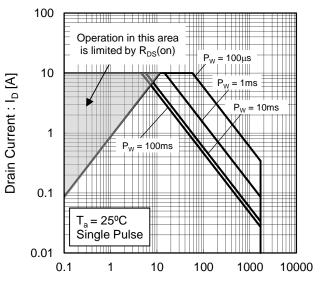
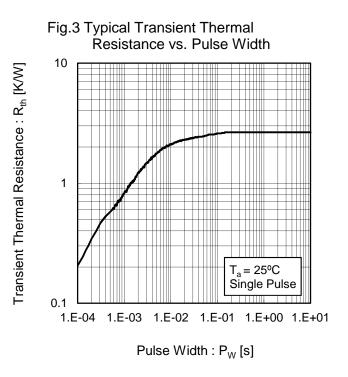


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]



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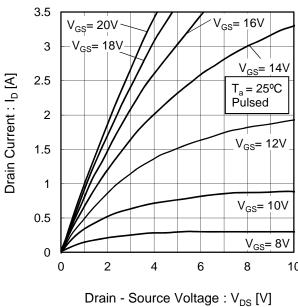
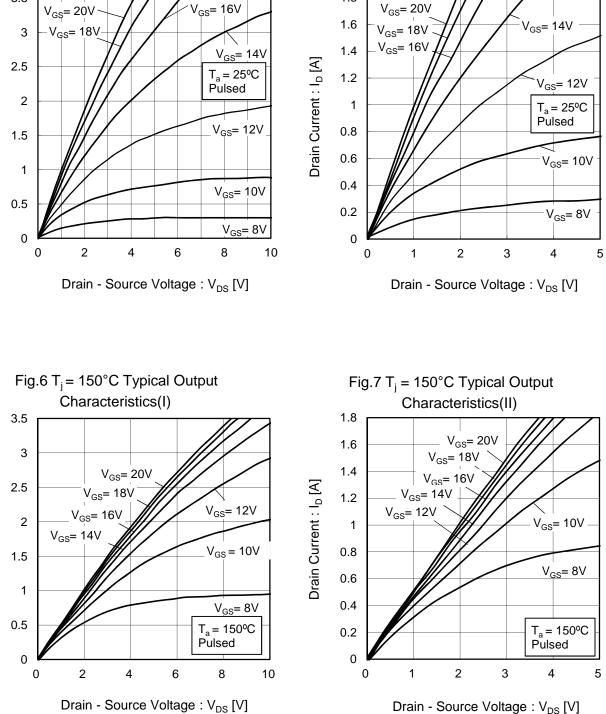


Fig.4 Typical Output Characteristics(I)

Fig.5 Typical Output Characteristics(II)



1.8

Drain Current : I_D [A]

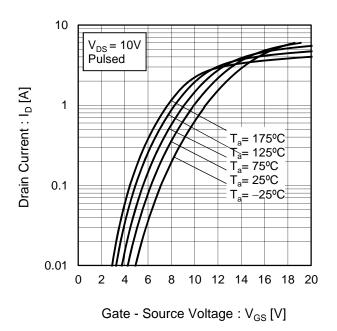


Fig.8 Typical Transfer Characteristics (I)

Fig.9 Typical Transfer Characteristics (II)

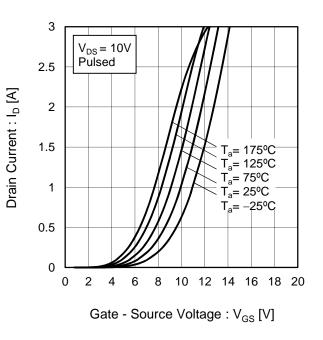
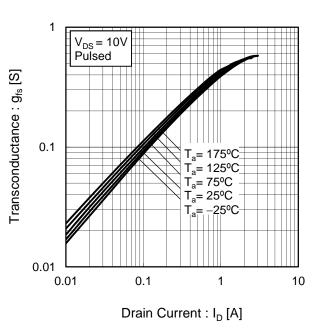


Fig.10 Gate Threshold Voltage vs. Junction Temperature 5 $V_{DS} = 10V$ 4.5 Gate Threshold Voltage : $V_{GS(th)}$ [V] $I_{\rm D} = 0.41 \,{\rm mA}$ 4 3.5 3 2.5 2 1.5 1 0.5 0 -50 0 50 100 200 150 Junction Temperature : T_i [°C]

Fig.11 Transconductance vs. Drain Current



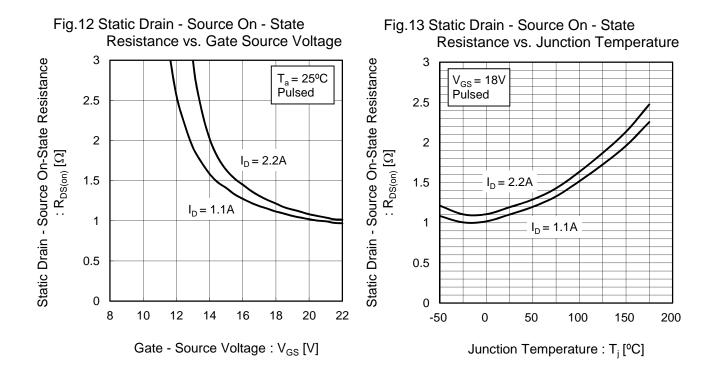


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current 10 Static Drain - Source On-State Resistance $V_{GS} = 18V$ Pulsed : $R_{DS(on)}$ [Ω] 1 T_a = 175°C T_a = 125⁰C T_{a} = 75°C $T_a = 25^{\circ}C$ T_a = −25°C 0.1 0.1 1 10

Drain Current : I_D [A]

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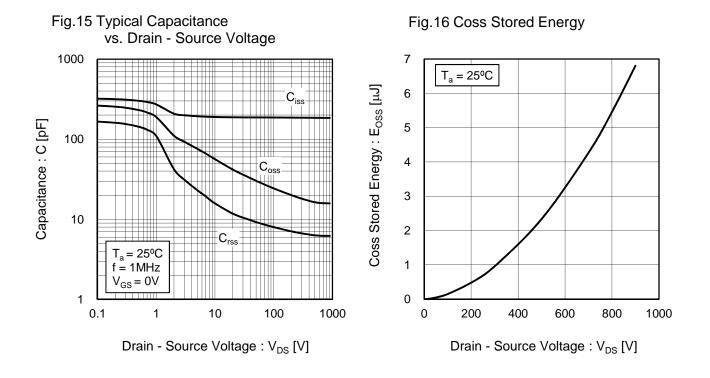
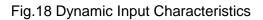
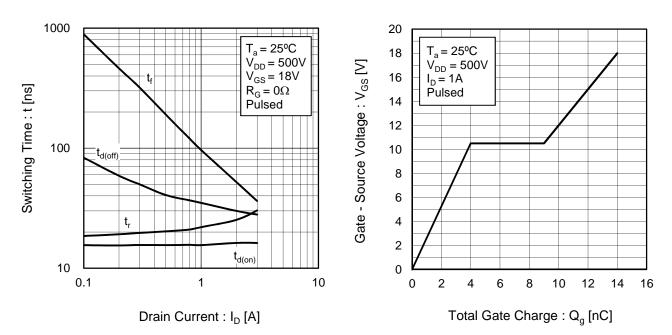
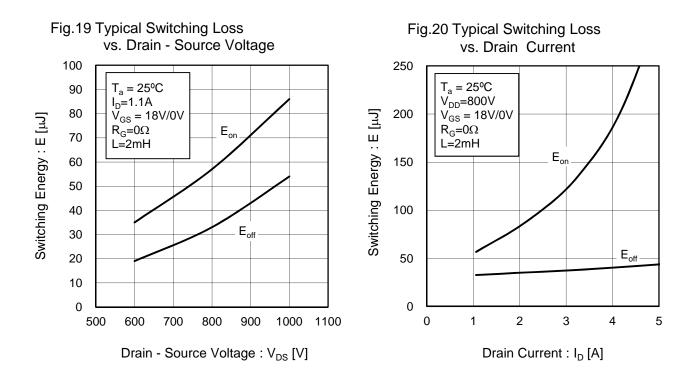
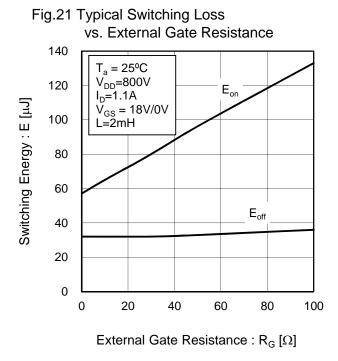


Fig.17 Switching Characteristics

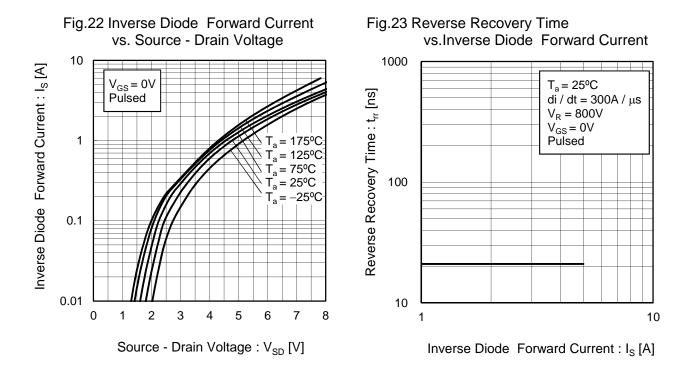














Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

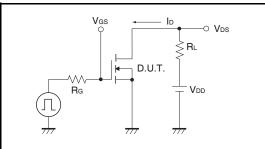


Fig.2-1 Gate Charge Measurement Circuit

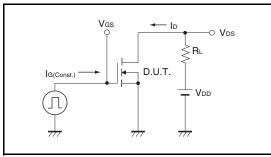


Fig.3-1 Switching Energy Measurement Circuit

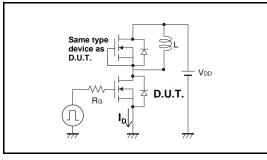


Fig.4-1 Reverse Recovery Time Measurement Circuit Fig.4-2 Reverse Recovery Waveform

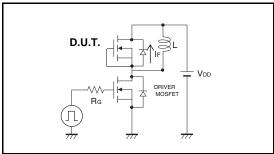


Fig.1-2 Switching Waveforms

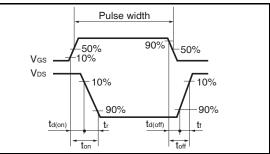


Fig.2-2 Gate Charge Waveform

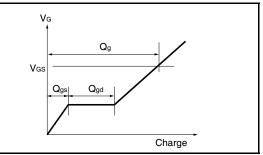
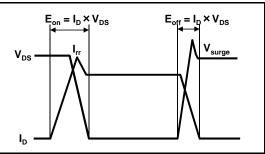
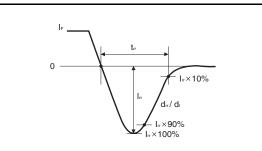


Fig.3-2 Switching Waveforms







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