SCT3105KRHR

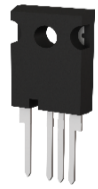
Automotive Grade N-channel SiC power MOSFET

Datasheet

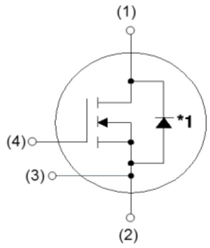
V_{DSS}	1200V
$R_{DS(on)}$ (Typ.)	105mΩ
I_D *1	24A
P_D	134W

●Outline

TO-247-4L



●Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

●Features

1) Qualified to AEC-Q101

2) Low on-resistance

3) Fast switching speed

4) Fast reverse recovery

5) Easy to parallel

6) Simple to drive

7) Pb-free lead plating ; RoHS compliant

●Application

・Automobile

・Switch mode power supplies

●Packaging specifications

Type	Packing	Tube
	Reel size (mm)	-
	Tape width (mm)	-
	Basic ordering unit (pcs)	30
	Taping code	C15
	Marking	SCT3105KR

●Absolute maximum ratings (T_{vj} = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source Voltage		V_{DSS}	1200	V
Continuous Drain current	T_c = 25°C	I_D *1	24	A
	T_c = 100°C	I_D *1	17	A
Pulsed Drain current (T_c = 25°C)		$I_{D,pulse}$ *2	60	A
Gate - Source voltage (DC)		V_{GSS}	-4 to +22	V
Gate - Source surge voltage (t_{surge} < 300ns)		V_{GSS_surge} *3	-4 to +26	V
Recommended drive voltage		V_{GS_op} *4	0 / +18	V
Virtual Junction temperature		T_{vj}	175	°C
Range of storage temperature		T_{stg}	-55 to +175	°C

●Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 1mA T_{vj} = 25°C	1200	-	-	V
		T_{vj} = -55°C	1200	-	-	
Zero Gate voltage Drain current	I_{DSS}	V_{GS} = 0V, V_{DS} =1200V T_{vj} = 25°C	-	1	10	μA
		T_{vj} = 150°C	-	2	-	
Gate - Source leakage current	I_{GSS+}	V_{GS} = +22V, V_{DS} = 0V	-	-	100	nA
Gate - Source leakage current	I_{GSS-}	V_{GS} = -4V, V_{DS} = 0V	-	-	-100	nA
Gate threshold voltage	$V_{GS\ (th)}$	V_{DS} = 10V, I_D = 3.81mA	2.7	-	5.6	V
Static Drain - Source on - state resistance	$R_{DS(on)}$ *5	V_{GS} = 18V, I_D = 7.6A T_{vj} = 25°C	-	105	137	mΩ
		T_{vj} = 150°C	-	179	-	
Gate input resistance	R_G	f = 1MHz, open drain	-	13	-	Ω

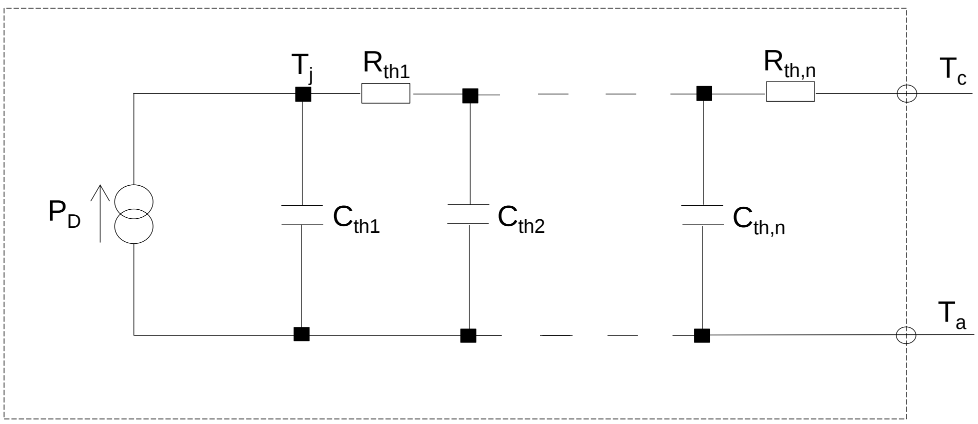
●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - case	R_{thJC}	-	0.86	1.12	K/W

●Typical Transient Thermal Characteristics

Symbol	Value	Unit
R_{th1}	1.14×10^{-1}	K/W
R_{th2}	5.07×10^{-1}	
R_{th3}	2.51×10^{-1}	

Symbol	Value	Unit
C_{th1}	5.02×10^{-4}	Ws/K
C_{th2}	4.91×10^{-3}	
C_{th3}	4.99×10^{-2}	



● **Electrical characteristics** (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Transconductance	g_{fs} *5	V_{DS} = 10V, I_D = 7.6A	-	3.4	-	S
Input capacitance	C_{iss}	V_{GS} = 0V V_{DS} = 800V f = 1MHz	-	574	-	pF
Output capacitance	C_{oss}		-	59	-	
Reverse transfer capacitance	C_{rss}		-	28	-	
Effective output capacitance, energy related	$C_{o(er)}$	V_{GS} = 0V V_{DS} = 0V to 600V	-	159	-	pF
Total Gate charge	Q_g *5	V_{DS} = 600V I_D = 7.6A V_{GS} = 18V See Fig. 1-1.	-	51	-	nC
Gate - Source charge	Q_{gs} *5		-	10	-	
Gate - Drain charge	Q_{gd} *5		-	25	-	
Turn - on delay time	$t_{d(on)}$ *5	V_{DS} = 600V I_D = 15A V_{GS} = 0V/+18V R_G = 0Ω, L = 750μH L_σ = 50nH, C_σ = 10pF See Fig. 2-1, 2-2, 2-3. E_{on} includes diode reverse recovery.	-	4	-	ns
Rise time	t_r *5		-	12	-	
Turn - off delay time	$t_{d(off)}$ *5		-	15	-	
Fall time	t_f *5		-	11	-	
Turn - on switching loss	E_{on} *5		-	134	-	μJ
Turn - off switching loss	E_{off} *5		-	10	-	

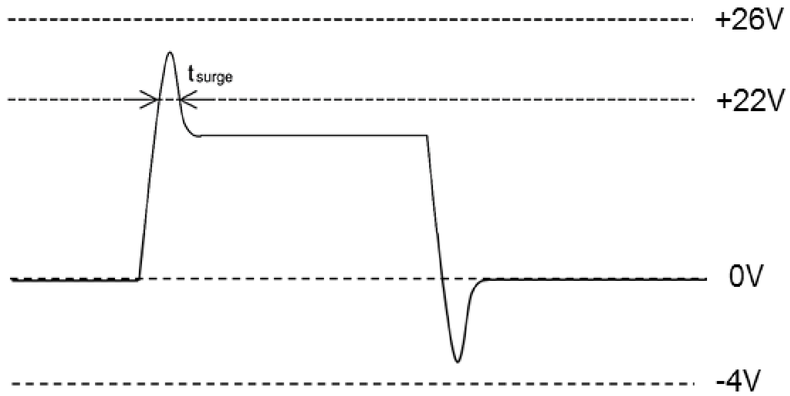
●**Body diode electrical characteristics** (Source-Drain) (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Body diode continuous, forward current	I_S *1	T_c = 25°C	-	-	24	A
Body diode direct current, pulsed	I_{SM} *2	T_c = 25°C	-	-	60	A
Forward voltage	V_{SD} *5	V_{GS} = 0V, I_S = 7.6A	-	3.2	-	V
Reverse recovery time	t_{rr} *5	I_F = 7.6A V_R = 600V di/dt = 2500A/µs $L_σ$ = 50nH, $C_σ$ = 10pF See Fig. 3-1, 3-2.	-	13	-	ns
Reverse recovery charge	Q_{rr} *5		-	175	-	nC
Peak reverse recovery current	I_{rrm} *5		-	22	-	A

*1 Limited by maximum T_{vj} and for Max. R_{thJC}.

*2 PW ≤ 10µs, Duty cycle ≤ 1%

*3 Example of acceptable V_{GS} waveform



Please note especially when using driver source that V_{GSS_surge} must be in the range of absolute maximum rating.

*4 Please be advised not to use SiC-MOSFETs with V_{GS} below 13V as doing so may cause thermal runaway.

*5 Pulsed

●Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

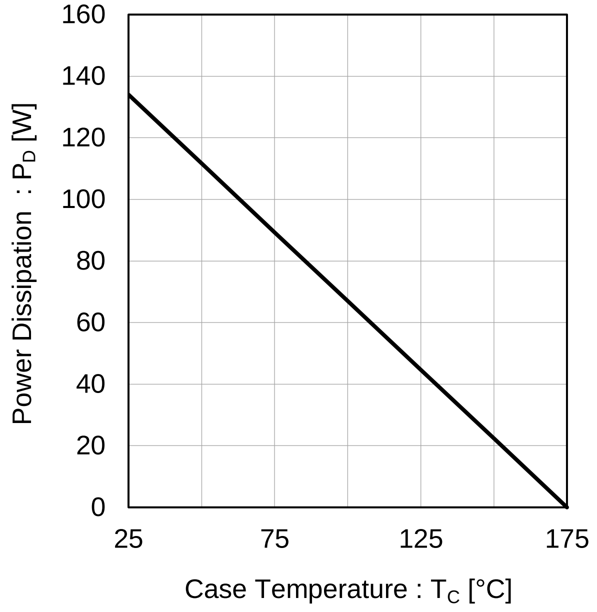


Fig.2 Maximum Safe Operating Area

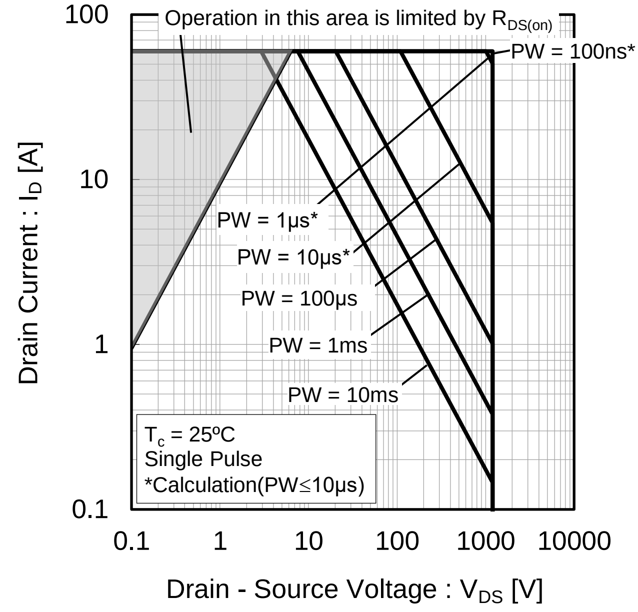
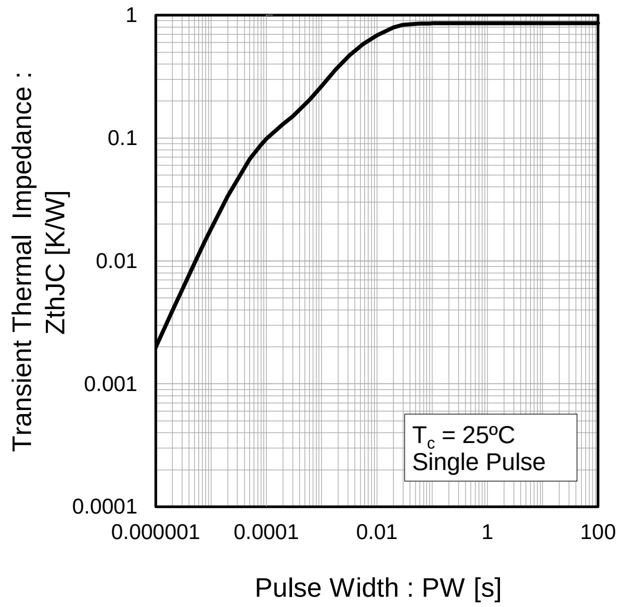


Fig.3 Typical Transient Thermal Impedance vs. Pulse Width



●Electrical characteristic curves

Fig.4 Typical Output Characteristics(I)

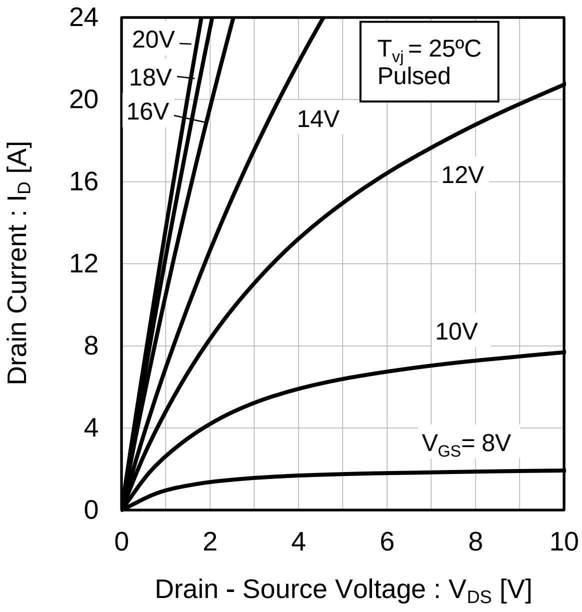


Fig.5 Typical Output Characteristics(II)

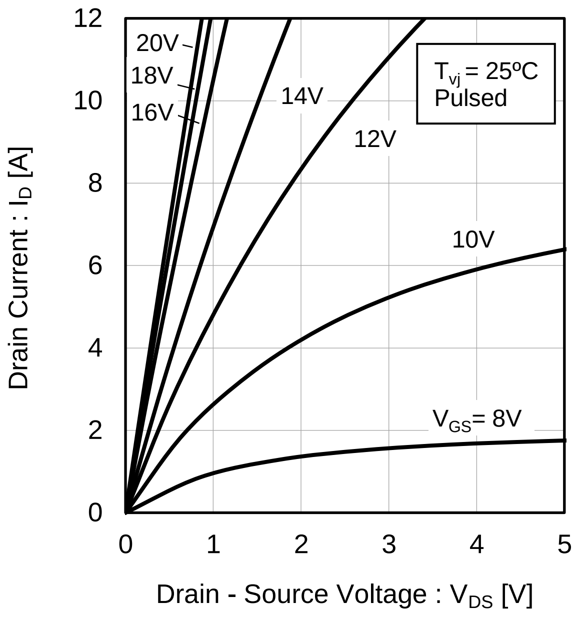
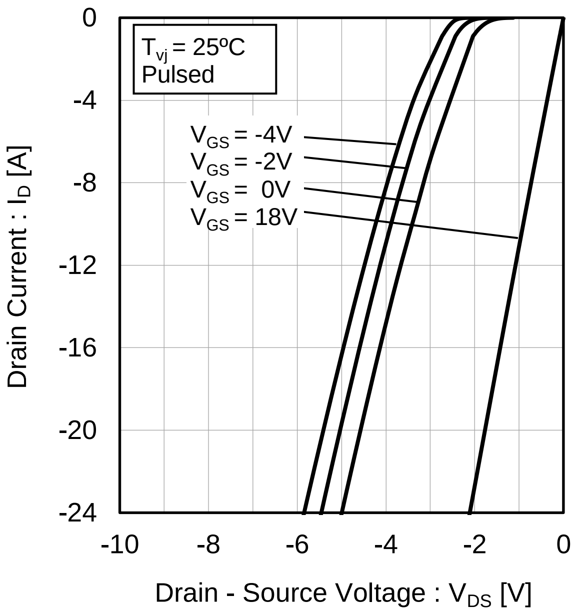


Fig.6 T_{vj} = 25ºC 3rd Quadrant Characteristics



●Electrical characteristic curves

Fig.7 T_{vj} = 150ºC Typical Output Characteristics(I)

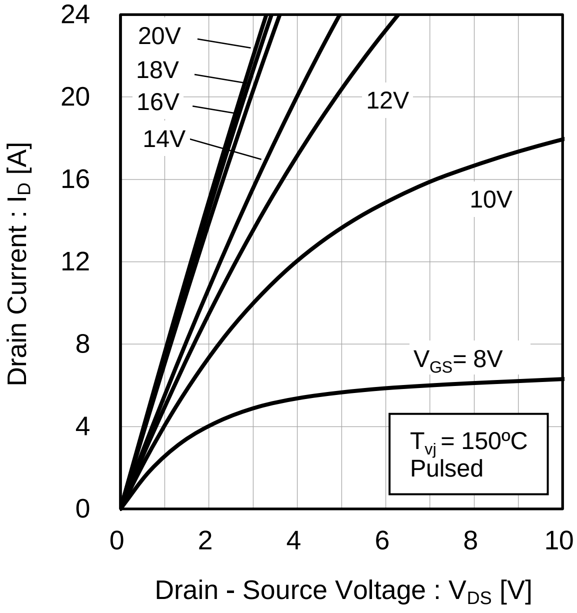


Fig.8 T_{vj} = 150ºC Typical Output Characteristics(II)

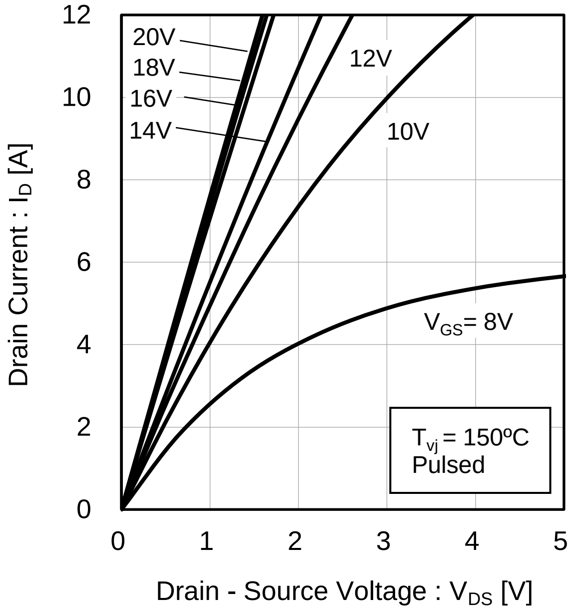


Fig.9 T_{vj} = 150ºC 3rd Quadrant Characteristics

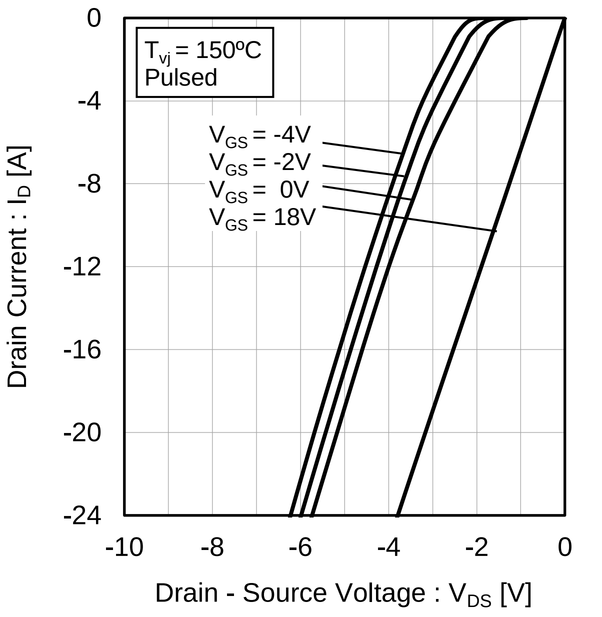
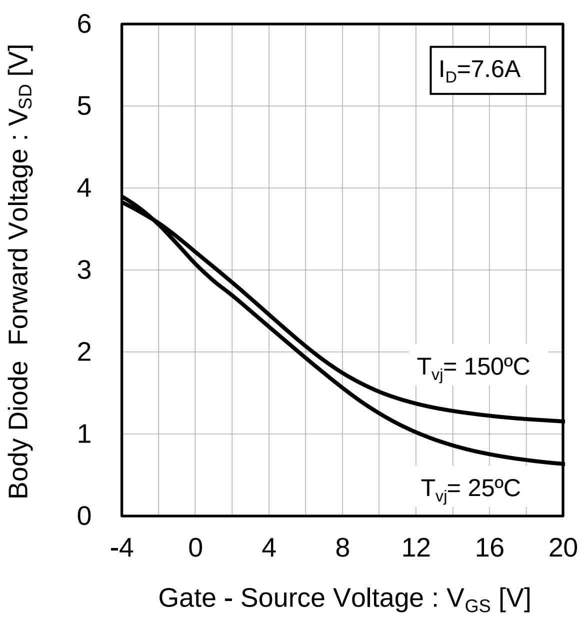


Fig.10 Body Diode Forward Voltage vs. Gate - Source Voltage



●Electrical characteristic curves

Fig.11 Typical Transfer Characteristics (I)

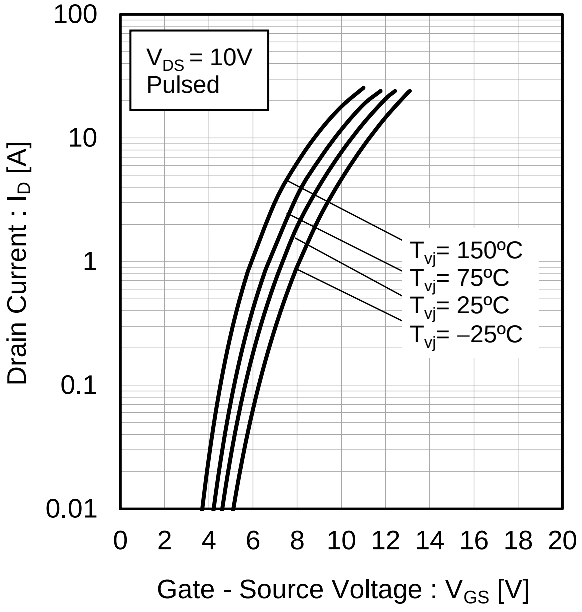


Fig.12 Typical Transfer Characteristics (II)

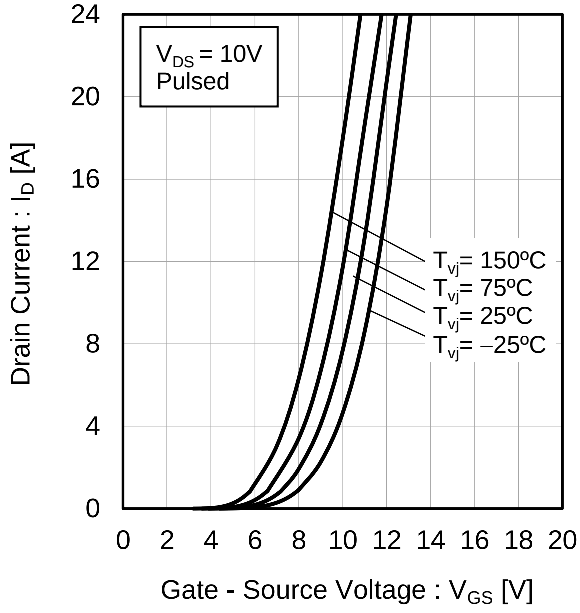


Fig.13 Gate Threshold Voltage vs. Virtual Junction Temperature

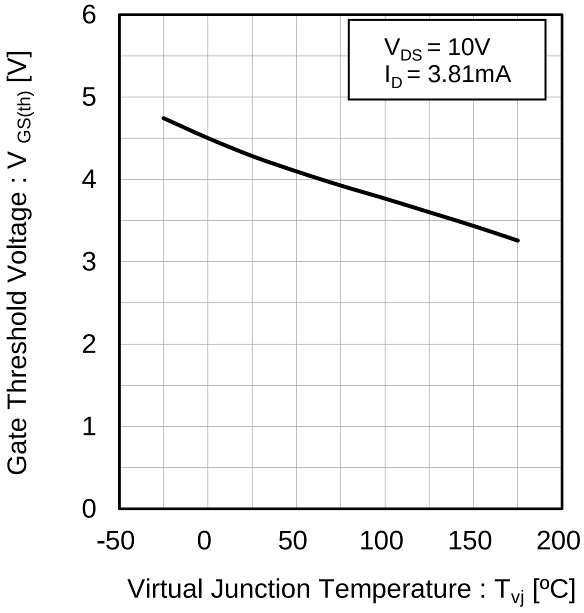
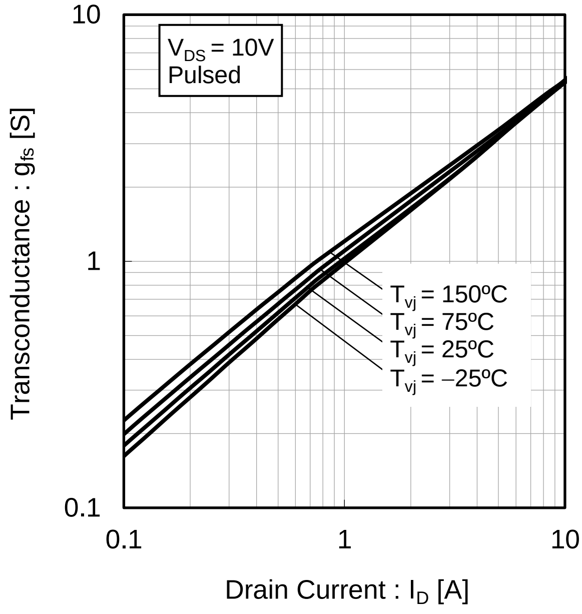


Fig.14 Transconductance vs. Drain Current



●Electrical characteristic curves

Fig.15 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

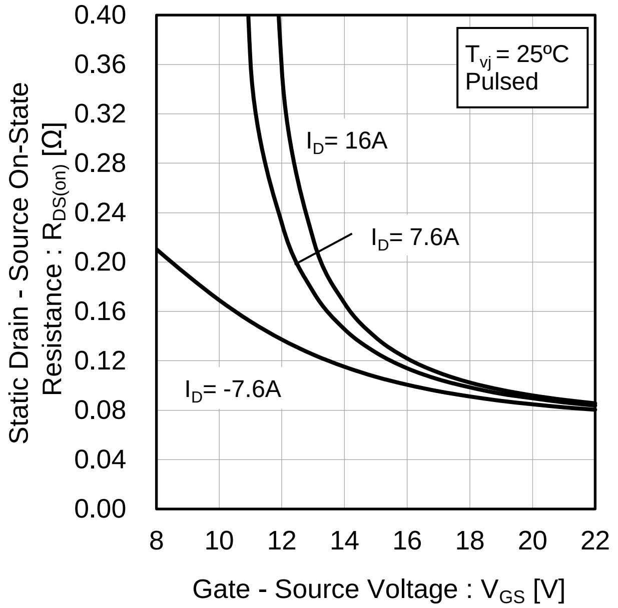


Fig.16 Static Drain - Source On - State Resistance vs. Virtual Junction Temperature

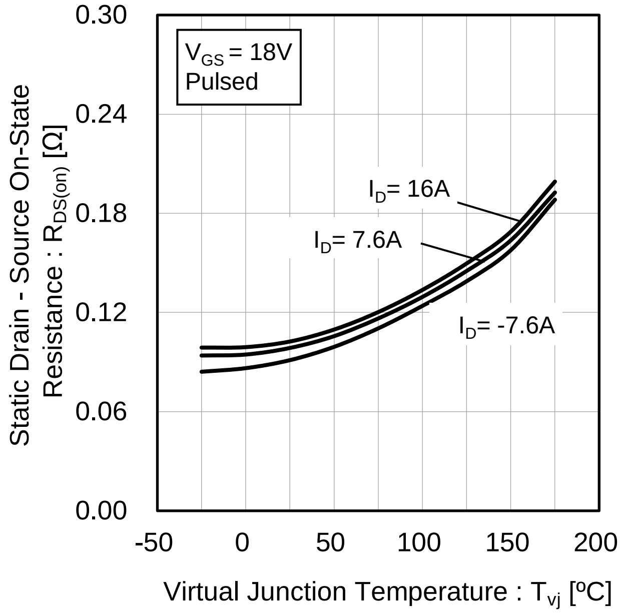


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current

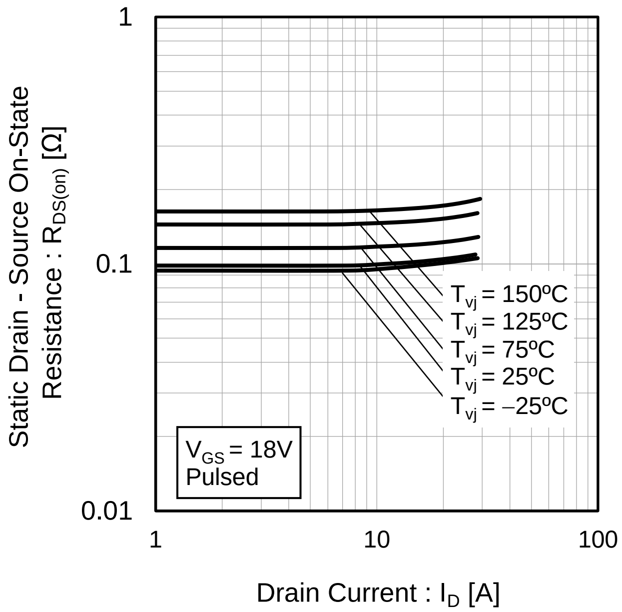
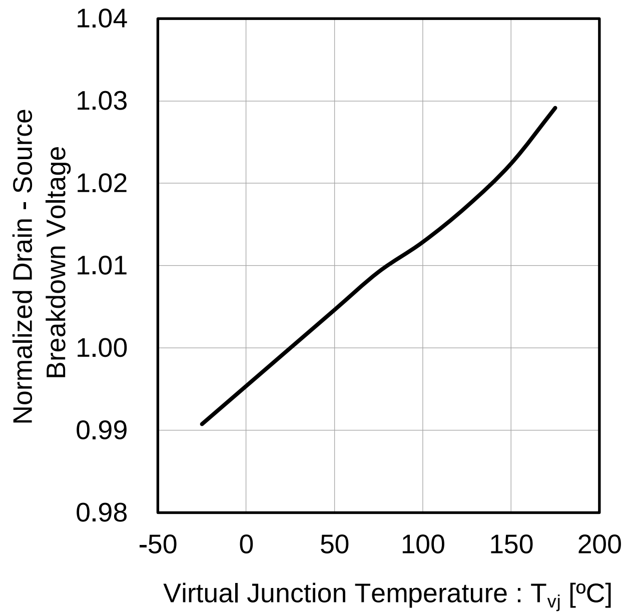


Fig.18 Normalized Drain - Source Breakdown Voltage vs. Virtual Junction Temperature



●Electrical characteristic curves

Fig.19 Typical Capacitance vs. Drain - Source Voltage

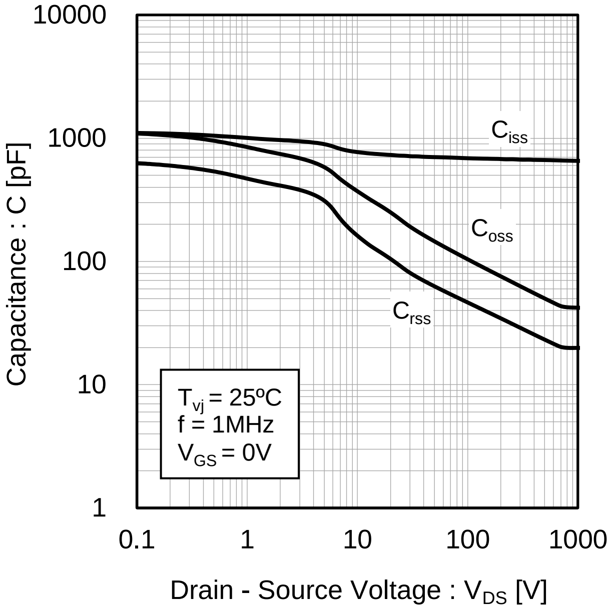


Fig.20 C_{oss} Stored Energy

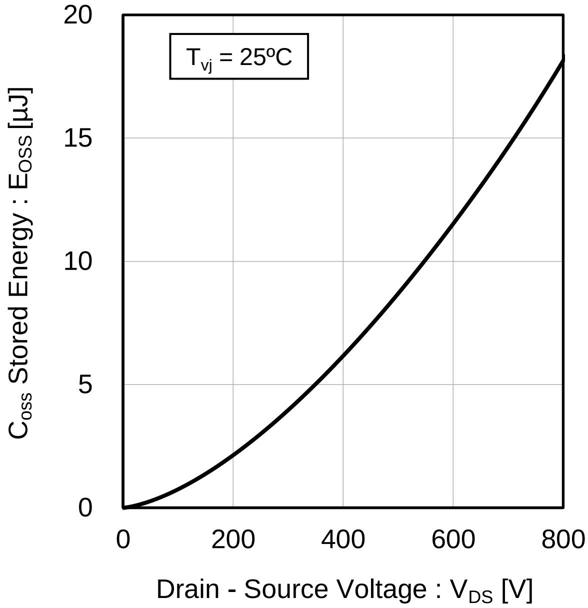
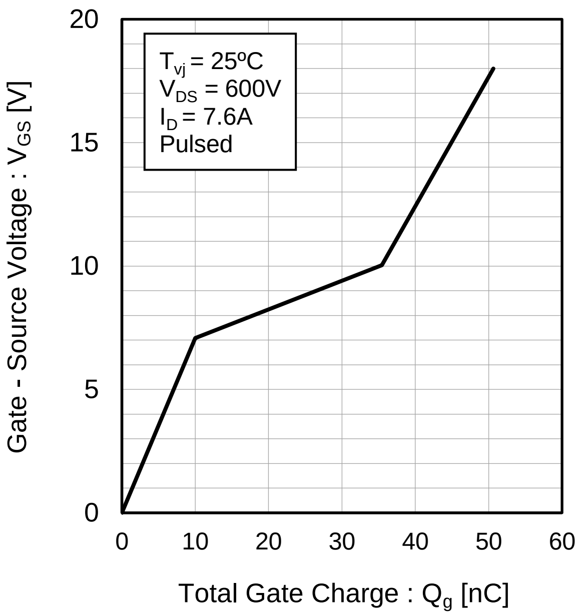
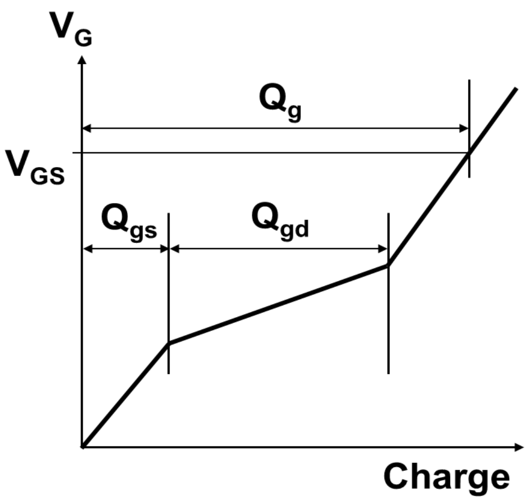


Fig.21 Dynamic Input Characteristics



*Gate Charge Waveform



●Electrical characteristic curves

Fig.22 Typical Switching Time vs. External Gate Resistance

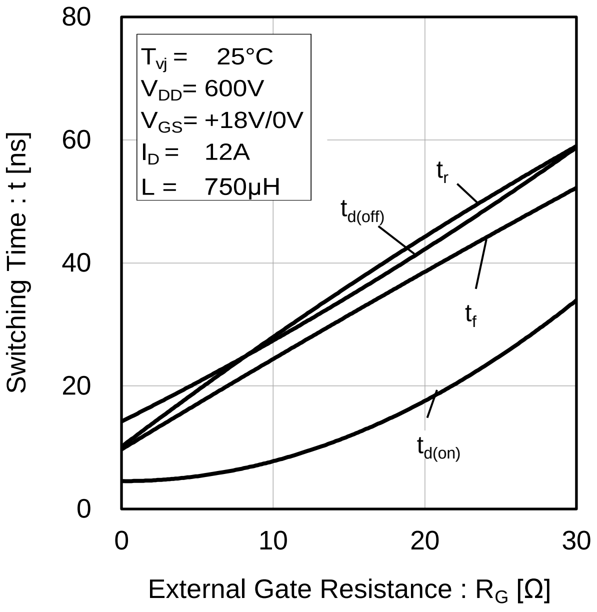


Fig.23 Typical Switching Loss vs. Drain - Source Voltage

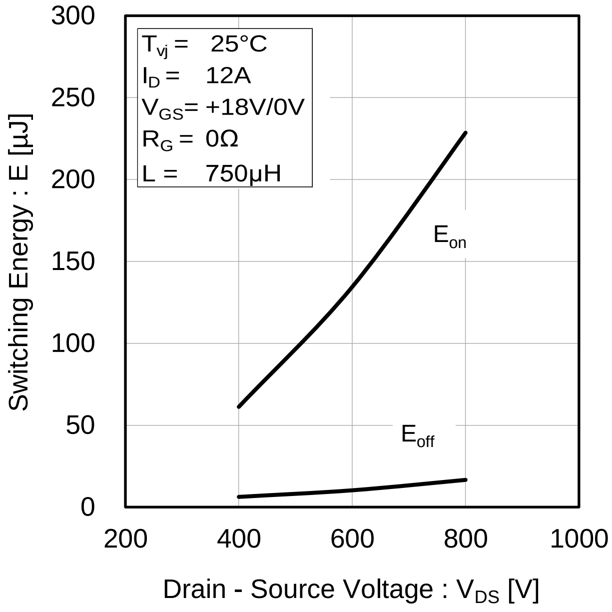


Fig.24 Typical Switching Loss vs. Drain Current

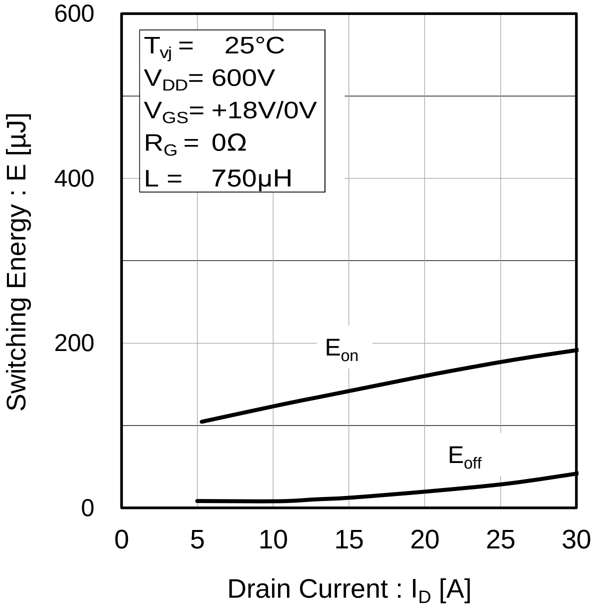
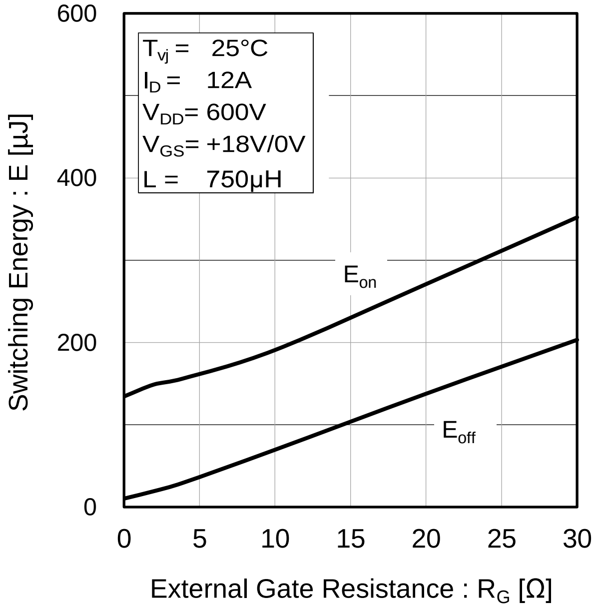


Fig.25 Typical Switching Loss vs. External Gate Resistance



●Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

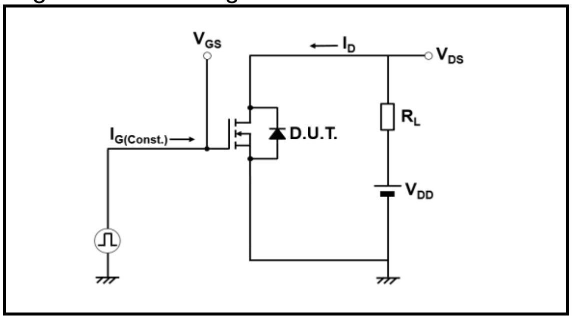


Fig.2-1 Switching Characteristics Measurement Circuit

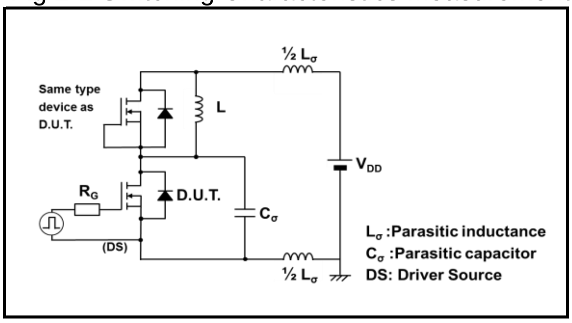


Fig.2-2 Waveforms for Switching Time

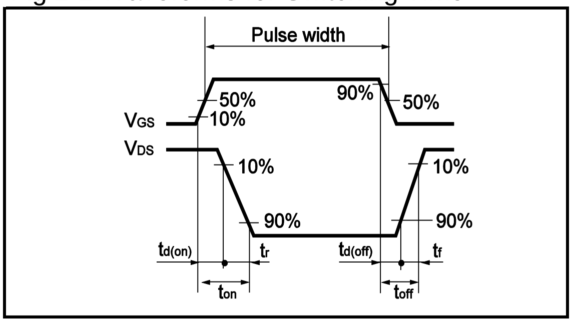


Fig.2-3 Waveforms for Switching Energy Loss

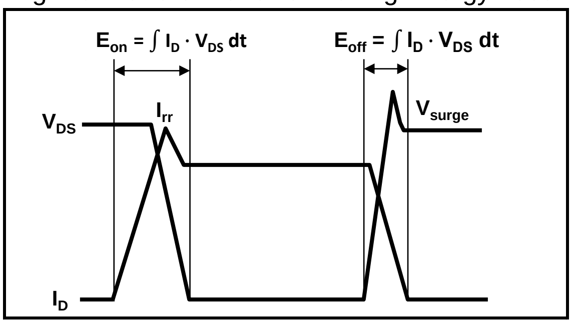


Fig.3-1 Reverse Recovery Time Measurement Circuit

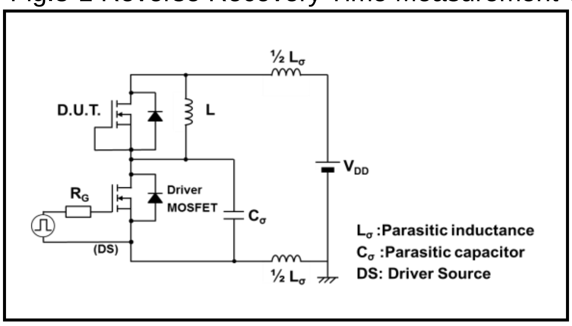
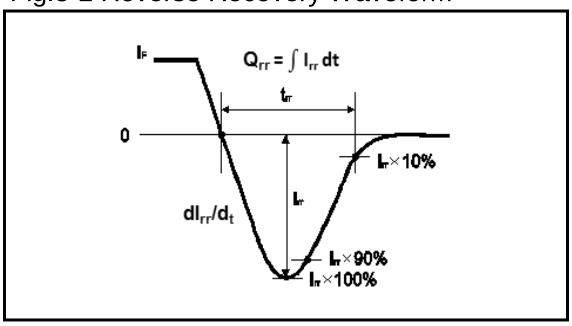
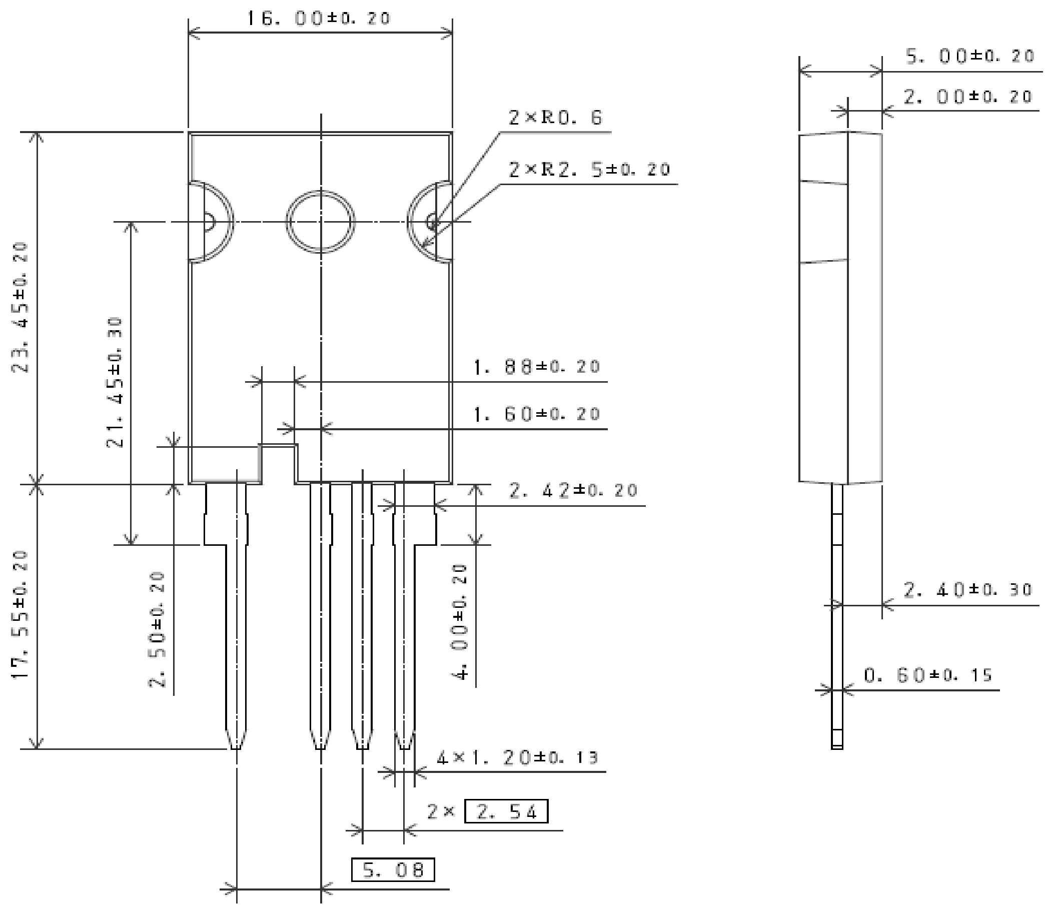


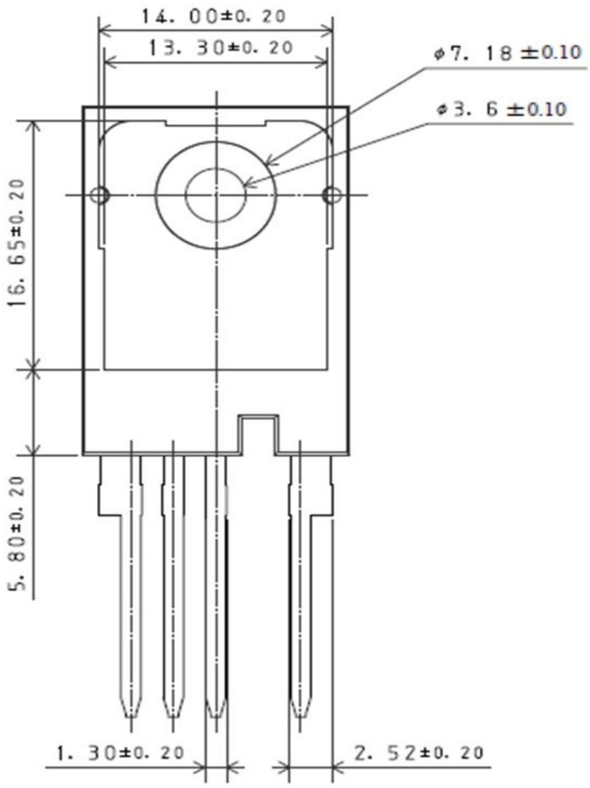
Fig.3-2 Reverse Recovery Waveform



●Package Dimensions

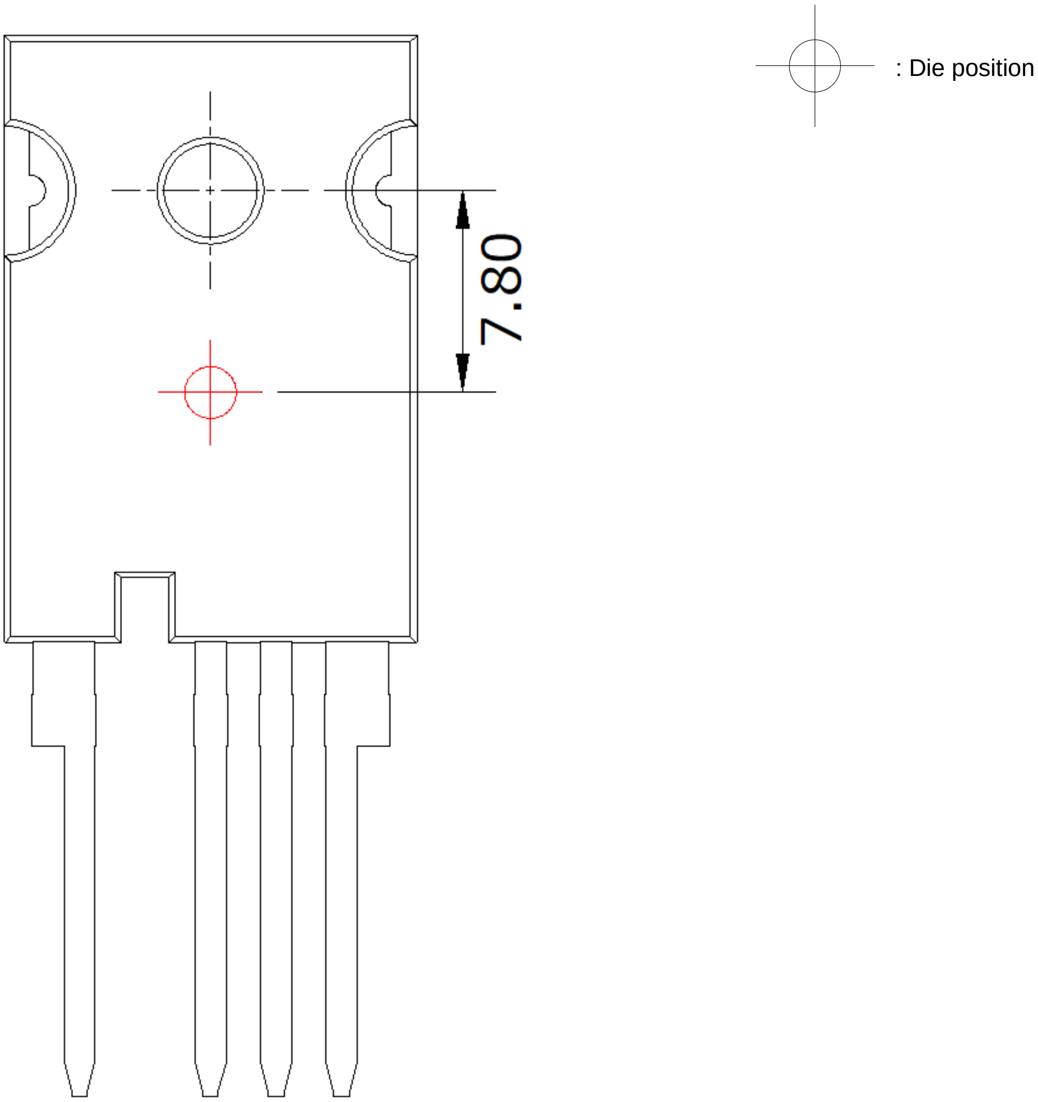


Unit: mm


14. 00±0. 20
13. 30±0. 20
φ7. 18 ±0.10
φ3. 6 ±0.10
16. 65±0. 20
5. 80±0. 20
1. 30±0. 20
2. 52±0. 20

Unit: mm

●Die Bonding Layout



･Front view of the packaging.

･Dimensions are design values.

･If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

Notes

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