

## **N-channel SiC power MOSFET**

$V_{\mathrm{DSS}}$	650V
R <sub>DS(on)</sub> (Typ.)	120mΩ
I <sub>D</sub> *1	21A
$P_D$	100W

# ●Outline TO-263-7L (Tab)

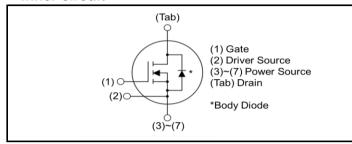
#### Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

## Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- · Induction heating
- Motor drives

#### •Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

## Packaging specifications

	Packing	Embossed tape
	Reel size (mm)	330
Type	Tape width (mm)	24
Type	Basic ordering unit (pcs)	1000
	Taping code	TL
	Marking	SCT3120AW7

## ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter		Symbol	Value	Unit
Drain - Source Voltage		$V_{DSS}$	650	V
Continuous Drain current	T <sub>c</sub> = 25°C	I <sub>D</sub> *1	21	А
Continuous Drain current	T <sub>c</sub> = 100°C	I <sub>D</sub> *1	15	А
Pulsed Drain current		I <sub>D,pulse</sub> *2	52	Α
Gate - Source voltage (DC)		$V_{GSS}$	-4 to +22	V
Gate - Source surge voltage (t <sub>surge</sub> < 300ns)		V <sub>GSS_surge</sub> *3	-4 to +26	V
Recommended drive voltage		V <sub>GS_op</sub> *4	0 / +18	V
Junction temperature		T <sub>j</sub>	175	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +175	°C

# •Electrical characteristics ( $T_a = 25$ °C)

Darameter	Symbol Conditions -		Values			Unit
Parameter			Min.	Тур.	Max.	Offic
		$V_{GS} = 0V$ , $I_D = 1mA$				
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$T_j = 25^{\circ}C$	650	-	-	V
vollago		T <sub>j</sub> = -55°C	650	-	-	
		$V_{GS} = 0V, V_{DS} = 650V$				
Zero Gate voltage Drain current	I <sub>DSS</sub>	$T_j = 25^{\circ}C$	-	1	10	μΑ
Drain danein		T <sub>j</sub> = 150°C	-	2	-	
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS} = +22V, \ V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I <sub>GSS-</sub>	$V_{GS} = -4V$ , $V_{DS} = 0V$	ı	ı	-100	nA
Gate threshold voltage	V <sub>GS (th)</sub>	$V_{DS} = 10V, I_{D} = 3.33mA$	2.7	1	5.6	V
		$V_{GS} = 18V, I_D = 6.7A$				
Static Drain - Source on - state resistance	R <sub>DS(on)</sub> *5	$T_j = 25^{\circ}C$	-	120	156	mΩ
		T <sub>j</sub> = 150°C	-	172	-	
Gate input resistance	$R_{G}$	f = 1MHz, open drain	-	18	-	Ω

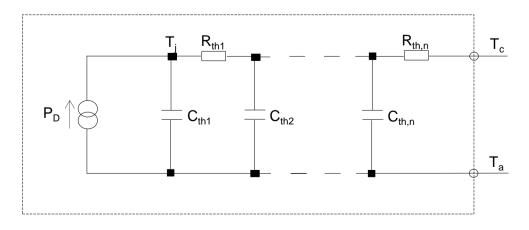
#### ●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case*6	$R_{thJC}$	-	1.17	1.5	°C/W

● Typical Transient Thermal Characteristics

Symbol	Value	Unit
R <sub>th1</sub>	1.95×10 <sup>-1</sup>	
R <sub>th2</sub>	3.47×10 <sup>-1</sup>	K/W
R <sub>th3</sub>	5.60×10 <sup>-1</sup>	

Symbol	Value	Unit
$C_{th1}$	1.38×10 <sup>-3</sup>	
$C_{th2}$	1.40×10 <sup>-2</sup>	Ws/K
C <sub>th3</sub>	8.68×10 <sup>-3</sup>	



# ●Electrical characteristics (T<sub>a</sub> = 25°C)

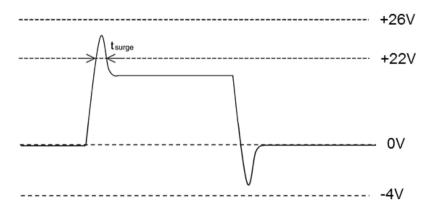
Parameter	Symbol	Conditions		Values		Unit
		Conditions	Min.	Тур.	Max.	Unit
Transconductance	<b>g</b> fs *5	$V_{DS} = 10V, I_{D} = 6.7A$	-	2.7	-	S
Input capacitance	C <sub>iss</sub>	$V_{GS} = 0V$	-	460	ı	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 500V	-	35	•	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	16	ı	
Effective output capacitance, energy related	C <sub>o(er)</sub>	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 300V$	-	70	ı	pF
Total Gate charge	Qg *5	$V_{DS} = 300V$ $I_{D} = 6.7A$	-	38	1	
Gate - Source charge	Q <sub>gs</sub> *5	$V_{GS} = 18V$	-	10	ı	nC
Gate - Drain charge	Q <sub>gd</sub> *5	See Fig. 1-1.	-	18	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DS} = 400V$ $I_{D} = 5.0A$	-	6	ı	
Rise time	t <sub>r</sub> *5	$V_{GS} = 0V/+18V$	-	14	-	no
Turn - off delay time	t <sub>d(off)</sub> *5	$R_G = 0\Omega, L = 750\mu H$ $L_{\sigma} = 50 nH, C_{\sigma} = 10 pF$	-	19	ı	ns
Fall time	t <sub>f</sub> *5	See Fig. 2-1, 2-2, 2-3.	-	11	-	
Turn - on switching loss	E <sub>on</sub> *5	E <sub>on</sub> includes diode reverse recovery.	-	49	-	1
Turn - off switching loss	E <sub>off</sub> *5		-	4	-	μJ

## ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions		Values	Unit	
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Body diode continuous, forward current	I <sub>S</sub> *1	T <sub>c</sub> = 25°C	-	-	21	А
Body diode direct current, pulsed	I <sub>SM</sub> *2	11 <sub>c</sub> = 23 0	ı	ı	52	А
Forward voltage	V <sub>SD</sub> *5	$V_{GS} = 0V, I_{D} = 6.7A$	1	3.2	ı	V
Reverse recovery time	t <sub>rr</sub> *5	$I_F = 5.0A$ $V_R = 400V$	ı	11	ı	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 2500A/µs	ı	133	ı	nC
Peak reverse recovery current	I <sub>rrm</sub> *5	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	11	-	А

<sup>\*1</sup> Limited by maximum temperature allowed.

## \*3 Example of acceptable V<sub>GS</sub> waveform



Please note especially when using driver source that  $V_{\text{GSS\_surge}}$  must be in the range of absolute maximum rating.

\*4 Please be advised not to use SiC-MOSFETs with V<sub>GS</sub> below 13V as doing so may cause thermal runaway.

#### \*5 Pulsed

\*6 The case is bottom of leadframe underneath the chip. Practial value of Rth(j-c) is influenced by design of the user. Discribed value is only vaild at the specific conditions such as JESD51-14.

<sup>\*2</sup>  $P_W \le 10\mu s$ , Duty cycle  $\le 1\%$ 

Fig.1 Power Dissipation Derating Curve

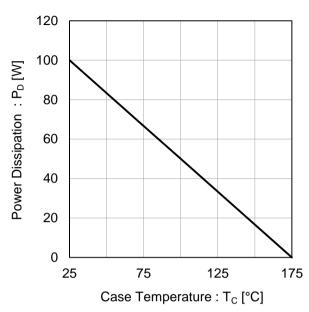


Fig.2 Maximum Safe Operating Area

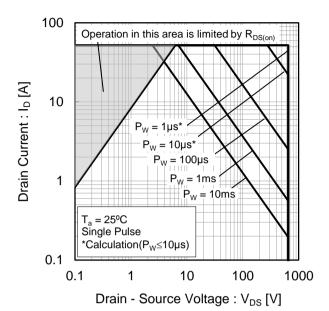


Fig.3 Typical Transient Thermal Resistance vs. Pulse Width

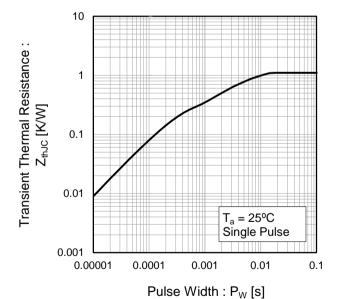


Fig.4 Typical Output Characteristics(I)

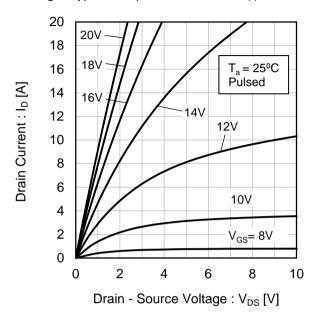


Fig.5 Typical Output Characteristics(II)

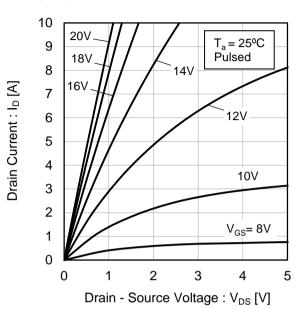


Fig.6 T<sub>i</sub> = 25°C 3rd Quadrant Characteristics

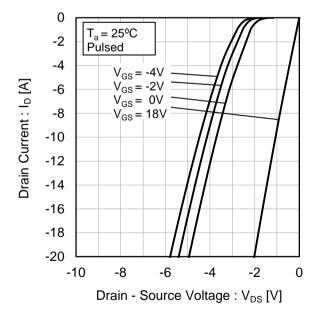


Fig.7 T<sub>i</sub> = 150°C Typical Output Characteristics(I) 20 20V 18 14V 18V  $T_a = 150^{\circ}C$ 16 Pulsed Drain Current : I<sub>D</sub> [A] 16V 14 12V 12 10V 10 8 6  $V_{GS} = 8V$ 4 2 0 0 2 4 6 10

Fig.8 T<sub>i</sub> = 150°C Typical Output

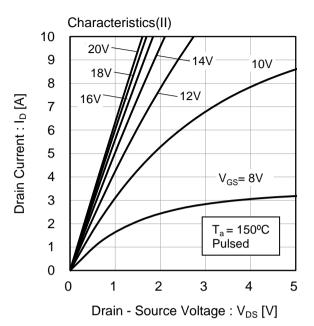


Fig.9 T<sub>i</sub> = 150°C 3rd Quadrant Characteristics

Drain - Source Voltage : V<sub>DS</sub> [V]

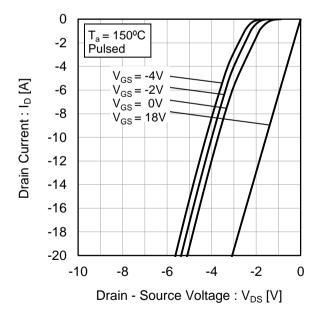


Fig.10 Body Diode Forward Voltage vs. Gate - Source Voltage

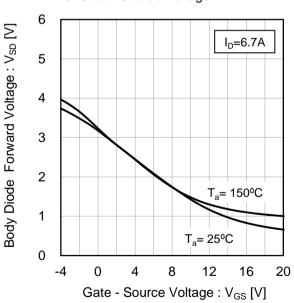


Fig.11 Typical Transfer Characteristics (I)

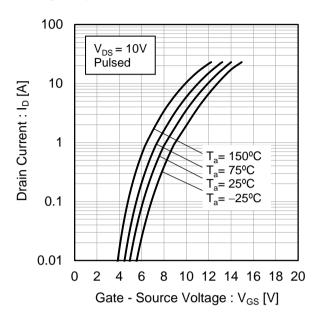


Fig.12 Typical Transfer Characteristics (II)

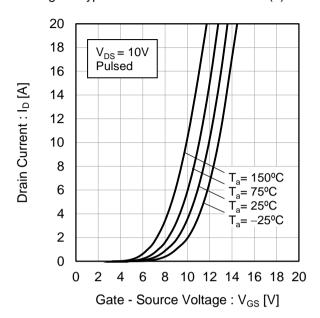


Fig.13 Gate Threshold Voltage vs. Junction Temperature

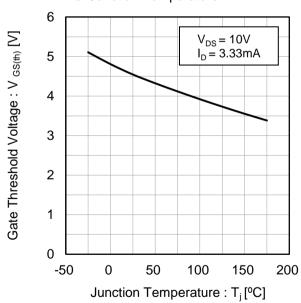
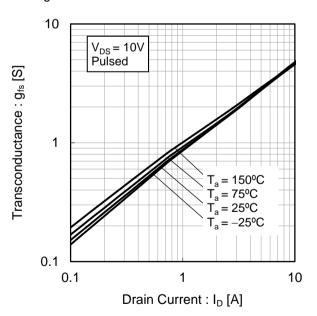
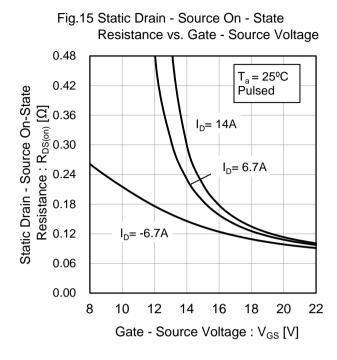


Fig.14 Transconductance vs. Drain Current





Resistance vs. Junction Temperature 0.24  $V_{GS} = 18V$ Static Drain - Source On-State Pulsed 0.20 I<sub>D</sub>= 14A Resistance :  $R_{DS(on)}$  [ $\Omega$ ] I<sub>D</sub>= 6.7A 0.16 0.12  $I_{D} = -6.7A$ 0.08 0.04 0.00 -50 0 50 100 150 200 Junction Temperature : T<sub>i</sub> [°C]

Fig.16 Static Drain - Source On - State

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current 1 Static Drain - Source On-State Resistance: R<sub>DS(on)</sub> [Ω] 0.1 = 150°C = 125°C T<sub>a</sub> = 75°C  $T_a = 25^{\circ}C$  $= -25^{\circ}C$  $V_{GS} = 18V$ Pulsed 0.01 1 10 100 Drain Current: ID [A]

Voltage vs. Junction Temperature

1.04

1.03

1.03

0.08

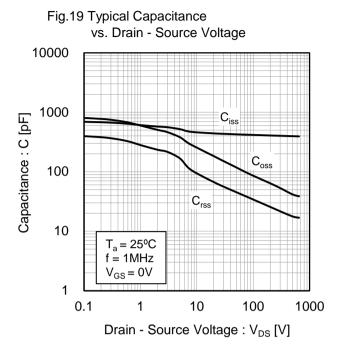
0.99

0.98

1.00

Junction Temperature : T<sub>i</sub> [°C]

Fig.18 Normalized Drain - Source Breakdown



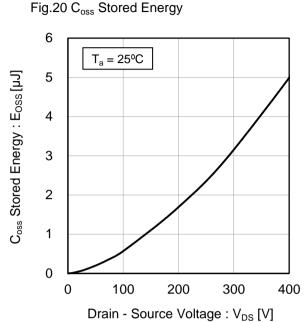
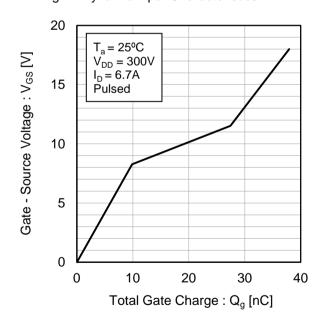
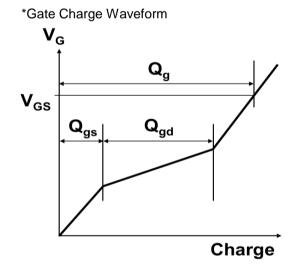


Fig.21 Dynamic Input Characteristics





#### Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

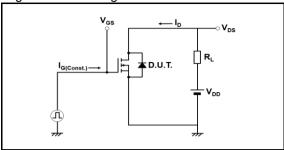


Fig.2-1 Switching Characteristics Measurement Circuit

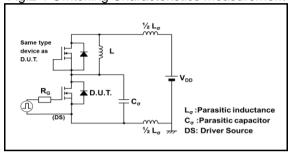


Fig.2-2 Waveforms for Switching Time

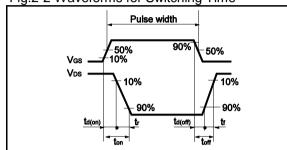


Fig.2-3 Waveforms for Switching Energy Loss

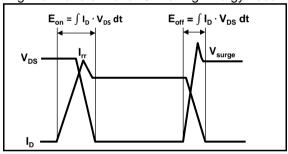


Fig.3-1 Reverse Recovery Time Measurement Circuit

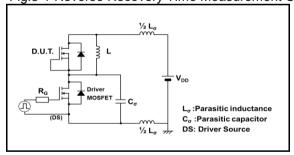


Fig.3-2 Reverse Recovery Waveform

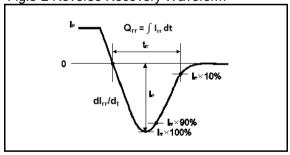


Fig.22 Typical Switching Time vs. External Gate Resistance

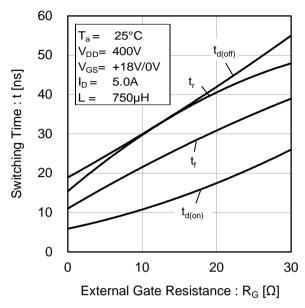


Fig.23 Typical Switching Loss vs. Drain - Source Voltage

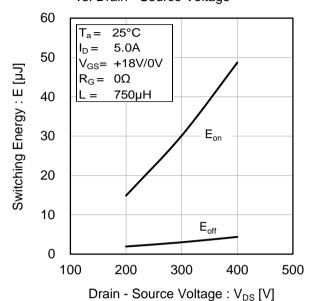


Fig.24 Typical Switching Loss vs. Drain Current

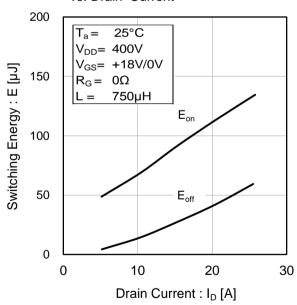
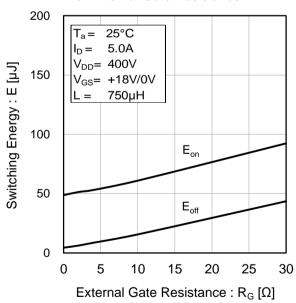


Fig.25 Typical Switching Loss vs. External Gate Resistance



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