

V <sub>DSS</sub>	-30V
R <sub>DS(on)</sub> (Max.)	18.5mΩ
Ι <sub>D</sub>	±9.0A
P <sub>D</sub>	2.0W

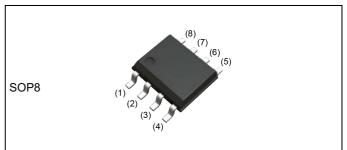
#### Features

- 1) Low on resistance.
- 2) Small Surface Mount Package (SOP8).
- 3) Pb-free lead plating ; RoHS compliant.
- 4) Halogen Free

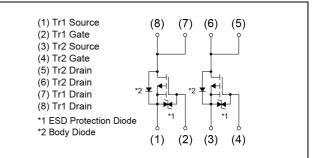
Application

Switching

#### Outline



#### Inner circuit



#### Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	SH8J66

# • Absolute maximum ratings ( $T_a = 25^{\circ}C$ ,unless otherwise specified) <Tr1 and Tr2>

<b>U</b> ( u	• •			
Parameter	Symbol	Value	Unit	
Drain - Source voltage	V <sub>DSS</sub>	-30	V	
Continuous drain current	I <sub>D</sub>	±9.0	Α	
Pulsed drain current	I <sub>DP</sub> *1	±36	Α	
Gate - Source voltage	V <sub>GSS</sub>	±20	V	
	P <sub>D</sub> *2	2.0	14/	
Power dissipation (total)	P <sub>D</sub> *3	1.4	W	
Junction temperature	Tj	150	°C	
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C	

#### •Thermal resistance

Deremeter	Symbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innotion embient (total)	$R_{thJA}^{*2}$	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	$R_{thJA}^{*3}$	-	-	89.2	C/VV

# •Electrical characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Deremeter	Cumph of	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = -1mA$		-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_i}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{i}}  I_{D} = -1mA$		-24.1	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>			-	-1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$I_{GSS}$ $V_{GS} = \pm 20V, V_{DS} = 0V$		-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{GS(th)}$ $V_{DS}$ = -10V, $I_D$ = -1mA		-	-2.5	V	
Gate threshold voltage temperature coefficient			-	3.3	-	mV/°C	
		V <sub>GS</sub> = -10V, I <sub>D</sub> = -9.0A	-	13.5	18.5		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.5A	-	17.5	23.6	mΩ	
		V <sub>GS</sub> = -4.0V, I <sub>D</sub> = -4.5A	-	19.0	24.7	1	
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain		4.9	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>   <sup>*4</sup>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -9A	11	-	-	S	

\*1 Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

- \*2 Mounted on a ceramic board (30×30×0.8mm)
- \*3 Mounted on a FR4 (25×25×0.8mm)

\*4 Pulsed

# •Electrical characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

Deremeter	Cump of	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	3000	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	400	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	400	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq -15 V, V_{GS} = -10 V$	-	20	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -4.5A	-	60	-	
Turn - off delay time	$t_{d(off)}^{*4}$	R <sub>L</sub> = 3.3Ω	-	170	-	ns
Fall time	t <sub>f</sub> *4	R <sub>G</sub> = 10Ω	-	100	-	

# •Gate charge characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

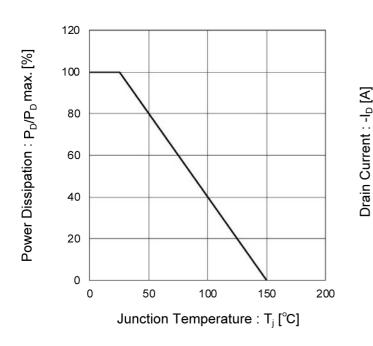
Deremeter	Cumpbed.	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg <sup>*4</sup>		-	35	-	
Gate - Source charge	$Q_{gs}^{*4}$	V <sub>DD</sub> ≃ -15V, I <sub>D</sub> = -9.0A V <sub>GS</sub> = -5.0V	-	9	-	nC
Gate - Drain charge	${\sf Q}_{\sf gd}^{*4}$		-	12	-	

# •Body diode electrical characteristics (Source-Drain) ( $T_a = 25^{\circ}C$ )

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
	Symbol		Min.	Тур.	Max.	UTIIL	
Continuous forward current	۱ <sub>s</sub>	T - 25°0	-	-	-1.6	^	
Pulse forward current	$I_{SP}^{*1}$	T <sub>a</sub> = 25°C	-	-	-36	A	
Forward voltage	$V_{SD}^{*4}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = -9A	-	-	-1.2	V	



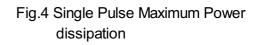


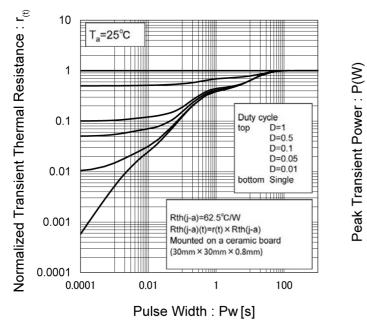
## Fig.1 Power Dissipation Derating Curve

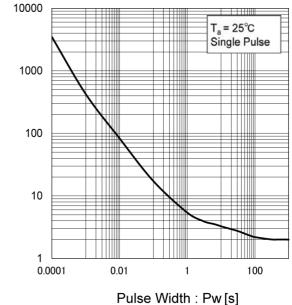
1000 Operation in this area is limited by R<sub>DS(on)</sub> (V<sub>GS</sub>=-10V) 100 Pw=100us 10  $P_w = 10ms$ 1 P<sub>w</sub>=1ms DC operation 0.1 T<sub>a</sub> = 25°C Single Pulse Mounted on a ceramic board (30mm × 30mm × 0.8mm) 1 1 1 1 1 1 1 1 0.01 0.1 10 100 1 Drain - Source Voltage : -V<sub>DS</sub> [V]

# Fig.2 Maximum Safe Operating Area

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width













9

8

7

6

5

4

3

2

1

0

0

#### Fig.5 Typical Output Characteristics(I)

V<sub>GS</sub>= -10V

V<sub>GS</sub>= -4.5V

V<sub>GS</sub>= -4.0V

V<sub>GS</sub>= -3.5V V<sub>GS</sub>= -3.0V T<sub>a</sub>=25°C

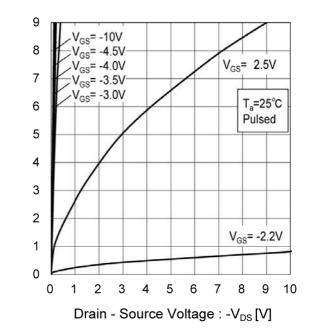
Drain Current : -I<sub>D</sub> [A]

Pulsed

V<sub>GS</sub>= -2.5V

V<sub>GS</sub>= -2.2V

#### Fig.6 Typical Output Characteristics(II)

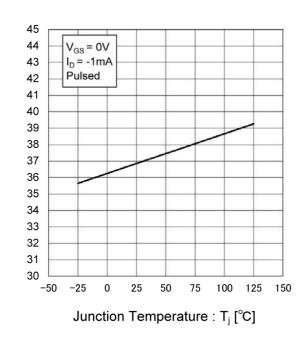


# Drain - Source Voltage : -V<sub>DS</sub> [V]

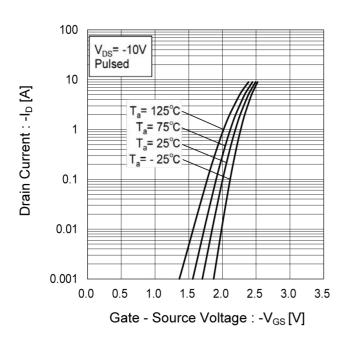
0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 1

# Fig.7 Breakdown Voltage vs. Junction Temperature

Drain-Source Breakdown Voltage : -V<sub>(BR)DSS</sub> [V]







# Fig.8 Typical Transfer Characteristics

Fig.9 Gate Threshold Voltage vs. Junction Temperature

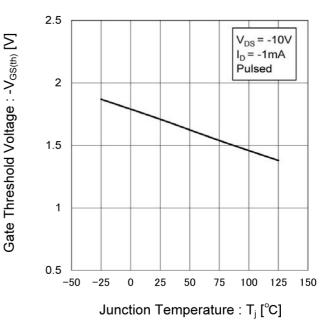
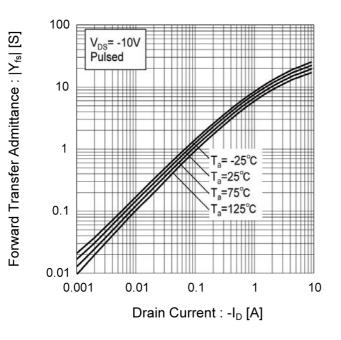


Fig.10 Forward Transfer Admittance vs. Drain Current





10

#### • Electrical characteristic curves

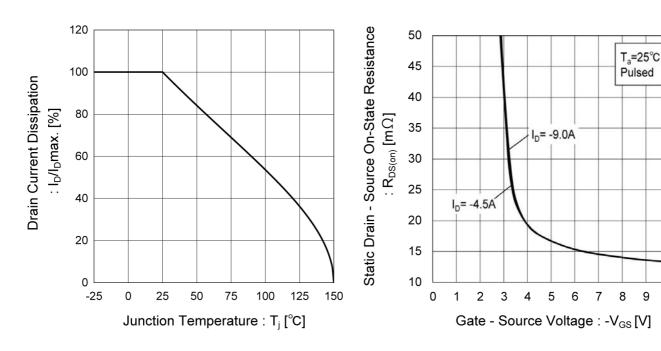


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

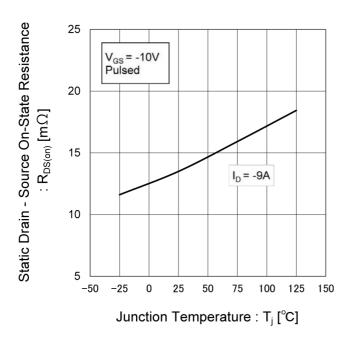
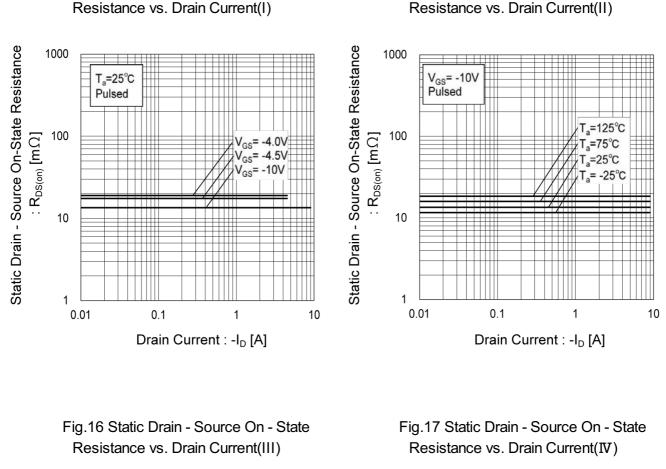
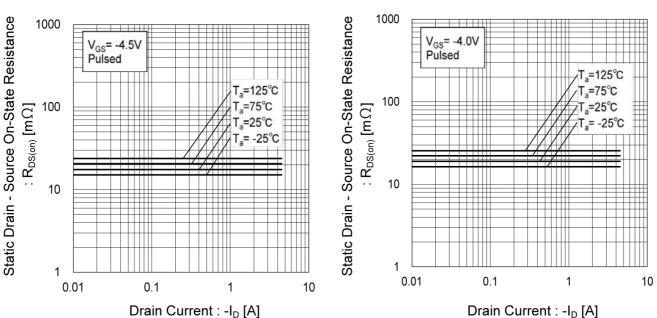


Fig.14 Static Drain - Source On - State



# Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)



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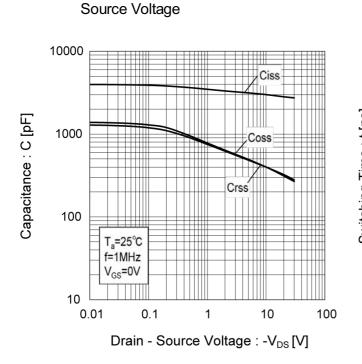


Fig.18 Typical Capacitance vs. Drain -

## Fig.19 Switching Characteristics

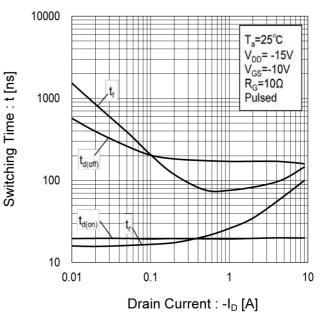


Fig.20 Dynamic Input Characteristics

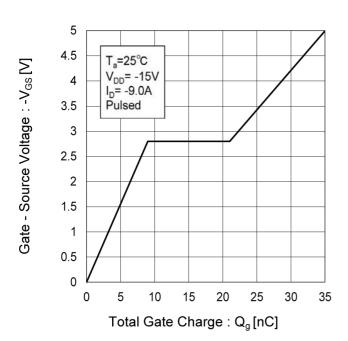
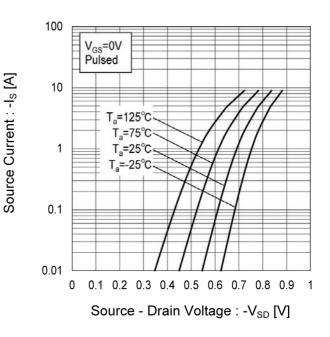
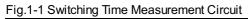


Fig.21 Source Current vs. Source Drain Voltage





## •Measurement circuits <It is the same for the Tr1 and Tr2>



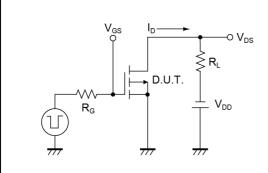


Fig.2-1 Gate Charge Measurement Circuit

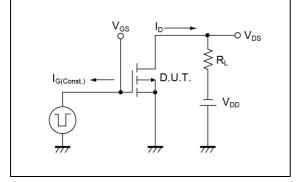
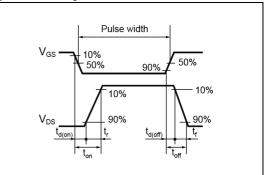
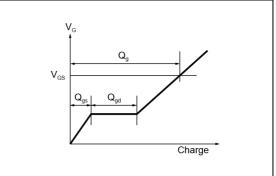


Fig.1-2 Switching Waveforms

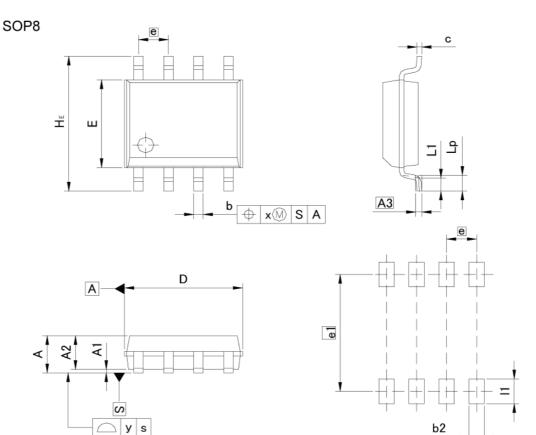








#### Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INC	HES
	MIN	MAX	MIN	MAX
A		1.75		0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.	25	0.0	10
b	0.30	0.50	0.012	0.020
с	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.	27	0.050	
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
x	0.15		0.0	06
У	0.	0.10		04

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2		0.65	<del></del>	0.026
e1	5.	15	0.1	203
11	<del></del>	1.15	<b>2</b> 76	0.045

Dimension in mm/inches



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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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