### 40V Nch+Nch Power MOSFET

V <sub>DSS</sub>	40V
R <sub>DS(on)</sub> (Max.)	85mΩ
I <sub>D</sub>	±5.2A
$P_D$	3W

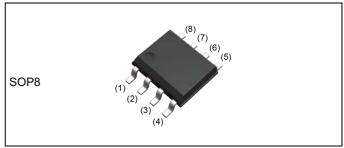
### Features

- 1) Low on resistance
- 2) Small Surface Mount Package
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free

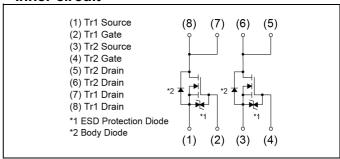
### Application

Switching

### Outline



### •Inner circuit



Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	SH8K25

# ullet **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) <Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{DSS}$	40	V	
Continuous drain current	I <sub>D</sub> *1	±5.2	Α	
Pulsed drain current	I <sub>DP</sub> *2	±8	Α	
Gate - Source voltage	V <sub>GSS</sub>	±12	V	
Avalanche current, single pulse	I <sub>AS</sub> *3	8	Α	
Avalanche energy, single pulse	E <sub>AS</sub> *3	0.48	mJ	
	P <sub>D</sub> *1	3		
Power dissipation (total)	P <sub>D</sub> *4	2	W	
	P <sub>D</sub> *5	1.4		
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C	

### ●Thermal resistance

Downwater	Currele el	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, embient (total)	R <sub>thJA</sub> *4	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R <sub>thJA</sub> *5	-	-	89.2	C/VV

### ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Davanastav	Cumbal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	UTIIL	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	40	-	-	V	
Breakdown voltage	ΔV <sub>(BR)DSS</sub>	I <sub>D</sub> = 1mA	_	27.3	-	mV/°C	
temperature coefficient	$\Delta T_{j}$	referenced to 25°C	-	21.5	-	IIIV/ C	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 40V, V_{GS} = 0V$	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 12V, V_{DS} = 0V$	-	-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta  V_{GS(th)}}{\Delta  T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-4.6	-	mV/°C	
Static drain - source	D *6	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5.2A	-	60	85	0	
on - state resistance	R <sub>DS(on)</sub> *6	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.0A	-	80	112	mΩ	
Gate resistance	$R_G$	f = 1MHz, open drain	-	19	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *6	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A	1.0	-	-	S	

<sup>\*1</sup> Pw ≤ 1s, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L  $\simeq$  10 $\mu$ H, V<sub>DD</sub> = 20V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>i</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*5</sup> Mounted on a Cu board (40×40×0.8mm)

<sup>\*6</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Daramatar	Cymala al	Conditions	Values			Unit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	UIIIL	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	100	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	50	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	15	-		
Turn - on delay time	t <sub>d(on)</sub> *6	V <sub>DD</sub> ≈ 20V,V <sub>GS</sub> = 10V	-	6	-		
Rise time	t <sub>r</sub> *6	I <sub>D</sub> = 2A	-	5	-		
Turn - off delay time	t <sub>d(off)</sub> *6	$R_L = 10\Omega$	-	17	-	ns	
Fall time	<b>t</b> <sub>f</sub> *6	$R_G = 10\Omega$	-	3	-		

# ullet Gate charge characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*6}$		-	1.7	-	
Gate - Source charge	Q <sub>gs</sub> *6	$V_{DD} \approx 20V$ , $I_D = 4A$ $V_{GS} = 5V$	-	0.9	-	nC
Gate - Drain charge	Q <sub>gd</sub> *6	1.00	-	0.3	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

### <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offit
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	1.6	^
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	-	-	8	А
Forward voltage	V <sub>SD</sub> *6	V <sub>GS</sub> = 0V, I <sub>S</sub> = 4A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

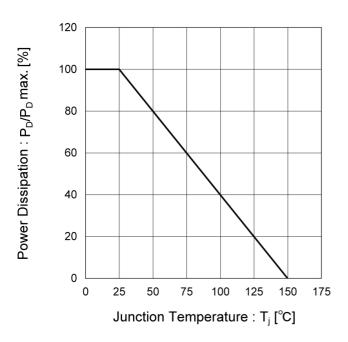
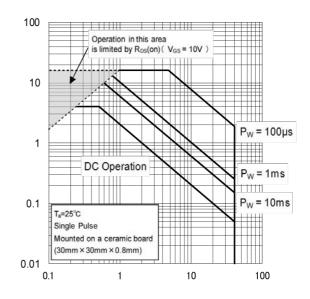


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

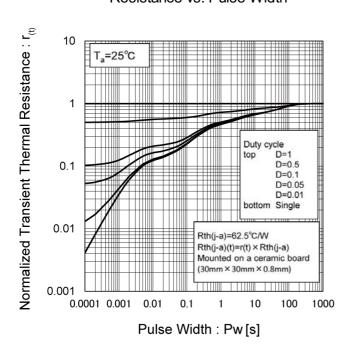
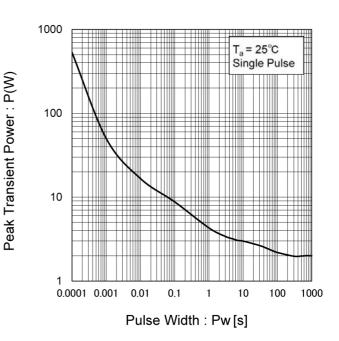


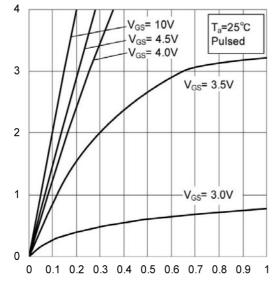
Fig.4 Single Pulse Maximum Power dissipation



Drain Current : I<sub>D</sub> [A]

### • Electrical characteristic curves

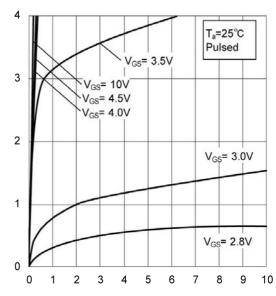
Fig.5 Typical Output Characteristics(I)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

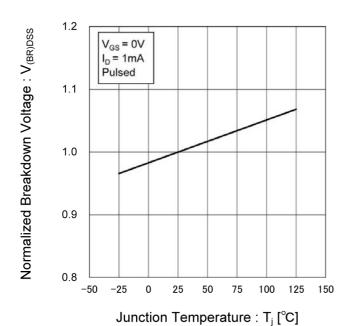


Fig.8 Typical Transfer Characteristics

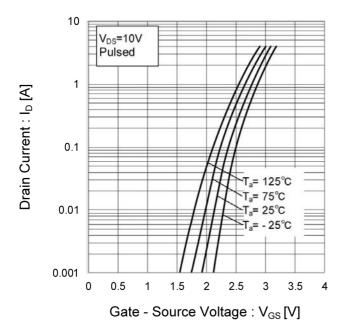
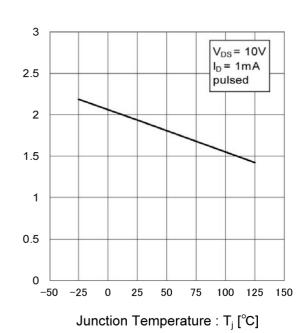


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage: VGS(th) [V]

Fig.10 Forward Transfer Admittance vs.
Drain Current

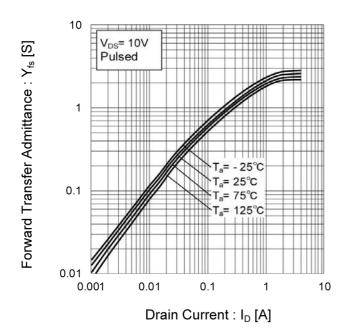


Fig.11 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

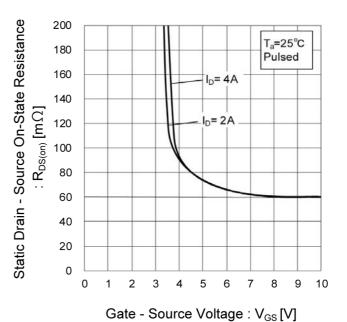


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

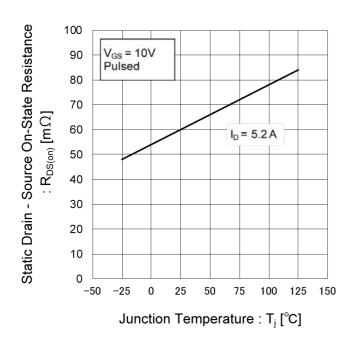


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

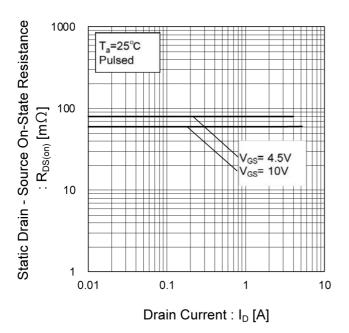
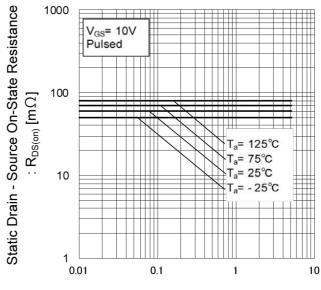


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)



Drain Current: ID [A]

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

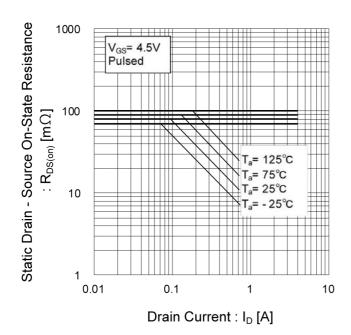
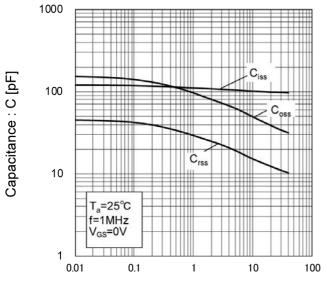


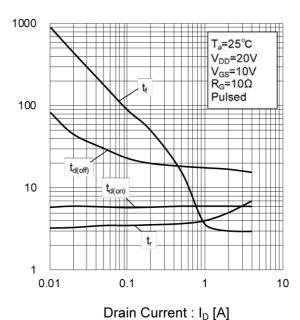
Fig.17 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.18 Switching Characteristics



Source voltage . V<sub>DS</sub>[V]

Switching Time : t [ns]

Fig.19 Dynamic Input Characteristics

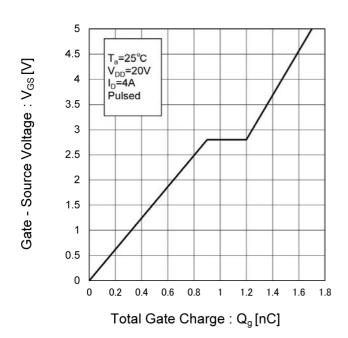
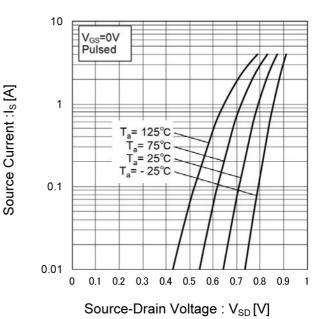


Fig.20 Source Current vs.

Source Drain Voltage



### • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

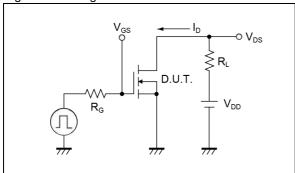


Fig.2-1 Gate Charge Measurement Circuit

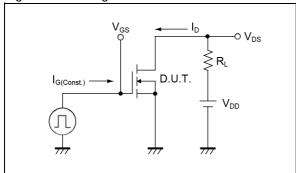


Fig.3-1 Avalanche Measurement Circuit

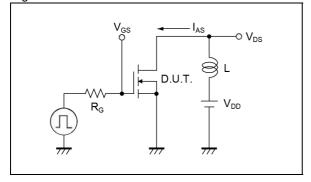


Fig.1-2 Switching Waveforms

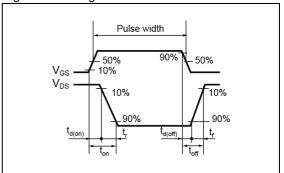


Fig.2-2 Gate Charge Waveform

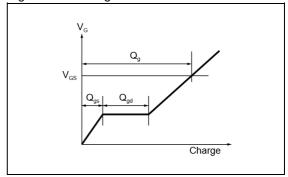
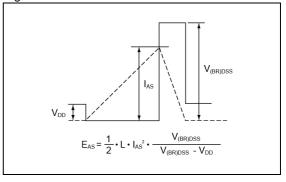
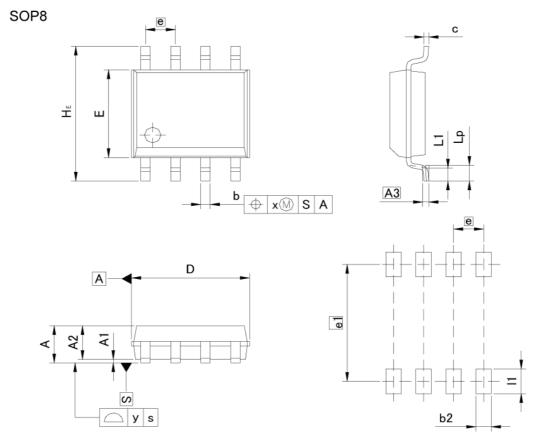


Fig.3-2 Avalanche Waveform



### Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
Α	<u></u>	1.75	= 1	0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.	25	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.	27	0.050	
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
х	0.	15	0.006	
У	0.10		0.0	04
DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX

 DIM
 MIN
 MAX
 MIN
 MAX

 b2
 0.65
 0.026

 e1
 5.15
 0.203

 l1
 1.15
 0.045

Dimension in mm/inches



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  - [d] the Products are exposed to high Electrostatic
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