# 35V Nch+Nch Power MOSFET

V <sub>DSS</sub>	35V
R <sub>DS(on)</sub> (Max.)	58mΩ
I <sub>D</sub>	±4.0A
P <sub>D</sub>	2.0W

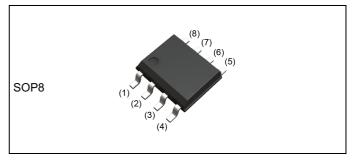
### Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free

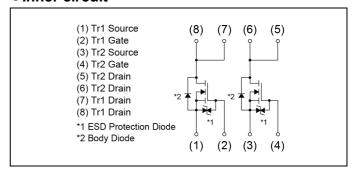
# Application

Switching

### Outline



## ●Inner circuit



# Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Basic ordering unit (pcs)	2500
	Taping code	ТВ
	Marking	SH8K51

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{DSS}$	35	V	
Continuous drain current	I <sub>D</sub>	±4.0	А	
Pulsed drain current	I <sub>DP</sub> *1	±16	А	
Gate - Source voltage	$V_{GSS}$	±20	V	
Device discipation (total)	$P_{D}^{*2}$	2.0	W	
Power dissipation (total)	P <sub>D</sub> *3	1.4		
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C	

## ●Thermal resistance

Doromotor	Cymab al	Values			l limit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registence, junction, ambient (total)	R <sub>thJA</sub> *2	ı	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R <sub>thJA</sub> *3	1	-	89.2	C/VV

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Damanatan	0	0	Values			l limit	
Parameter Symbol Conditions		Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	35	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	<del></del>		37.3	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 35V, V <sub>GS</sub> = 0V		-	1	μА	
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V		-	±10	μА	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA		-	2.8	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-3.8	-	mV/°C	
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.0A	-	42	58		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.0A	-	60	84	mΩ	
on - state resistance		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 4.0A	-	70	98	-	
Gate resistance	$R_G$	f = 1MHz, open drain	-	6.3	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4.0A	2.5	-	-	S	

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Parameter	Cumbal	Conditions	Values			Unit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	300	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	85	-	pF	
Reverse transfer capacitance C <sub>rs</sub>		f = 1MHz	-	40	-		
Turn - on delay time	$t_{d(on)}^{*4}$	V <sub>DD</sub> ≈ 15V,V <sub>GS</sub> = 10V	-	7	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 2.0A	-	6	-		
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 7.5\Omega$	-	23	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	5	-		

# ullet Gate charge characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*4}$		-	4.0	5.6	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \approx 15V, I_{D} = 4.0A$ $V_{GS} = 5V$	-	1.6	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	1.00	-	1.5	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

## <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	1.6	Δ.	
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	16	Α	
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 4.0A	-	-	1.2	V	

Fig.1 Power Dissipation Derating Curve

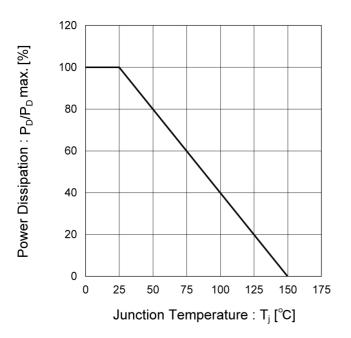
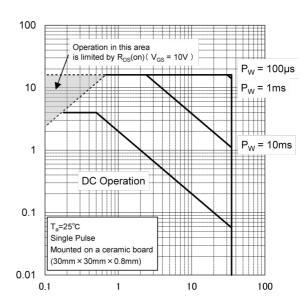


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage: V<sub>DS</sub>[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

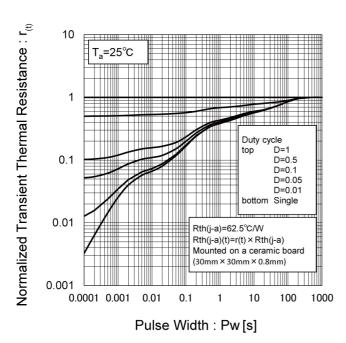
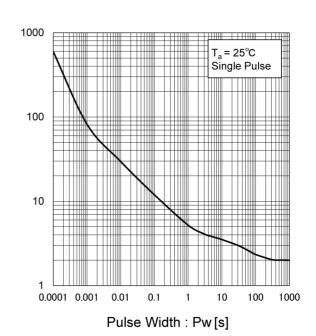


Fig.4 Single Pulse Maximum Power dissipation

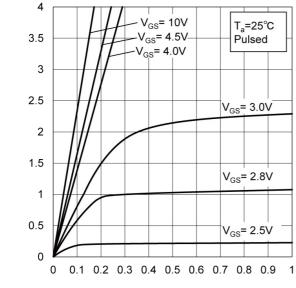


Peak Transient Power : P(W)

Drain Current : I<sub>D</sub> [A]

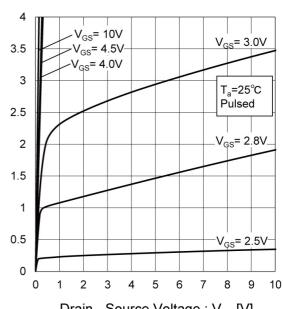
#### Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs. **Junction Temperature** 

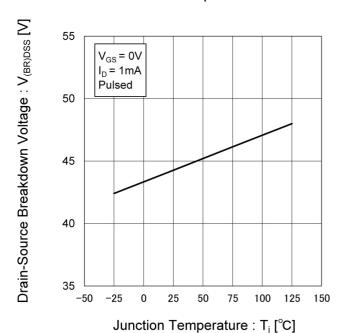


Fig.8 Typical Transfer Characteristics

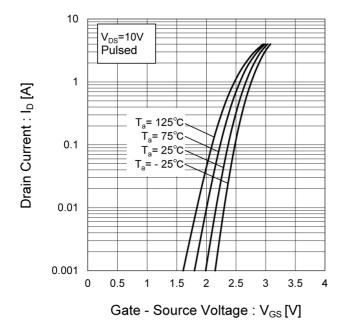


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

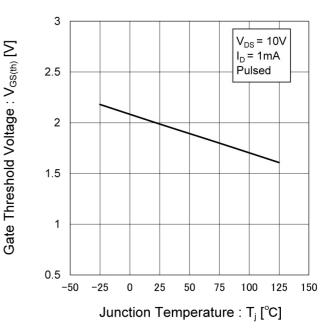


Fig.10 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

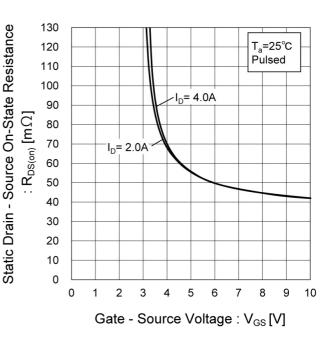


Fig.12 Static Drain - Source On - State Resistance vs. Junction Temperature

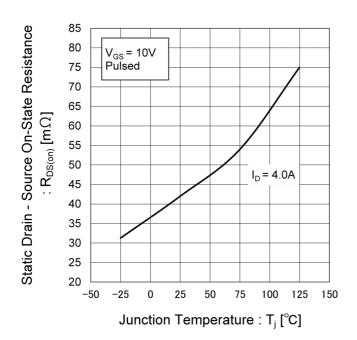


Fig.13 Static Drain - Source On - State
Resistance vs. Drain Current (I)

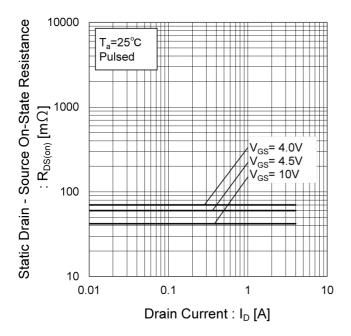


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (II)

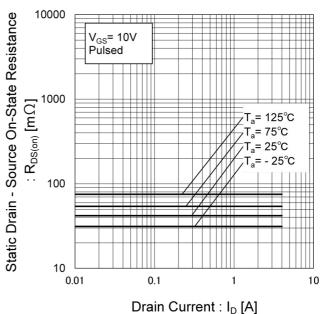


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (III)

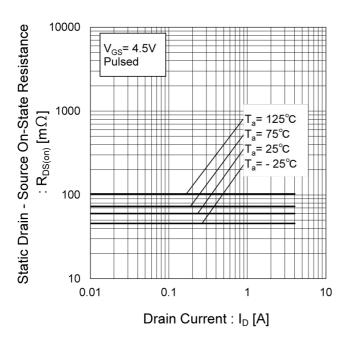


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (IV)

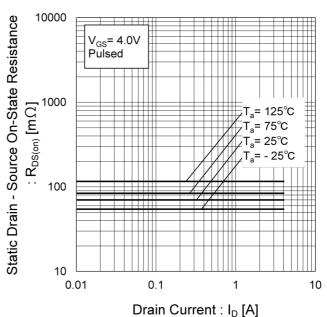


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

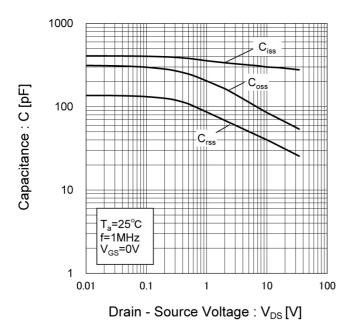


Fig.18 Switching Characteristics

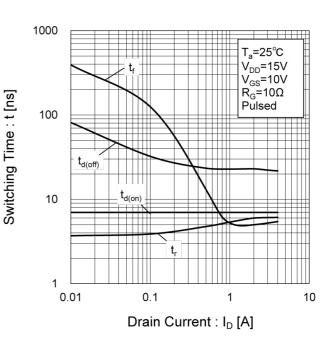


Fig.19 Dynamic Input Characteristics

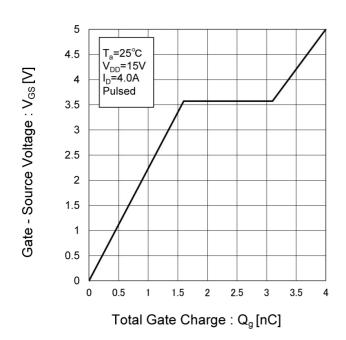
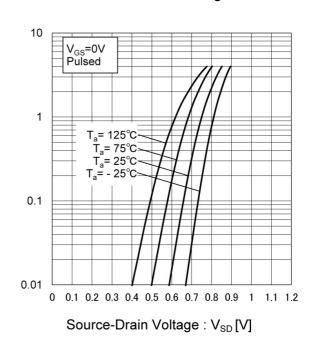


Fig.20 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

## • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

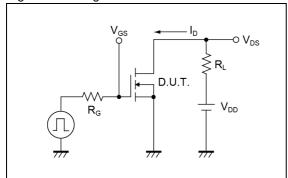


Fig.2-1 Gate Charge Measurement Circuit

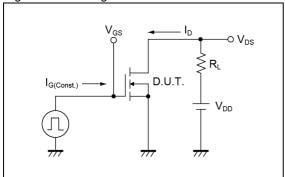


Fig.1-2 Switching Waveforms

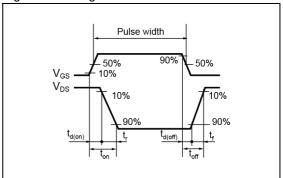
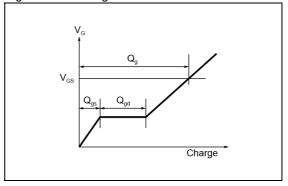
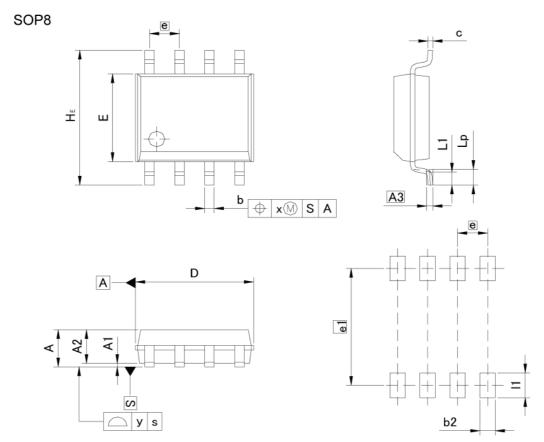


Fig.2-2 Gate Charge Waveform



## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	-	1.75	-	0.069	
A1	0.	15	0.0	06	
A2	1.40	1.60	0.055	0.063	
A3	0.2	25	0.0	10	
b	0.30	0.50	0.012	0.020	
С	0.10	0.30	0.004	0.012	
D	4.80	5.20	0.189	0.205	
Е	3.75	4.05	0.148	0.159	
е	1.2	1.27		50	
HE	5.70	6.30	0.224	0.248	
L1	0.40	0.60	0.016	0.024	
Lp	0.65	0.85	0.026	0.033	
х	0.15		0.006		
у 0.10			0.004		
DIM	MILIMETERS		INC	HES	
DIM	MIN	MAX	MIN	MAX	

	DIM	MILIMETERS		INCHES		
		MIN	MAX	MIN	MAX	
	b2	-	0.65	-	0.026	
	e1	5.	15	0.2	03	
	l1	-,7	1.15	- 1	0.045	

Dimension in mm/inches



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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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