100V Nch+Nch Power MOSFET

V _{DSS}	100V
R _{DS(on)} (Max.)	170mΩ
I _D	±3.0A
P _D	2.0W

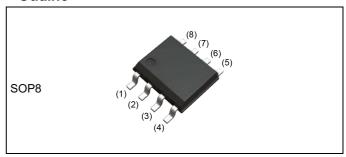
Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free

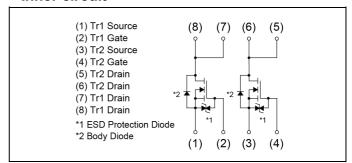
Application

Switching

Outline



•Inner circuit



Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	SH8K52

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	100	V
Continuous drain current	I _D	±3.0	Α
Pulsed drain current	I _{DP} *1	±12	Α
Gate - Source voltage	V_{GSS}	±20	V
Avalanche current, single pulse	I _{AS} *2	3.0	А
Avalanche energy, single pulse	E _{AS} *2	6.5	mJ
Douge discipation (total)	P _D *3	2.0	١٨/
Power dissipation (total)	P _D *4	1.4	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Downwater	Currele el	Values			I India
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, embient (total)	R _{thJA} *3	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R _{thJA} *4	-	-	89.2	C/VV

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Parameter	Cumabal	Conditions	Values			Linit
Parameter Symbol Conditions		Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	100	-	-	V
Breakdown voltage	ΔV _{(BR)DSS}	I _D = 1mA		116.9	-	mV/°C
temperature coefficient	ΔT _j	referenced to 25°C	-	116.9		mv/ C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μА
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±10	μА
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA	1.0	-	2.5	V
Gate threshold voltage	$\Delta V_{GS(th)}$	I _D = 1mA		2.6		\ //º0
temperature coefficient	ΔT_j	referenced to 25°C	-	-3.6	-	mV/°C
		V _{GS} = 10V, I _D = 3A	-	120	170	
Static drain - source on - state resistance	R _{DS(on)} *5	V _{GS} = 4.5V, I _D = 3A	-	130	180	mΩ
on state recipianies		V _{GS} = 4.0V, I _D = 3A	-	135	190	
Gate resistance	R_G	f = 1MHz, open drain	-	6.9	-	Ω
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 10V, I _D = 3A	3.5	-	-	S

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*2} L \simeq 1mH, V_{DD} = 50V, R_G = 25 Ω , STARTING T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*3} Mounted on a ceramic board (30×30×0.8mm)

^{*4} Mounted on a Cu board (25×25×0.8mm)

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Daramatar	Cymala al	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	UIIIL	
Input capacitance	C _{iss}	V _{GS} = 0V	-	610	-		
Output capacitance	C _{oss}	C_{oss} $V_{DS} = 25V$		55	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	35	-		
Turn - on delay time	t _{d(on)} *5	V _{DD} ≈ 50V,V _{GS} = 10V	-	13	-		
Rise time	t _r *5	I _D = 1.5A	-	13	-		
Turn - off delay time	t _{d(off)} *5	$R_L = 33\Omega$	-	50	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	14	-		

ullet Gate charge characteristics (T_a = 25°C) <Tr1 and Tr2>

Parameter	Cumbal	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*5}		-	8.5	-	
Gate - Source charge	Q _{gs} *5	$V_{DD} \approx 50V$, $I_D = 3A$ $V_{GS} = 5V$	-	1.8	-	nC
Gate - Drain charge	Q _{gd} *5	1.00	-	3.5	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Parameter	Cympol	Conditions	Values			Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T - 25°C	-	-	1.0	^
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	12	Α
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 3A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

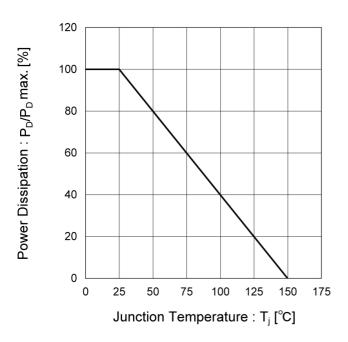
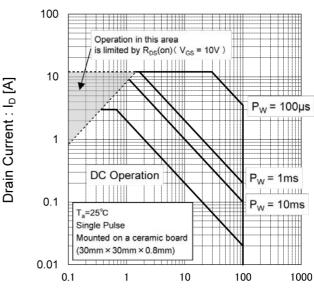


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

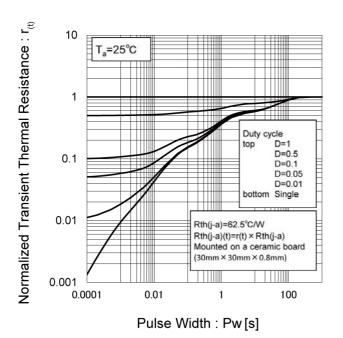
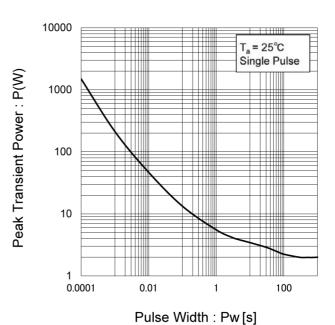


Fig.4 Single Pulse Maximum Power dissipation



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Fig.5 Typical Output Characteristics(I)

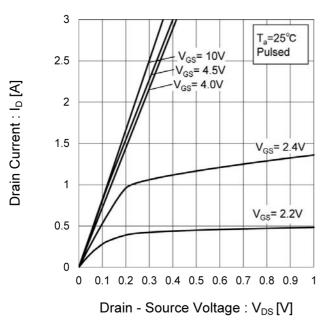
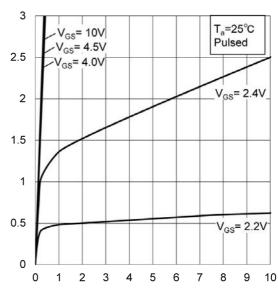


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

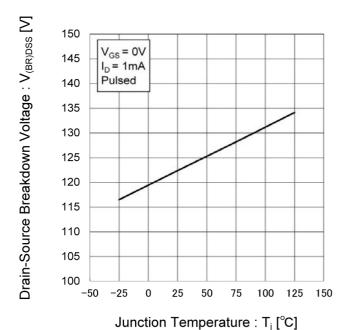


Fig.8 Typical Transfer Characteristics

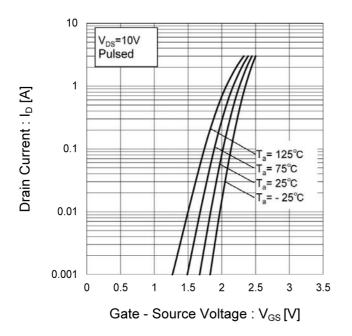
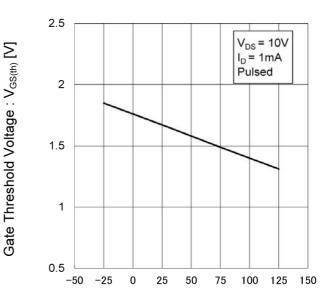


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Junction Temperature : T_j [°C]

Fig.10 Forward Transfer Admittance vs.
Drain Current

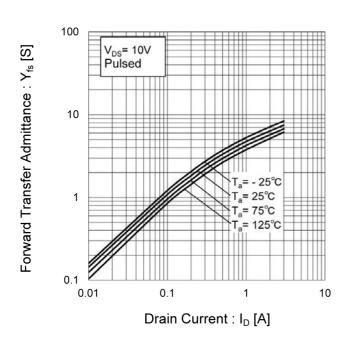


Fig.11 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I_D/I_Dmax. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T_j [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

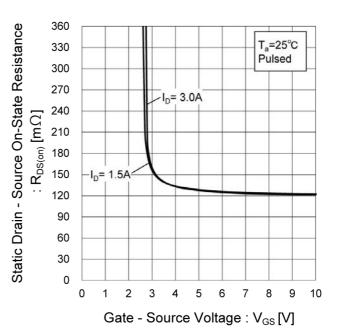
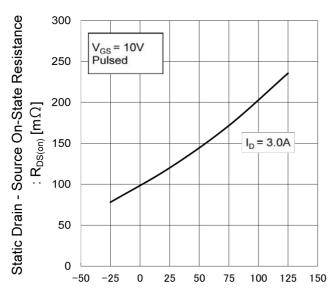


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]

Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

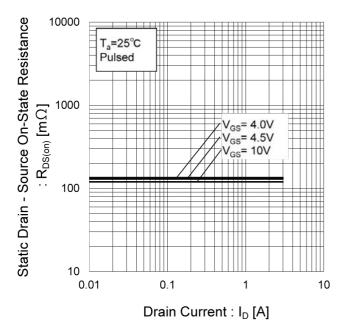


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

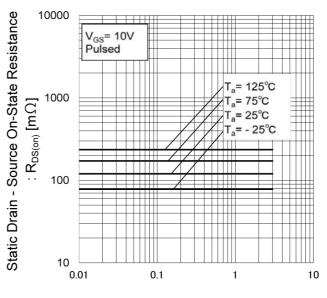


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

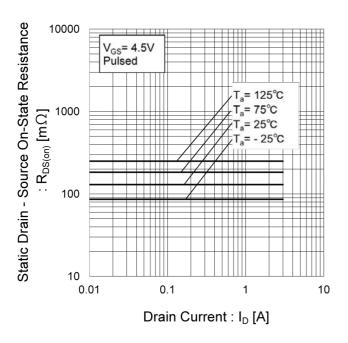
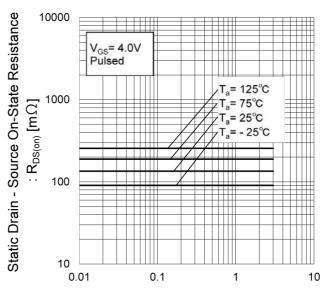


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

Drain Current: ID [A]



Drain Current: I_D [A]

Fig.18 Typical Capacitance vs.

Drain - Source Voltage

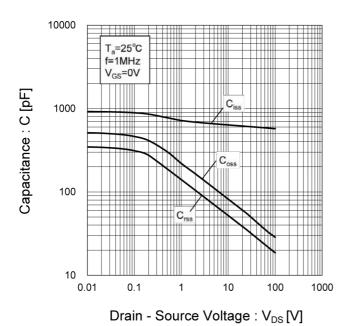


Fig.19 Switching Characteristics

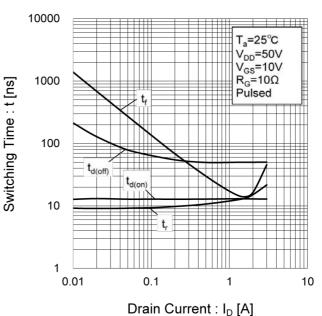


Fig.20 Dynamic Input Characteristics

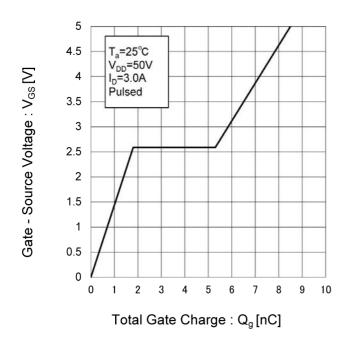
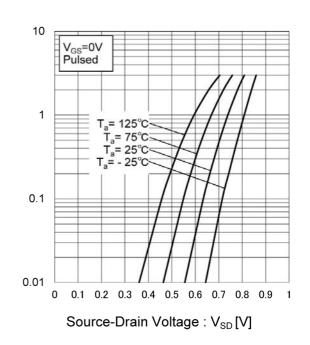


Fig.21 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

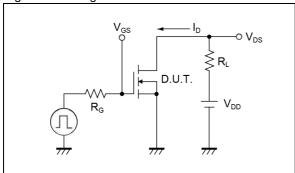


Fig.2-1 Gate Charge Measurement Circuit

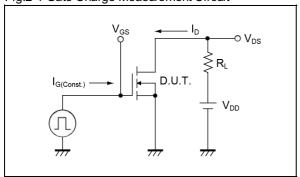


Fig.3-1 Avalanche Measurement Circuit

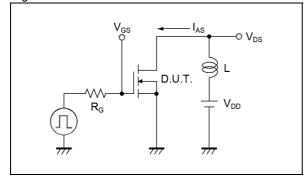


Fig.1-2 Switching Waveforms

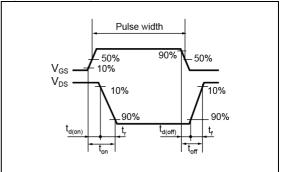


Fig.2-2 Gate Charge Waveform

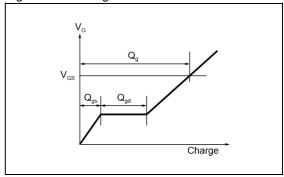
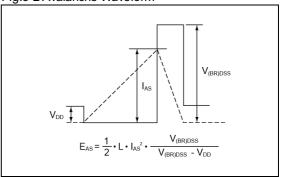
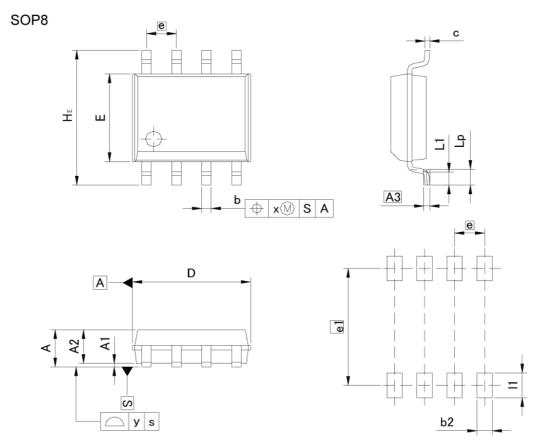


Fig.3-2 Avalanche Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	<u></u>	1.75	=	0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.	25	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.	27	0.050	
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
х	0.	15	0.0	06
у	0.10		0.0	04
DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX

 DIM
 MILIMETERS
 INCHES

 MIN
 MAX
 MIN
 MAX

 b2
 0.65
 0.026

 e1
 5.15
 0.203

 l1
 1.15
 0.045

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCTI
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PGA-E Rev.004

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