

# SP8J66FRA

## -30V Pch +Pch Power MOSFET

V <sub>DSS</sub>	-30V
R <sub>DS(on)</sub> (Max.)	18.5mΩ
I <sub>D</sub>	±9.0A
P <sub>D</sub>	2.0W

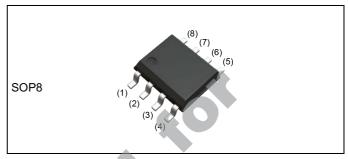
## Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating; RoHS compliant
- 4) AEC-Q101 Qualified

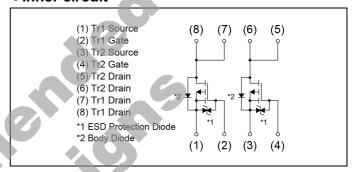
# Application

Switching

### Outline



## •Inner circuit



# Packaging specifications

● Packag	ging specifications	
	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	SP8J66

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-30	V
Continuous drain current	I <sub>D</sub>	±9.0	Α
Pulsed drain current	I <sub>DP</sub> *1	±36	Α
Gate - Source voltage	$V_{GSS}$	±20	V
Dower dissipation (total)	P <sub>D</sub> *2	2.0	W
Power dissipation (total)	P <sub>D</sub> *3	1.4	VV
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

# ●Thermal resistance

Downwater	Cumbal	Values			I India
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, embient (total)	$R_{thJA}^{*2}$	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	$R_{thJA}^{*3}$	-	-	89.2	C/VV

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

		0 18	Values			11.76
Parameter	Symbol	Conditions	Min. Typ.		Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\DeltaV_{(BR)DSS}}{\DeltaT_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-24.1	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = -30V, V_{GS} = 0V$		-	-1	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	-	-	±10	μA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	-	-2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{\text{GS(th)}}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	1	3.3	ı	mV/°C
		$V_{GS}$ = -10V, $I_D$ = -9.0A	1	13.5	18.5	
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	$V_{GS}$ = -4.5V, $I_D$ = -4.5A	1	17.5	23.6	mΩ
on state resistance		$V_{GS}$ = -4.0V, $I_D$ = -4.5A	-	19.0	24.7	
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	4.9	-	Ω
Forward Transfer Admittance	Transfer $ Y_{f_0} ^{*4}$ $ Y_{f_0}  = -10 \text{V}$ $ f_0  = -9 \text{A}$		11	-	-	S

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Daramatar	Cymahal	Conditions	Values			Unit
Parameter	Symbol	ymbol Conditions		Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	3000	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	400	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	400	-	
Turn - on delay time	t <sub>d(on)</sub> *4	V <sub>DD</sub> ≃ -15V,V <sub>GS</sub> = -10V	-	20	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -4.5A		60	-	
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 3.3\Omega$	-0	170	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	<b>S</b> -	100	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Parameter	Symbol	Conditions		Values		Unit
raiametei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*4}$		-	35	-	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \simeq -15V, I_{D} = -9A$ $V_{GS} = -5V$	-	9	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	133	-	12	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

<Tr1 and Tr2>

Darameter	Cymhol	Conditions	Values			l limit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	Is	T - 25°C	-	-	-1.6	_
Pulse forward current	I <sub>SP</sub> *1	<sup>∤</sup> T <sub>a</sub> = 25°C	-	-	-36	A
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = -9A	-	-	-1.2	V

Fig.1 Power Dissipation Derating Curve

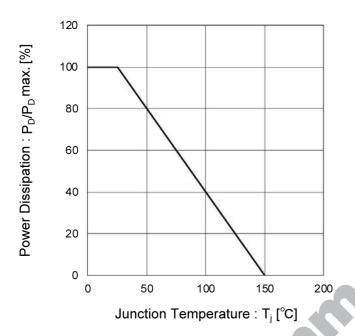
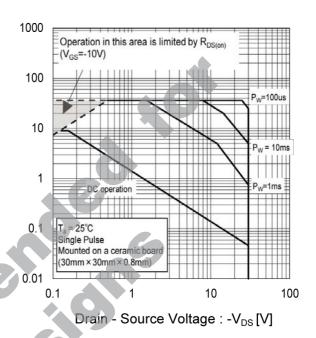


Fig.2 Maximum Safe Operating Area



Drain Current: -l<sub>D</sub> [A]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

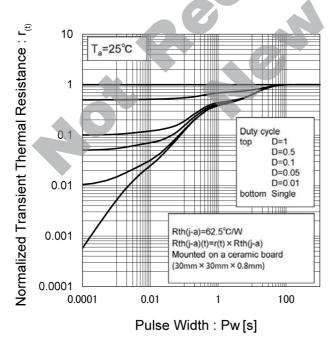


Fig.4 Single Pulse Maximum Power dissipation

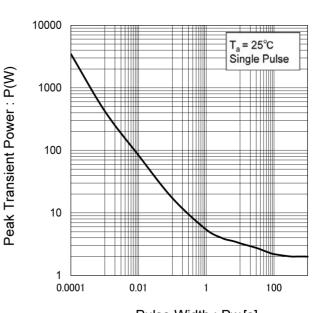


Fig.5 Typical Output Characteristics(I)

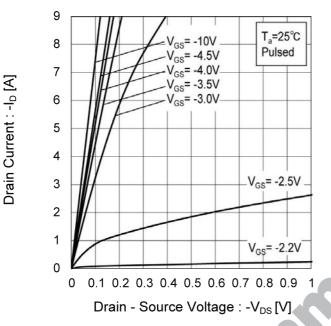
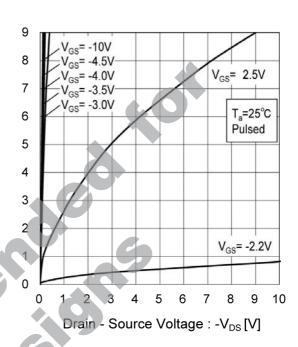


Fig.6 Typical Output Characteristics(II)



Drain Current : -I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs. Junction Temperature

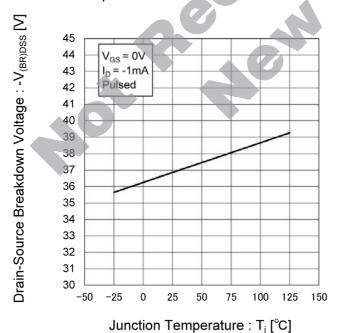


Fig.8 Typical Transfer Characteristics

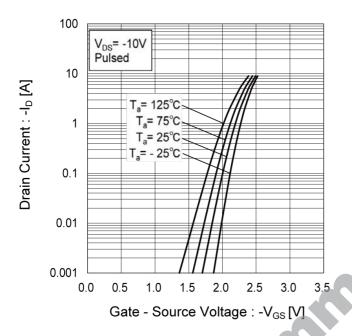
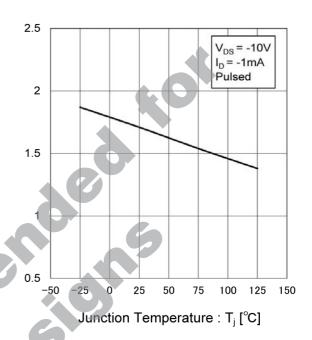


Fig.9 Gate Threshold Voltage vs. Junction Temperature



Gate Threshold Voltage: -V<sub>GS(th)</sub> [V]

Fig.10 Forward Transfer Admittance vs. Drain Current

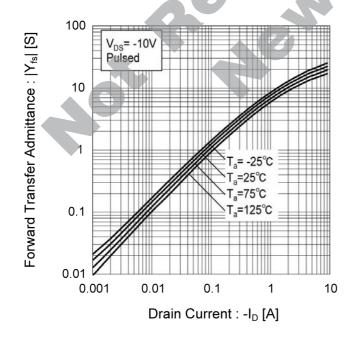


Fig.11 Drain Current Derating Curve

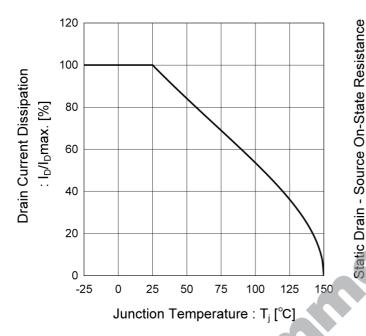


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

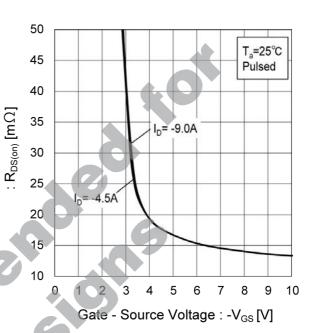


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

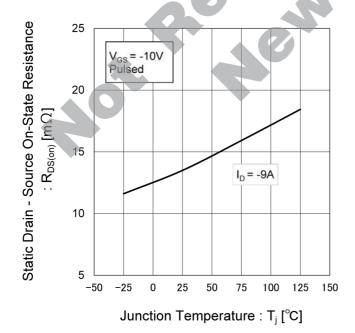


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

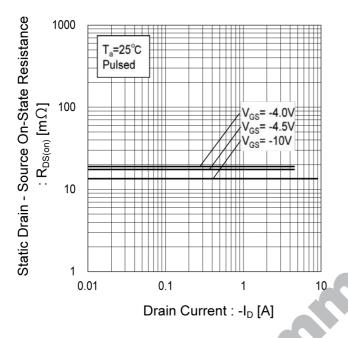


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

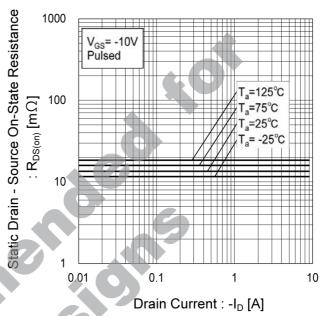


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

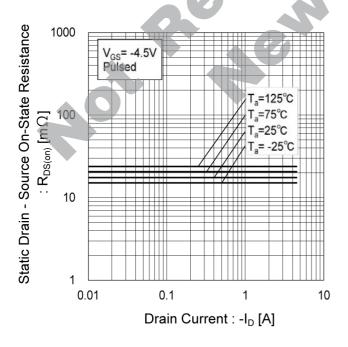


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

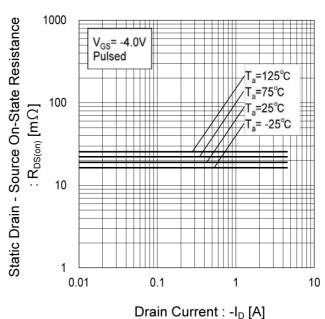


Fig.18 Typical Capacitance vs. Drain - Source Voltage

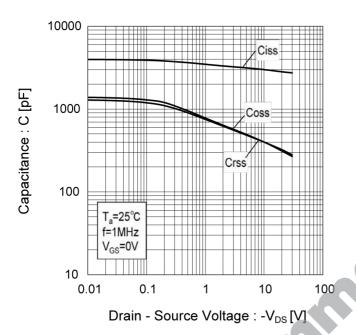


Fig.19 Switching Characteristics

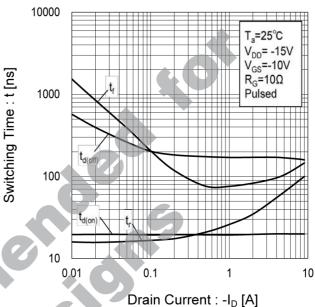


Fig.20 Dynamic Input Characteristics

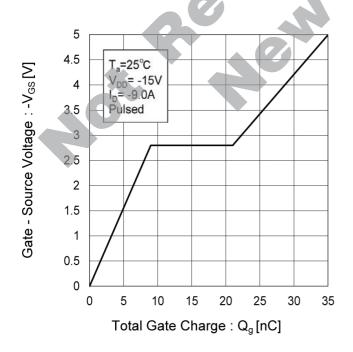
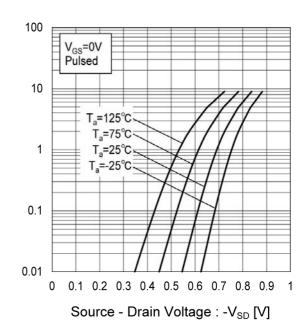


Fig.21 Source Current vs. Source Drain Voltage



Source Current : -Is [A]

## • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

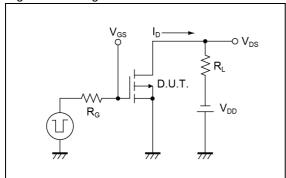


Fig.2-1 Gate Charge Measurement Circuit

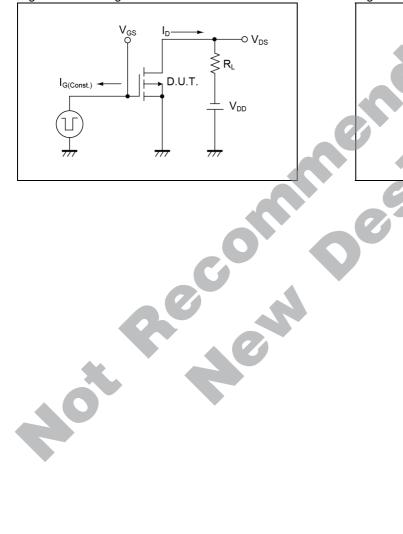


Fig.1-2 Switching Waveforms

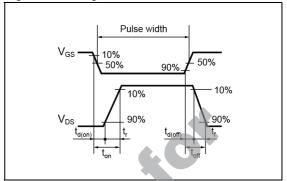
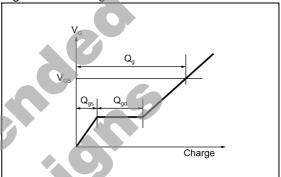
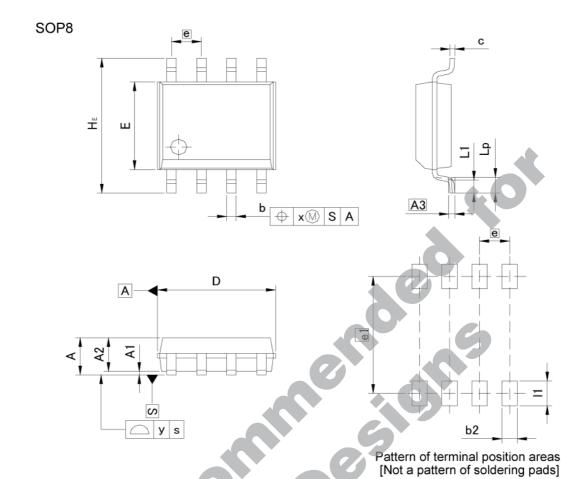


Fig.2-2 Gate Charge Waveform





# Dimensions



DIM	MILIME	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	B) (	1.75		0.069
A1	0.	5	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.2	25	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.2	27	0.0	50
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
х	0.15		0.0	06
у	0.10		0.0	04

DIM	MILIME	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	<del>-</del> 2	0.65	<del>7</del> %	0.026
e1	5.	15	0.2	203
11	T-2	1.15	75°	0.045

Dimension in mm/inches

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1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1					
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CLASSIV	CLASSIII	CLASSⅢ	CLASSIII		

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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