

|                    |       |
|--------------------|-------|
| $V_{DSS}$          | 60V   |
| $R_{DS(on)}(Max.)$ | 65mΩ  |
| $I_D$              | ±4.5A |
| $P_D$              | 2.0W  |

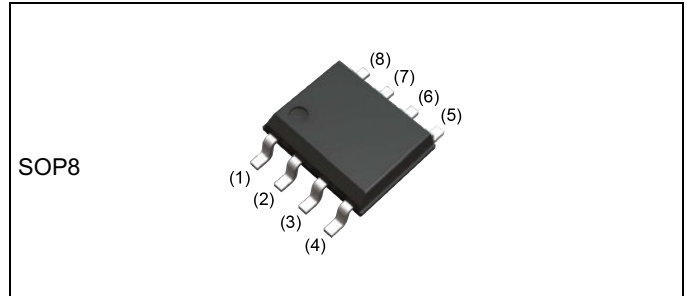
●Features

- 1) Low on - resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free lead plating ; RoHS compliant
- 4) AEC-Q101 Qualified

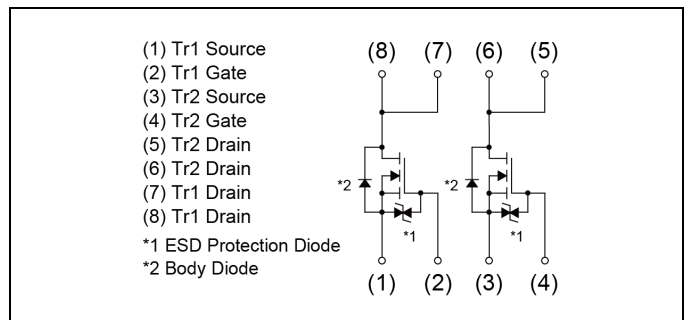
●Application

Switching

●Outline



●Inner circuit



●Packaging specifications

| Type                      | Packing        | Embossed Tape |
|---------------------------|----------------|---------------|
|                           | Reel size (mm) | 330           |
| Tape width (mm)           | 12             |               |
| Basic ordering unit (pcs) | 2500           |               |
| Taping code               | TB             |               |
| Marking                   | SP8K32         |               |

●Absolute maximum ratings ( $T_a = 25^\circ C$  ,unless otherwise specified) <Tr1 and Tr2>

| Parameter  | Symbol        | Value       | Unit |
|--|---------------|-------------|------|
| Drain - Source voltage                           | $V_{DSS}$     | 60          | V    |
| Continuous drain current                         | $I_D$         | ±4.5        | A    |
| Pulsed drain current                             | $I_{DP}^{*1}$ | ±18         | A    |
| Gate - Source voltage                            | $V_{GSS}$     | ±20         | V    |
| Power dissipation (total)                        | $P_D^{*2}$    | 2.0         | W    |
|  | $P_D^{*3}$    | 1.4         |      |
| Junction temperature                             | $T_j$         | 150         | °C   |
| Operating junction and storage temperature range | $T_{stg}$     | -55 to +150 | °C   |

### ● Thermal resistance

| Parameter                                      | Symbol          | Values |      |      | Unit |
|--|-----------------|--------|------|------|------|
|  |                 | Min.   | Typ. | Max. |      |
| Thermal resistance, junction - ambient (total) | $R_{thJA}^{*2}$ | -      | -    | 62.5 | °C/W |
|  | $R_{thJA}^{*3}$ | -      | -    | 89.2 |      |

### ● Electrical characteristics ( $T_a = 25^\circ\text{C}$ ) <Tr1 and Tr2>

| Parameter                                      | Symbol                                  | Conditions                                      | Values |      |      | Unit  |
|--|---|---|--------|------|------|-------|
|  |   |   | Min.   | Typ. | Max. |       |
| Drain - Source breakdown voltage               | $V_{(BR)DSS}$                           | $V_{GS} = 0V, I_D = 1mA$                        | 60     | -    | -    | V     |
| Breakdown voltage temperature coefficient      | $\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$ | $I_D = 1mA$<br>referenced to $25^\circ\text{C}$ | -      | 63.7 | -    | mV/°C |
| Zero gate voltage drain current                | $I_{DSS}$                               | $V_{DS} = 60V, V_{GS} = 0V$                     | -      | -    | 1    | μA    |
| Gate - Source leakage current                  | $I_{GSS}$                               | $V_{DS} = 0V, V_{GS} = \pm 20V$                 | -      | -    | ±10  | μA    |
| Gate threshold voltage                         | $V_{GS(th)}$                            | $V_{DS} = 10V, I_D = 1mA$                       | 1.0    | -    | 2.5  | V     |
| Gate threshold voltage temperature coefficient | $\frac{\Delta V_{GS(th)}}{\Delta T_j}$  | $I_D = 1mA$<br>referenced to $25^\circ\text{C}$ | -      | -2.8 | -    | mV/°C |
| Static drain - source on - state resistance    | $R_{DS(on)}^{*4}$                       | $V_{GS} = 10V, I_D = 4.5A$                      | -      | 46   | 65   | mΩ    |
|  |   | $V_{GS} = 4.5V, I_D = 4.5A$                     | -      | 52   | 73   |       |
|  |   | $V_{GS} = 4.0V, I_D = 4.5A$                     | -      | 55   | 77   |       |
| Gate resistance                                | $R_G$                                   | $f = 1MHz, \text{open drain}$                   | -      | 5.9  | -    | Ω     |
| Forward Transfer Admittance                    | $ Y_{fs} ^{*4}$                         | $V_{DS} = 10V, I_D = 4.5A$                      | 4.0    | -    | -    | S     |

\*1  $P_w \leq 10\mu s$ , Duty cycle  $\leq 1\%$

\*2 Mounted on a ceramic board (30×30×0.8mm)

\*3 Mounted on a FR4 (25×25×0.8mm)

\*4 Pulsed

**●Electrical characteristics** ( $T_a = 25^\circ\text{C}$ ) <Tr1 and Tr2>

| Parameter                    | Symbol            | Conditions                         | Values |      |      | Unit |
|------------------------------|-------------------|------------------------------------|--------|------|------|------|
|                              |                   |                                    | Min.   | Typ. | Max. |      |
| Input capacitance            | $C_{iss}$         | $V_{GS} = 0V$                      | -      | 500  | -    | pF   |
| Output capacitance           | $C_{oss}$         | $V_{DS} = 10V$                     | -      | 120  | -    |      |
| Reverse transfer capacitance | $C_{rss}$         | $f = 1\text{MHz}$                  | -      | 55   | -    |      |
| Turn - on delay time         | $t_{d(on)}^{*4}$  | $V_{DD} \approx 30V, V_{GS} = 10V$ | -      | 12   | -    | ns   |
| Rise time                    | $t_r^{*4}$        | $I_D = 2.3A$                       | -      | 18   | -    |      |
| Turn - off delay time        | $t_{d(off)}^{*4}$ | $R_L = 13\Omega$                   | -      | 40   | -    |      |
| Fall time                    | $t_f^{*4}$        | $R_G = 10\Omega$                   | -      | 13   | -    |      |

**●Gate charge characteristics** ( $T_a = 25^\circ\text{C}$ ) <Tr1 and Tr2>

| Parameter            | Symbol        | Conditions  | Values |      |      | Unit |
|----------------------|---------------|---|--------|------|------|------|
|                      |               |   | Min.   | Typ. | Max. |      |
| Total gate charge    | $Q_g^{*4}$    | $V_{DD} \approx 30V, I_D = 4.5A$<br>$V_{GS} = 5V$ | -      | 7.0  | 10   | nC   |
| Gate - Source charge | $Q_{gs}^{*4}$ |   | -      | 1.6  | -    |      |
| Gate - Drain charge  | $Q_{gd}^{*4}$ |   | -      | 2.5  | -    |      |

**●Body diode electrical characteristics** (Source-Drain) ( $T_a = 25^\circ\text{C}$ )

&lt;Tr1 and Tr2&gt;

| Parameter                  | Symbol        | Conditions                | Values |      |      | Unit |
|----------------------------|---------------|---------------------------|--------|------|------|------|
|                            |               |                           | Min.   | Typ. | Max. |      |
| Continuous forward current | $I_S$         | $T_a = 25^\circ\text{C}$  | -      | -    | 1.0  | A    |
| Pulse forward current      | $I_{SP}^{*1}$ |                           | -      | -    | 18   |      |
| Forward voltage            | $V_{SD}^{*4}$ | $V_{GS} = 0V, I_S = 4.5A$ | -      | -    | 1.2  | V    |

● Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve



Fig.2 Maximum Safe Operating Area

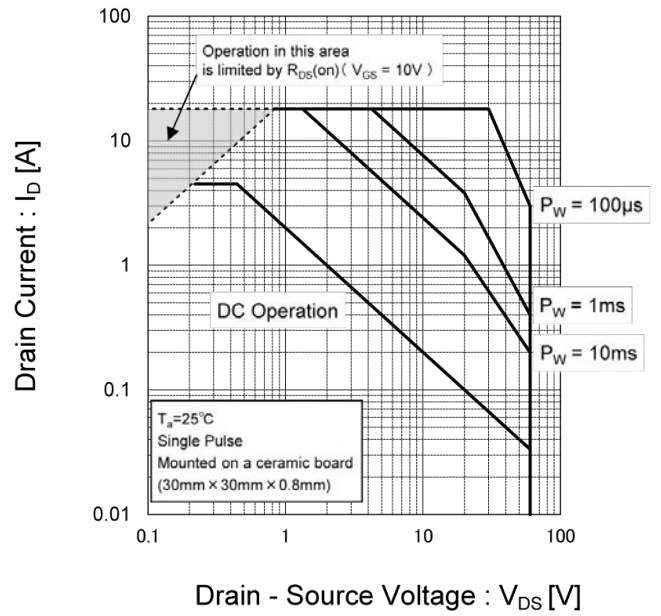


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

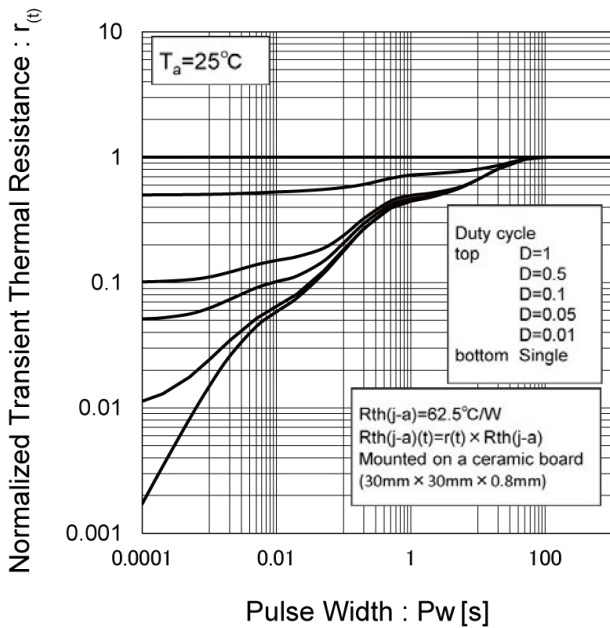
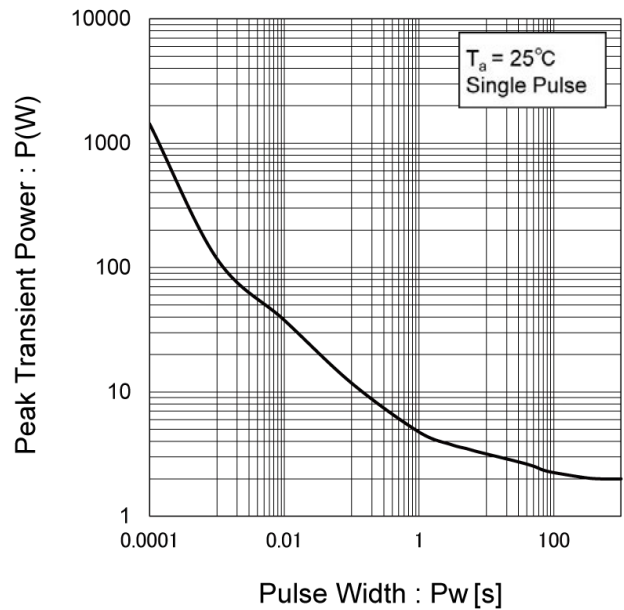


Fig.4 Single Pulse Maximum Power dissipation



● Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

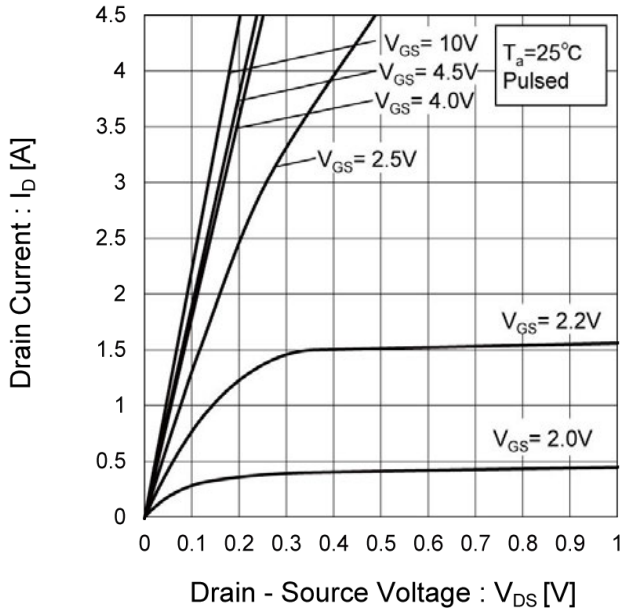


Fig.6 Typical Output Characteristics(II)

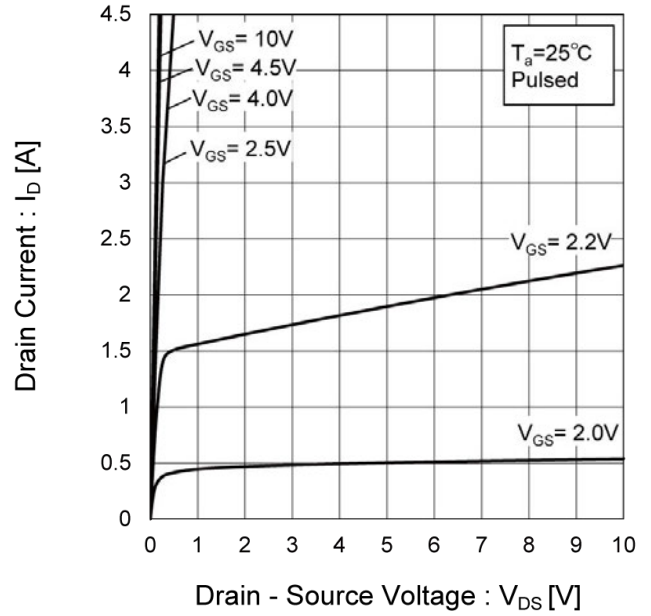
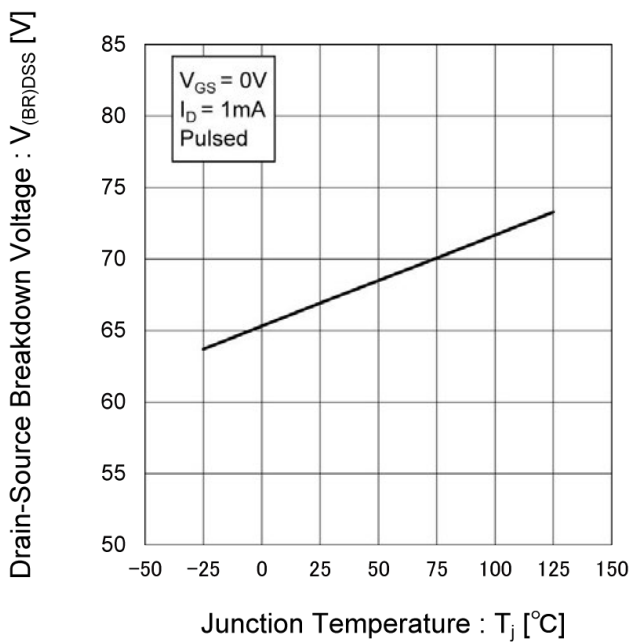


Fig.7 Breakdown Voltage vs. Junction Temperature



●Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

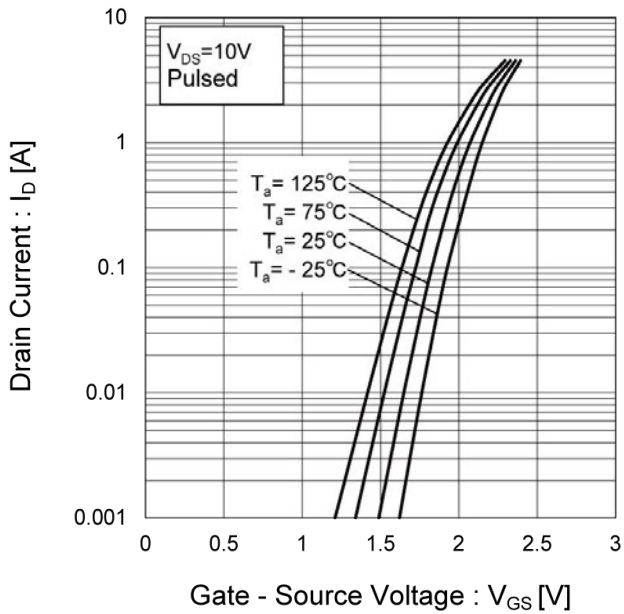


Fig.9 Gate Threshold Voltage vs. Junction Temperature

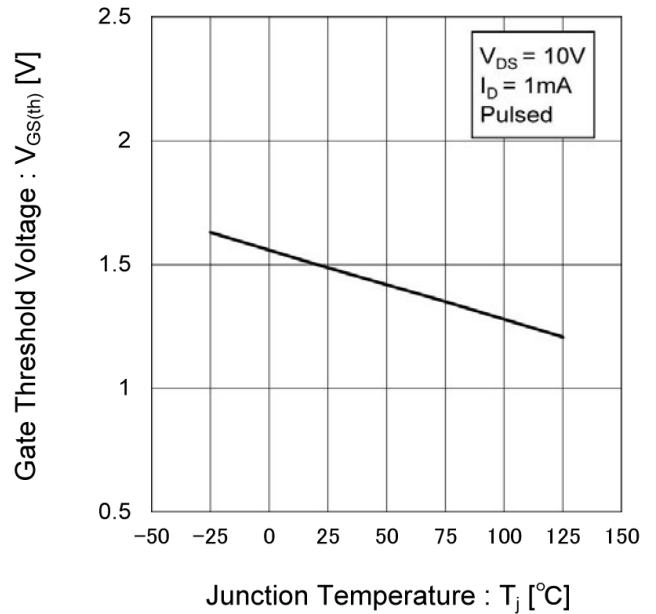
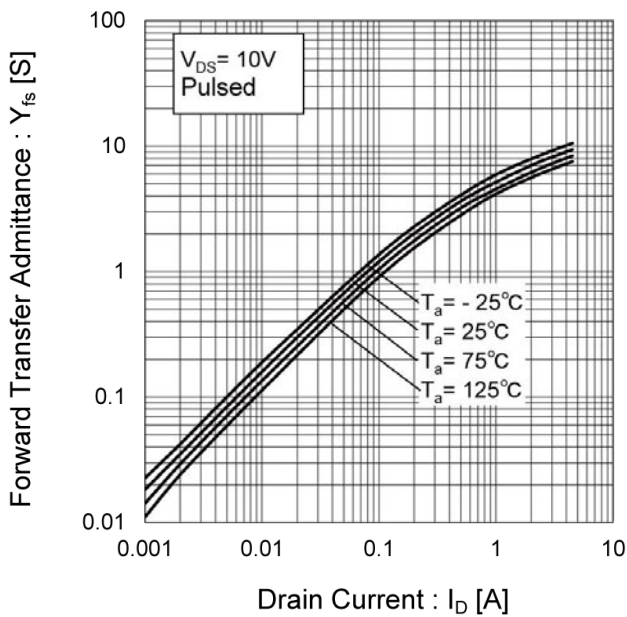


Fig.10 Forward Transfer Admittance vs. Drain Current



● Electrical characteristic curves

Fig.11 Drain Current Derating Curve



Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

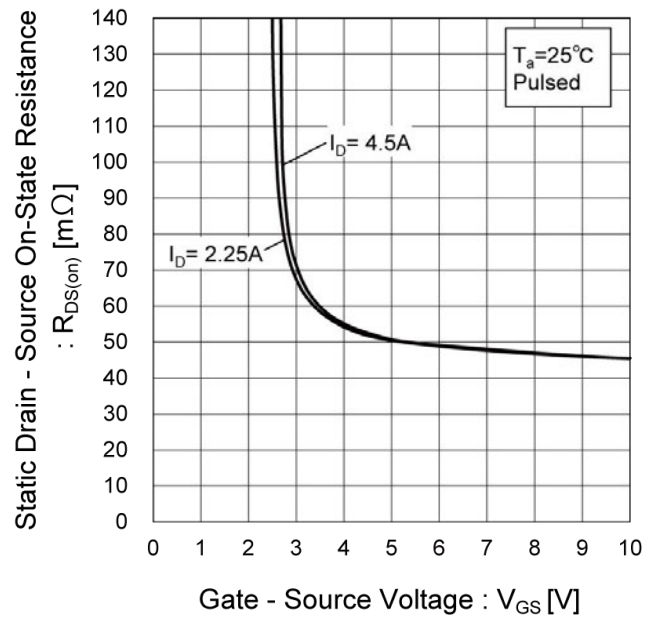
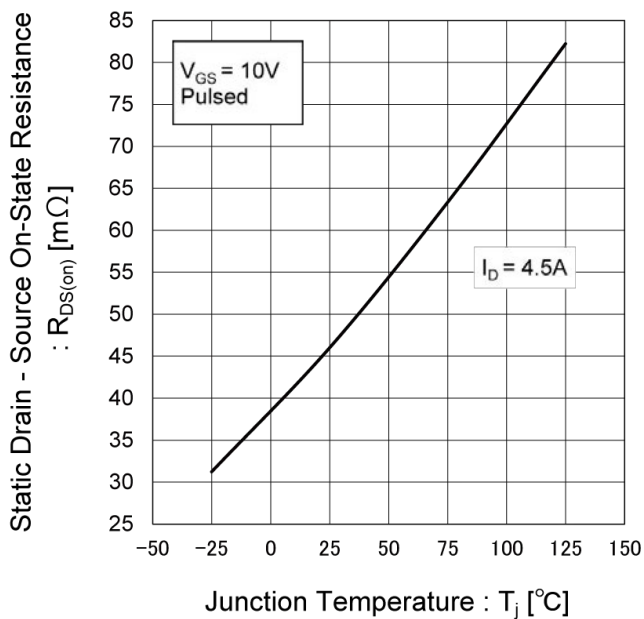


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



● Electrical characteristic curves

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

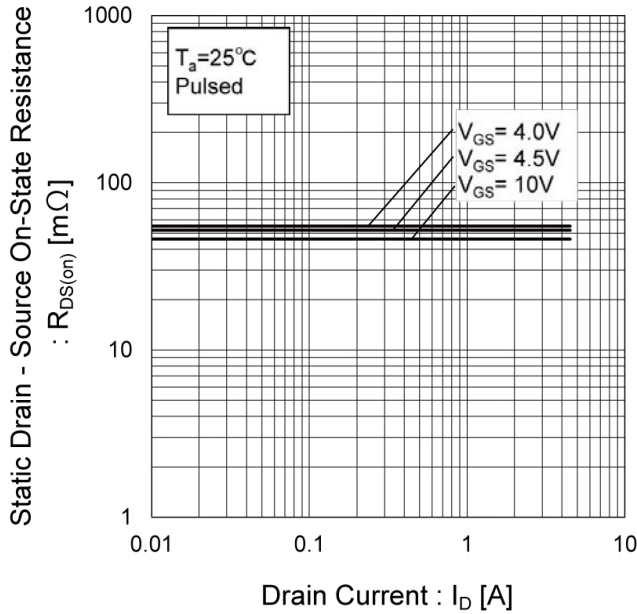


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

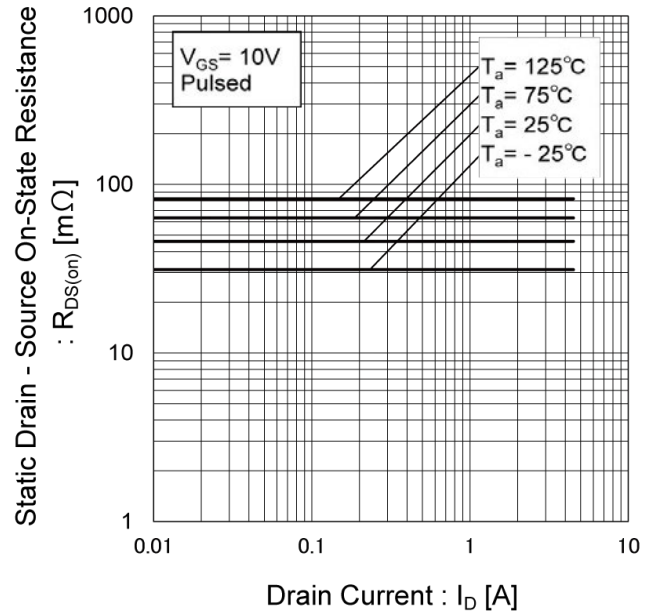


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

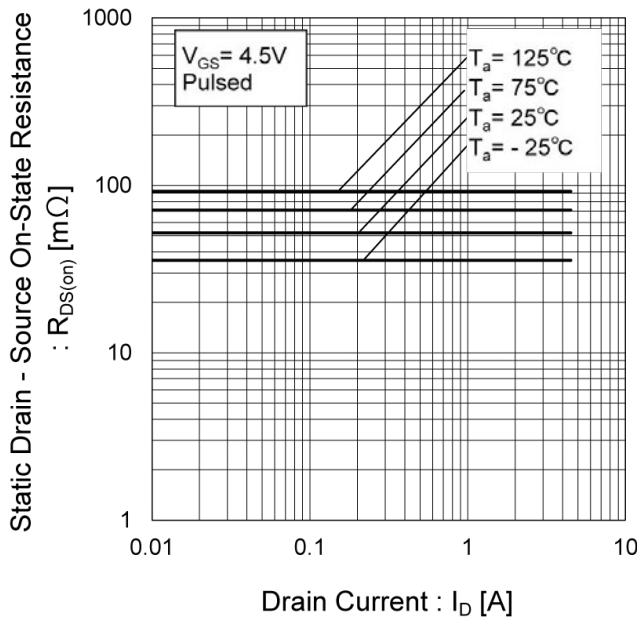
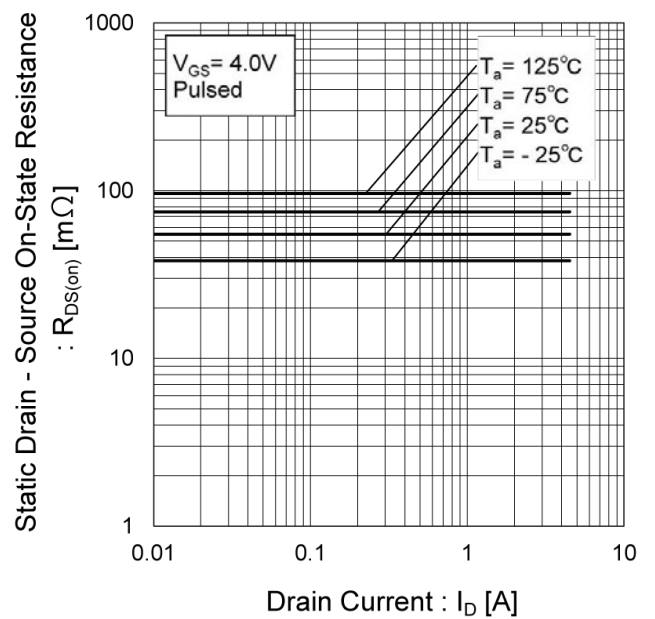


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)





● Electrical characteristic curves

Fig.18 Typical Capacitance vs. Drain - Source Voltage

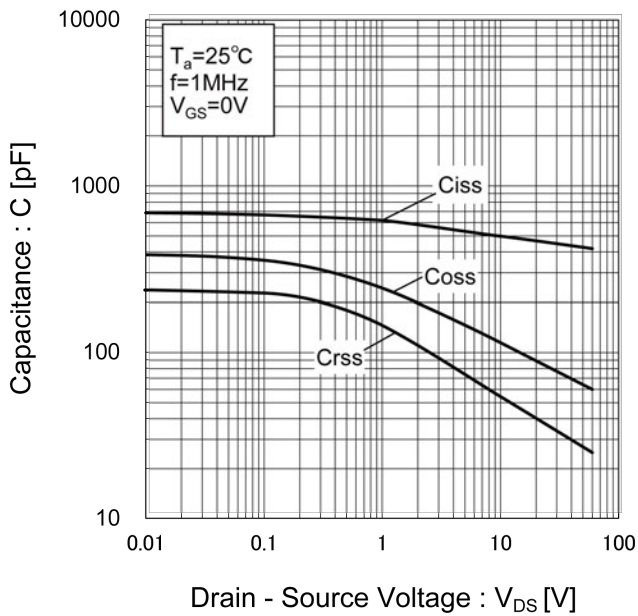


Fig.19 Switching Characteristics

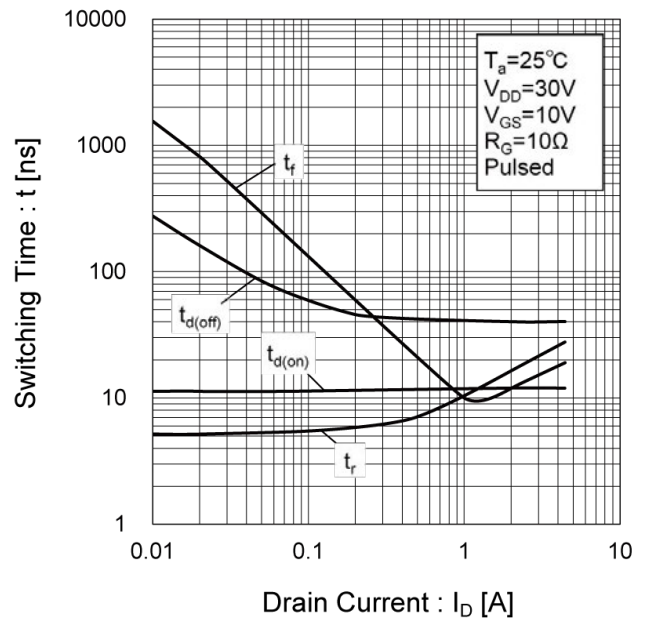


Fig.20 Dynamic Input Characteristics

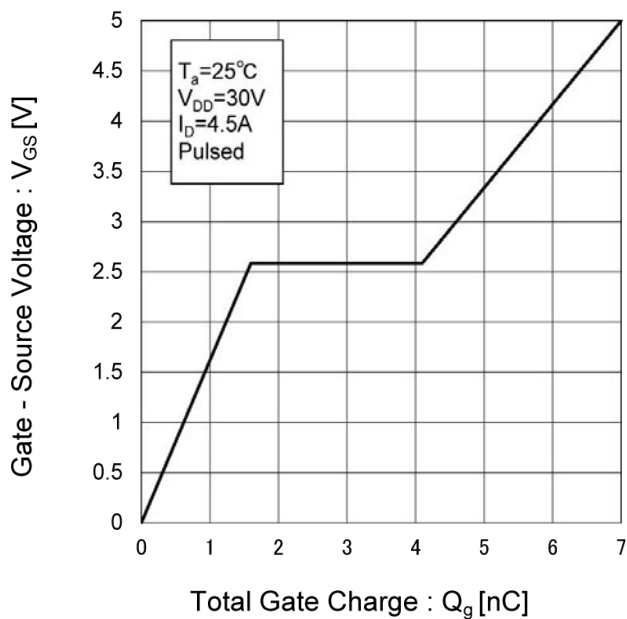
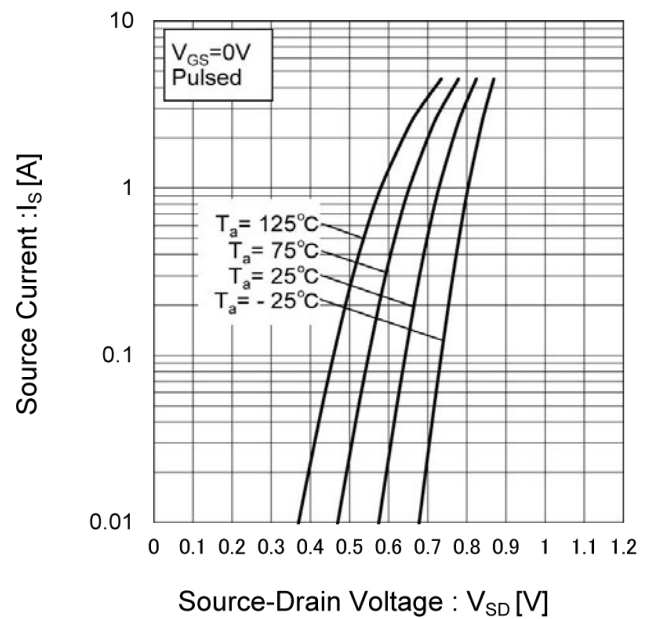


Fig.21 Source Current vs. Source Drain Voltage



● Measurement circuits <It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

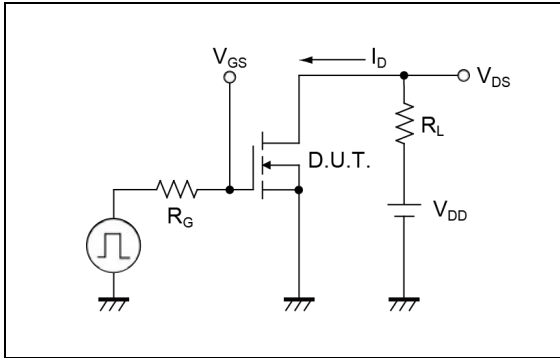


Fig.1-2 Switching Waveforms

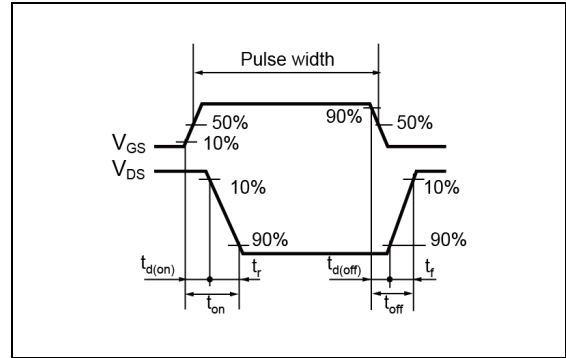


Fig.2-1 Gate Charge Measurement Circuit

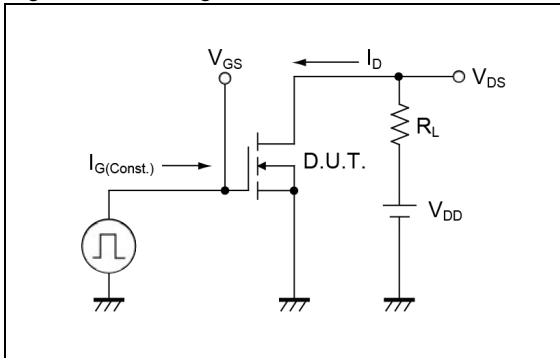
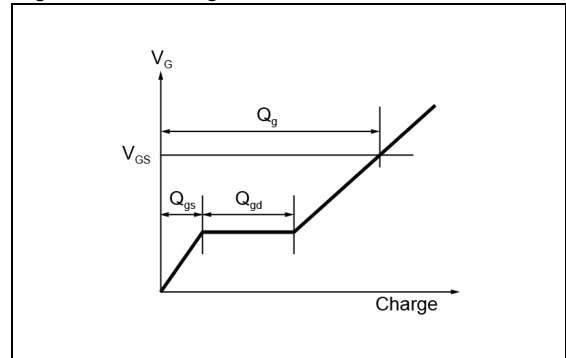
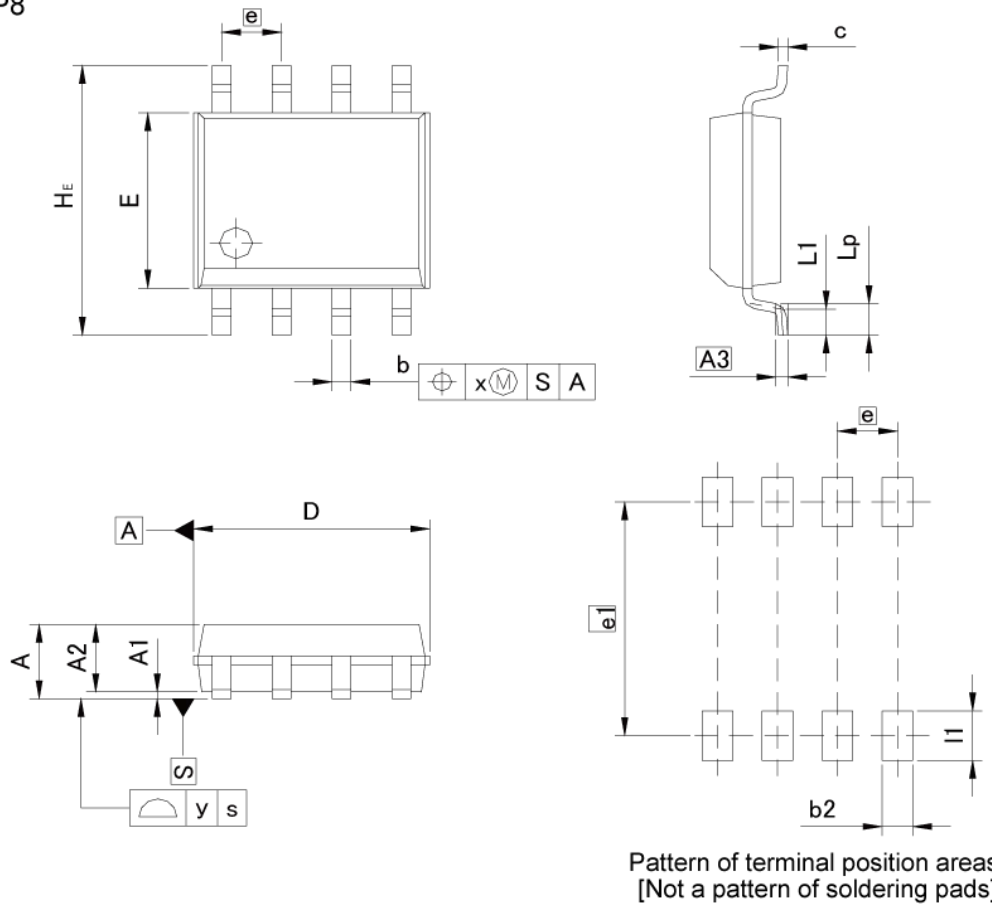


Fig.2-2 Gate Charge Waveform



●Dimensions

SOP8



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIMETERS |      | INCHES |       |
|-----|------------|------|--------|-------|
|     | MIN        | MAX  | MIN    | MAX   |
| A   | -          | 1.75 | -      | 0.069 |
| A1  | 0.15       |      | 0.006  |       |
| A2  | 1.40       | 1.60 | 0.055  | 0.063 |
| A3  | 0.25       |      | 0.010  |       |
| b   | 0.30       | 0.50 | 0.012  | 0.020 |
| c   | 0.10       | 0.30 | 0.004  | 0.012 |
| D   | 4.80       | 5.20 | 0.189  | 0.205 |
| E   | 3.75       | 4.05 | 0.148  | 0.159 |
| e   | 1.27       |      | 0.050  |       |
| HE  | 5.70       | 6.30 | 0.224  | 0.248 |
| L1  | 0.40       | 0.60 | 0.016  | 0.024 |
| Lp  | 0.65       | 0.85 | 0.026  | 0.033 |
| x   | 0.15       |      | 0.006  |       |
| y   | 0.10       |      | 0.004  |       |

| DIM | MILIMETERS |      | INCHES |       |
|-----|------------|------|--------|-------|
|     | MIN        | MAX  | MIN    | MAX   |
| b2  | -          | 0.65 | -      | 0.026 |
| e1  | 5.15       |      | 0.203  |       |
| l1  | -          | 1.15 | -      | 0.045 |

Dimension in mm/inches

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|-----------|-----------|------------|-----------|
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| CLASS IV  |           | CLASS III  |           |

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
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  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
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5. Please verify and confirm characteristics of the final or mounted products in using the Products.
6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
8. Confirm that operation temperature is within the specified range described in the product specification.
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2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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