

4V Drive Nch+Pch MOSFET

AEC-Q101 Qualified

SP8M6FRA

●Structure

Silicon N-channel / P-channel MOSFET

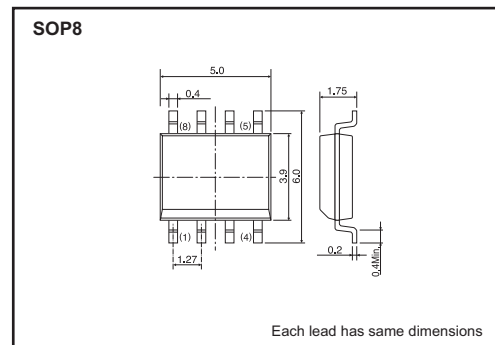
●Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (SOP8).

●Application

Power switching, DC / DC converter.

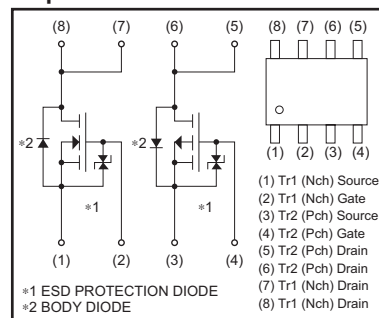
●Dimensions (Unit : mm)



●Packaging specifications

Type	Package	Taping
	Code	TB
	Basic ordering unit (pieces)	2500
SP8M6FRA		○

●Equivalent circuit



*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits		Unit	
		Nchannel	Pchannel		
Drain-source voltage	V _{DSS}	30	-30	V	
Gate-source voltage	V _{GSS}	±20	±20	V	
Drain current	Continuous	I _D	±5.0	±3.5	A
	Pulsed	I _{DP} *1	±20	±14	A
Source current (Body diode)	Continuous	I _S	1.6	-1.6	A
	Pulsed	I _{SP} *1	20	-14	A
Total power dissipation	P _D *2	2		W	
Channel temperature	T _{ch}	150		°C	
Storage temperature	T _{stg}	-55 to +150		°C	

*1 Pw≤10μs, Duty cycle≤1%

*2 MOUNTED ON A CERAMIC BOARD.

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	R _{th (ch-a)} *	62.5	°C / W

*MOUNTED ON A CERAMIC BOARD.

Transistors

N-ch

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	–	–	±10	μA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	30	–	–	V	$I_D=1mA, V_{GS}=0V$
Zero gate voltage drain current	I_{DSS}	–	–	1	μA	$V_{DS}=30V, V_{GS}=0V$
Gate threshold voltage	$V_{GS(th)}$	1.0	–	2.5	V	$V_{DS}=10V, I_D=1mA$
Static drain-source on-state resistance	$R_{DS(on)}$ *	–	36	51	mΩ	$I_D=5.0A, V_{GS}=10V$
		–	52	73		$I_D=5.0A, V_{GS}=4.5V$
		–	58	82		$I_D=5.0A, V_{GS}=4V$
Forward transfer admittance	$ Y_{fs} $ *	3.0	–	–	S	$I_D=5.0A, V_{DS}=10V$
Input capacitance	C_{iss}	–	230	–	pF	$V_{DS}=10V$
Output capacitance	C_{oss}	–	80	–	pF	$V_{GS}=0V$
Reverse transfer capacitance	C_{rss}	–	50	–	pF	$f=1MHz$
Turn-on delay time	$t_{d(on)}$ *	–	6	–	ns	$I_D=2.5A, V_{DD}\approx 15V$
Rise time	t_r *	–	8	–	ns	$V_{GS}=10V$
Turn-off delay time	$t_{d(off)}$ *	–	22	–	ns	$R_L=6.0\Omega$
Fall time	t_f *	–	5	–	ns	$R_G=10\Omega$
Total gate charge	Q_g *	–	3.9	–	nC	$V_{DD}\approx 15V$
Gate-source charge	Q_{gs} *	–	1.1	–	nC	$V_{GS}=5V$
Gate-drain charge	Q_{gd} *	–	1.4	–	nC	$I_D=5.0A$

*Pulsed

●Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_{SD} *	–	–	1.2	V	$I_S=6.4A, V_{GS}=0V$

*Pulsed

Transistors

P-ch

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	–	–	± 10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	–30	–	–	V	$I_D = -1mA, V_{GS} = 0V$
Zero gate voltage drain current	I_{DSS}	–	–	–1	μA	$V_{DS} = -30V, V_{GS} = 0V$
Gate threshold voltage	$V_{GS(th)}$	–1.0	–	–2.5	V	$V_{DS} = -10V, I_D = -1mA$
Static drain-source on-state resistance	$R_{DS(on)}$ *	–	65	90	m Ω	$I_D = -3.5A, V_{GS} = -10V$
		–	100	140		$I_D = -1.75A, V_{GS} = -4.5V$
		–	120	165		$I_D = -1.75A, V_{GS} = -4.0V$
Forward transfer admittance	$ Y_{fs} $ *	1.8	–	–	S	$I_D = -1.75A, V_{DS} = -10V$
Input capacitance	C_{iss}	–	490	–	pF	$V_{DS} = -10V$
Output capacitance	C_{oss}	–	110	–	pF	$V_{GS} = 0V$
Reverse transfer capacitance	C_{rss}	–	75	–	pF	$f = 1MHz$
Turn-on delay time	$t_{d(on)}$ *	–	10	–	ns	$I_D = -1.75A, V_{DD} = -15V$
Rise time	t_r *	–	15	–	ns	$V_{GS} = -10V$
Turn-off delay time	$t_{d(off)}$ *	–	35	–	ns	$R_L = 8.57\Omega$
Fall time	t_f *	–	10	–	ns	$R_G = 10\Omega$
Total gate charge	Q_g *	–	5.5	–	nC	$V_{DD} = -15V$
Gate-source charge	Q_{gs} *	–	1.5	–	nC	$V_{GS} = -5V$
Gate-drain charge	Q_{gd} *	–	2.0	–	nC	$I_D = -3.5A$

*Pulsed

●Body diode characteristics (Source-Drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_{SD}	–	–	–1.2	V	$I_S = -1.0A, V_{GS} = 0V$

Transistors

N-ch

●Electrical characteristic curves

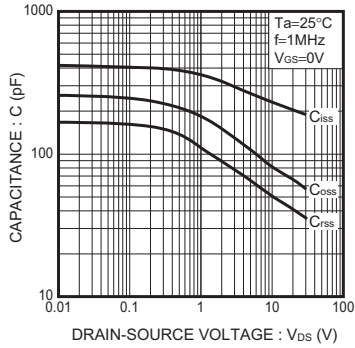


Fig.1 Typical Capacitance vs. Drain-Source Voltage

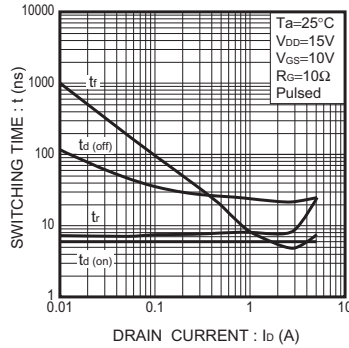


Fig.2 Switching Characteristics

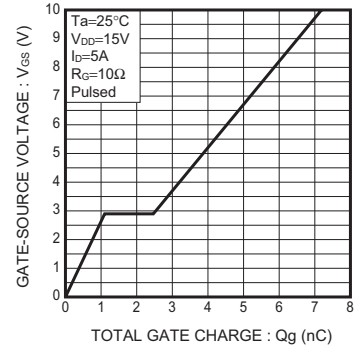


Fig.3 Dynamic Input Characteristics

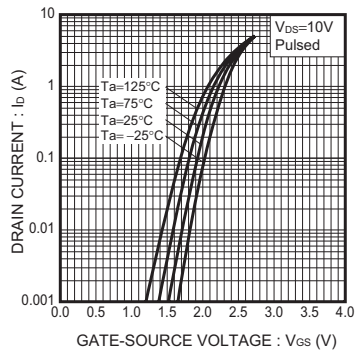


Fig.4 Typical Transfer Characteristics

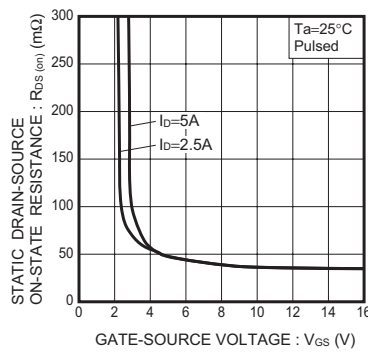


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

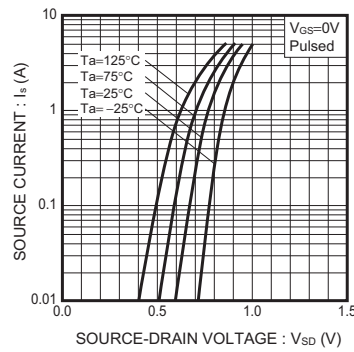


Fig.6 Source Current vs. Source-Drain Voltage

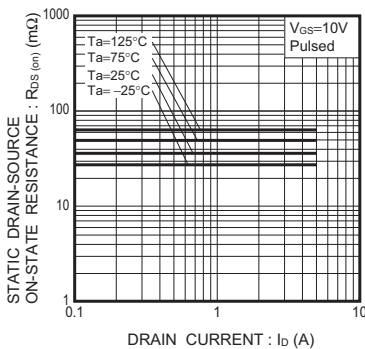


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

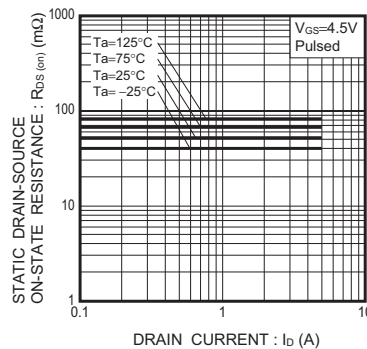


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

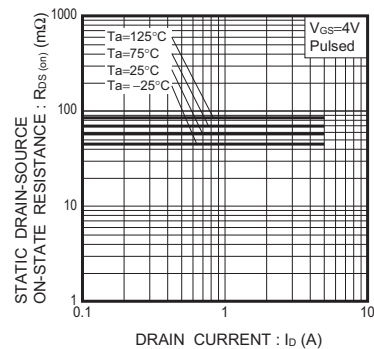


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

Transistors

P-ch

●Electrical characteristic curves

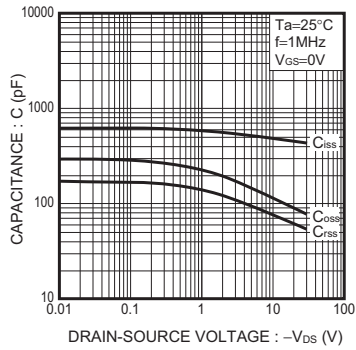


Fig.1 Typical Capacitance vs. Drain-Source Voltage

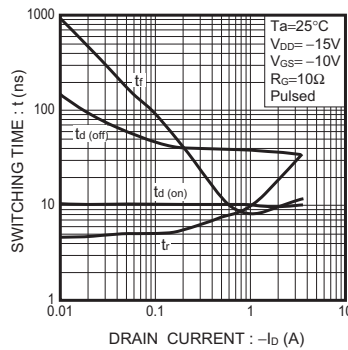


Fig.2 Switching Characteristics

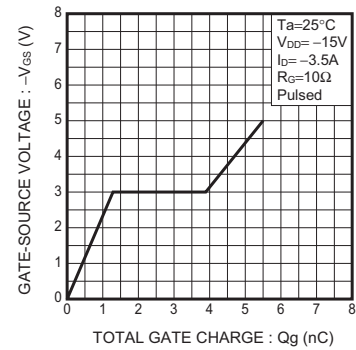


Fig.3 Dynamic Input Characteristics

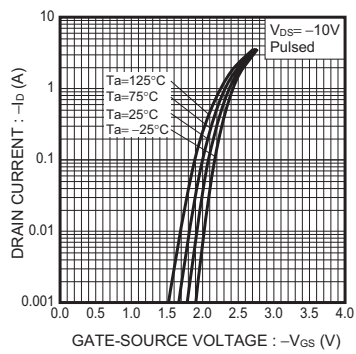


Fig.4 Typical Transfer Characteristics

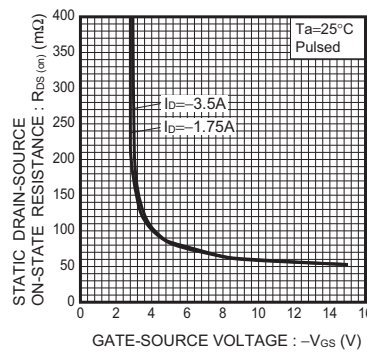


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

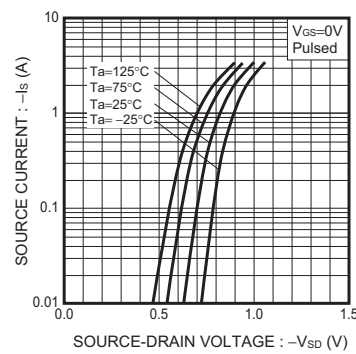


Fig.6 Source Current vs. Source-Drain Voltage

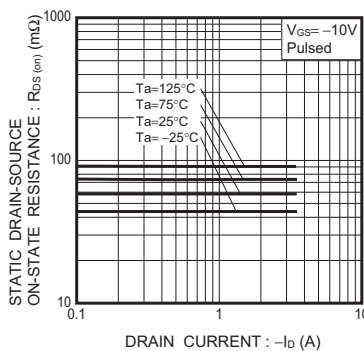


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

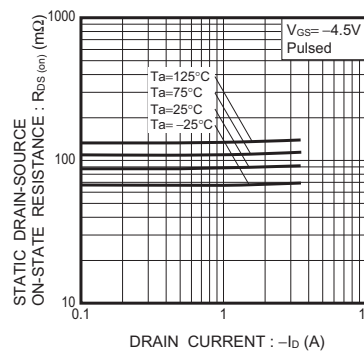


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

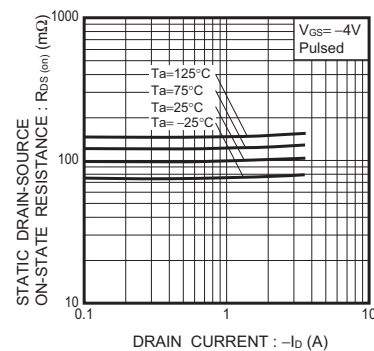


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
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 - [h] Use of the Products in places subject to dew condensation
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5. Please verify and confirm characteristics of the final or mounted products in using the Products.
6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
8. Confirm that operation temperature is within the specified range described in the product specification.
9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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