-20V Pch + Pch Middle Power MOSFET

V _{DSS}	-20V
R _{DS(on)} (Max.)	68mΩ
I _D	±2.5A
P_D	1.25W

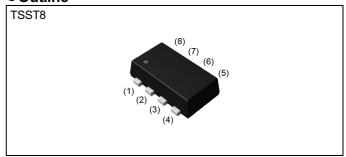
Features

- 1) Low on resistance.
- 2) -1.5V Drive.
- 3) Built-in G-S Protection Diode.
- 4) Small Surface Mount Package (TSST8).
- 5) Pb-free lead plating; RoHS compliant

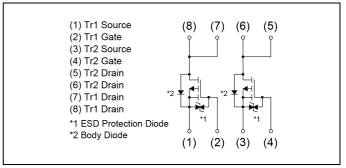
Application

Switching

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TR
	Marking	J21

ullet Absolute maximum ratings (T_a = 25°C) <It is the same ratings for the Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	-20	V
Continuous drain current	I _D	±2.5	Α	
Pulsed drain current	I _{D,pulse} *1	±10	Α	
Gate - Source voltage	V _{GSS}	±10	V	
total		D *2	1.25	
Power dissipation	element	- P _D *2	1.0	W
total		P _D *3	0.6	
Junction temperature	T _j	150	°C	
Range of storage temperature	T _{stg}	-55 to +150	°C	

●Thermal resistance

Parameter		Cymbol	Values			Lloit
		Symbol	Min.	Тур.	Max.	Unit
	total	D *2	-	-	100	
Thermal resistance, junction - ambient	element	R _{thJA} *2	-	-	125	°C/W
	total	R _{thJA} *3	-	-	208	

ullet Electrical characteristics (T_a = 25°C) <It is the same characteristics for the Tr1 and Tr2>

Damanatan	0	0			Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = -1mA$	-20	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\DeltaV_{(BR)DSS}}{\DeltaT_j}$	I _D = -1mA referenced to 25°C	-	-21.9	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V	_	-	-1	μΑ	
Gate - Source leakage current	I _{GSS}	$V_{DS} = 0V, V_{GS} = \pm 10V$	-	-	±10	μΑ	
Gate threshold voltage	V _{GS(th)}	V _{DS} = -10V, I _D = -1mA	-0.3	-	-1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I _D = -1mA referenced to 25°C	-	2.4	-	mV/°C	
		V _{GS} = -4.5V, I _D = -2.5A	-	49	68		
Static drain - source	D *4	V _{GS} = -2.5V, I _D = -1.2A	-	68	95		
on - state resistance	R _{DS(on)} *4	V _{GS} = -1.8V, I _D = -1.2A	-	100	150	- mΩ	
		V _{GS} = -1.5V, I _D = -0.5A	-	140	280		
Gate input resistance	R_{G}	f = 1MHz, open drain	-	12	-	Ω	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = -10V, I _D = -2.5A	2.5	-	-	S	

● Electrical characteristics (T_a = 25°C) < It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	1270	-	
Output capacitance	C _{oss}	V _{DS} = -10V	-	100	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	90	-	
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq -10V, V_{GS} = -4.5V$	-	9	-	
Rise time	t _r *4	I _D = -1.2A	-	30	-	
Turn - off delay time	t _{d(off)} *4	$R_L = 8.3\Omega$	-	120	-	ns
Fall time	t _f *4	$R_G = 10\Omega$	-	85	-	

● Gate charge characteristics (T_a = 25°C) < It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol Conditions		Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q _g *4	V _{DD} ≃ -10V	1	12	1	
Gate - Source charge	Q _{gs} *4	I _D = -2.5A	-	2.5	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = -4.5V	-	2.0	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol Conditions -		Min.	Тур.	Max.	Offic	
Body diode continuous forward current	I _S	⊤ _a = 25°C	-	-	-0.8	A	
Body diode pulse current	I _{SP} *1	1 _a - 25 C	-	-	-10	A	
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_{S} = -2.5A$	-	-	-1.2	V	

^{*1} Pw≦10µs , Duty cycle≦1%



^{*2} Mounted on a ceramic board (30×30×0.8mm)

^{*3} Mounted on a FR4 (20×20×0.8mm)

^{*4} Pulsed

Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

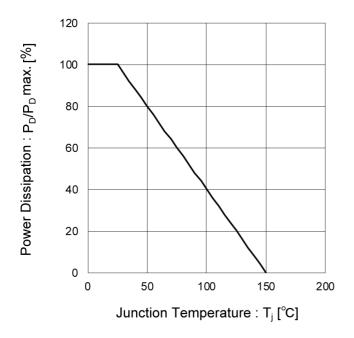
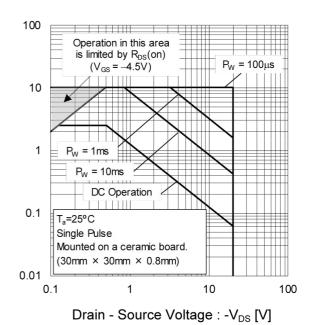


Fig.2 Maximum Safe Operating Area



Drain Current: -l_D [A]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

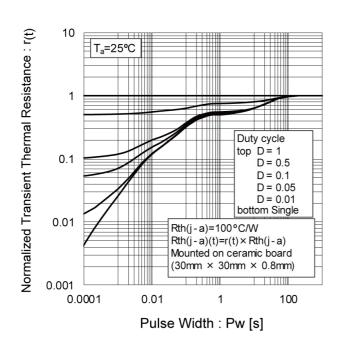
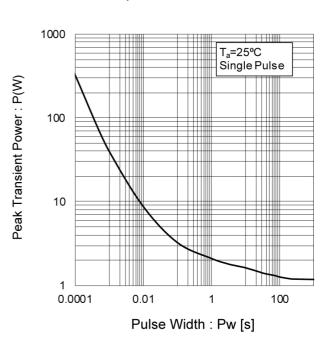


Fig.4 Single Pulse Maximum Power dissipation



Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

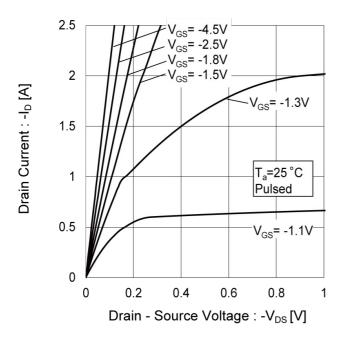


Fig.6 Typical Output Characteristics(II)

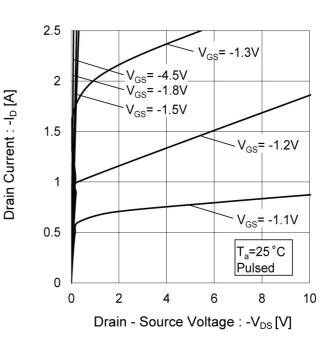


Fig.7 Breakdown Voltage vs.
Junction Temperature

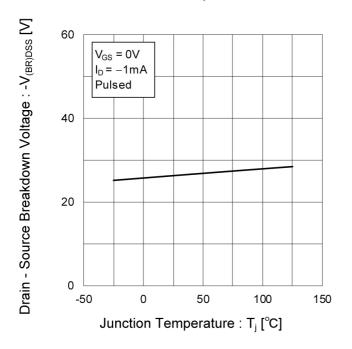
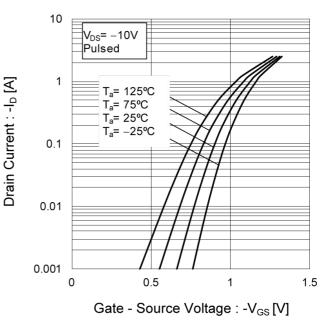


Fig.8 Typical Transfer Characteristics



Gate Threshold Voltage: -V_{GS(th)} [V]

• Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

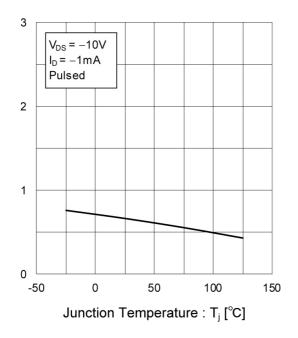


Fig.10 Forward Transfer Admittance vs.
Drain Current

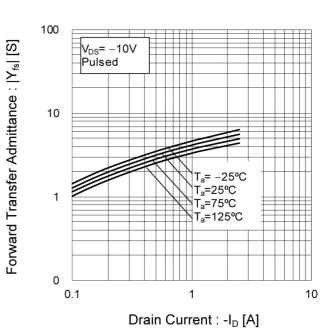


Fig.11 Drain Current Derating Curve

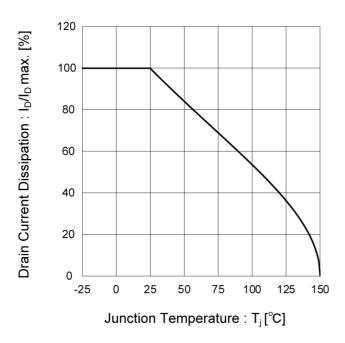
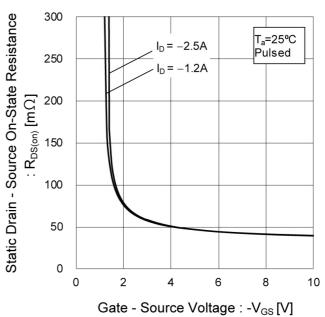


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



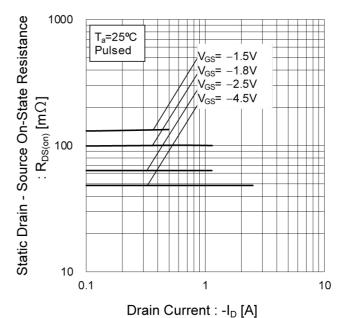
TT8J21

• Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

100 Static Drain - Source On-State Resistance 90 80 70 $: R_{DS(on)} [m\Omega]$ 60 50 40 30 20 V_{GS} = -4.5V $I_{D} = -2.5A$ 10 Pulsed -50 25 50 100 125 150 -25 Junction Temperature : T_i [°C]

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)





TT8J21 Datasheet

• Electrical characteristic curves

Fig.15 Static Drain - Source On - State
Resistance vs. Drain Current(II)

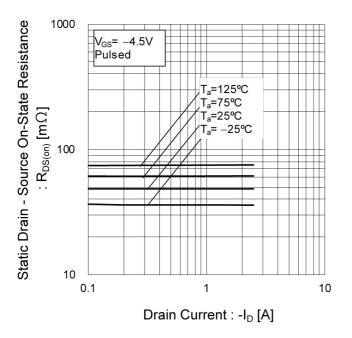


Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current(III)

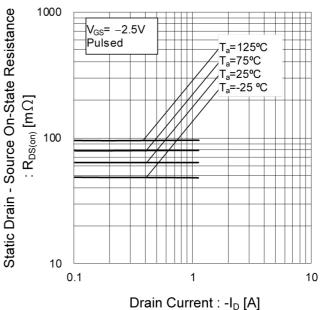


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

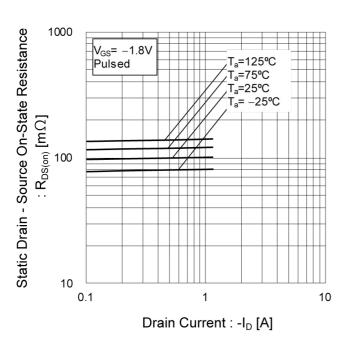
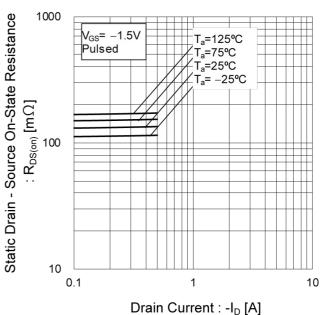


Fig.18 Static Drain - Source On - State Resistance vs. Drain Current(V)



• Electrical characteristic curves

Fig.19 Typical Capacitance vs.

Drain - Source Voltage

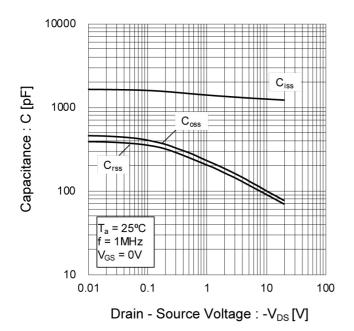


Fig.20 Switching Characteristics

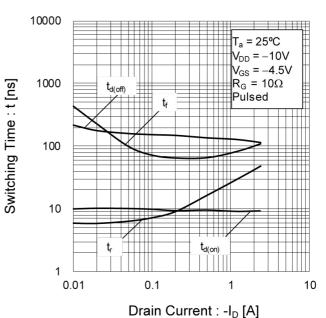


Fig.21 Dynamic Input Characteristics

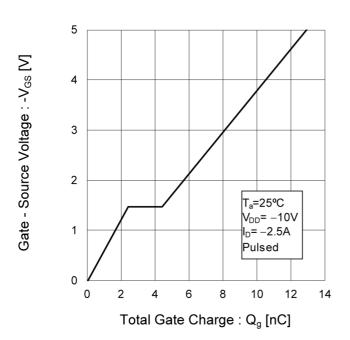
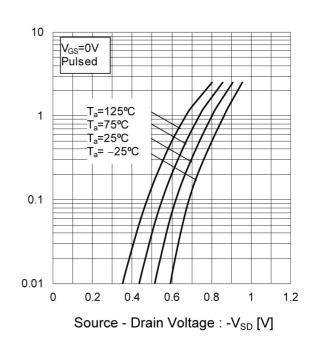


Fig.22 Source Current vs.

Source Drain Voltage



Source Current : -Is [A]

• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

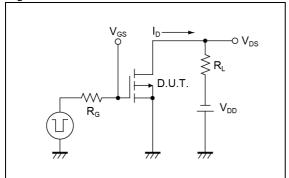


Fig. 2-1 GATE CHARGE MEASUREMENT CIRCUIT

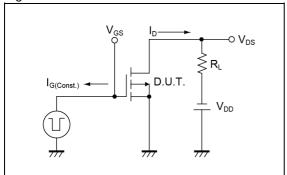


Fig. 1-2 SWITCHING WAVEFORMS

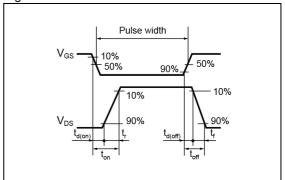
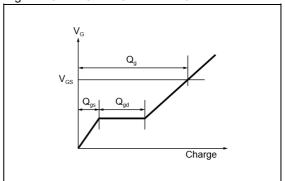


Fig. 2-2 GATE CHARGE WAVEFORM

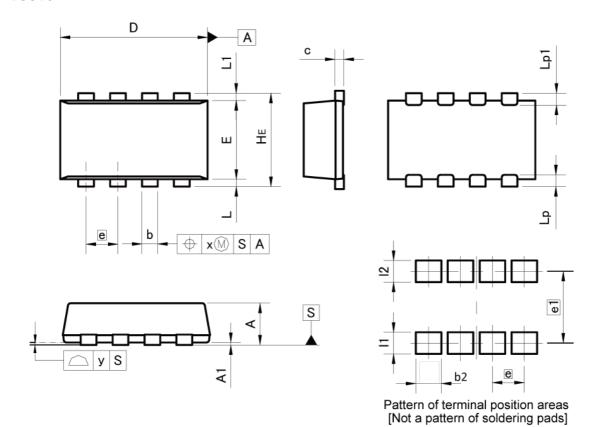


Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

TSST8



DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.22	0.42	0.009	0.017
С	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	1.50	1.70	0.059	0.067
е	0.0	65	0.0	26
HE	1.80	2.00	0.071	0.079
L	0.05	0.25	0.002	0.010
L1	0.05	0.25	0.002	0.010
Lp	0.15	0.34	0.006	0.013
Lp1	0.15	0.34	0.006	0.013
x	1,-	0.10	-	0.004
У	-	0.10	-	0.004

DIM MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX
b2	-	0.52	-	0.020
e1	1.4	1.46)57
11	-	0.44	-	0.017
12	ů. 	0.44		0.017

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCTI
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PGA-E Rev.003

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