UM6K1N

s same dimensions

Each

Abbreviated symbol

•External dimensions (Unit : mm)

UMT6

I

Unit

V

V

mΑ

mΑ

mW

°C

°C

2.5V Drive Nch+Nch MOS FET UM6K1N

Structure

Silicon N-channel MOS FET

Features

- 1) Two 2SK3018 transistors in a single UMT package.
- 2) The MOS FET elements are independent, eliminating mutual interference.
- 3) Mounting cost and area can be cut in half.
- 4) Low On-resistance.
- 5) Low voltage drive (2.5V drive) makes this device ideal for portable equipment.

Applications

Interfacing, switching (30V, 100mA)

Packag	jing specifications		●Inner circuit	(6) (5) Gate Protection (4)	
Туре	Package Code	Taping TN			7
1990	Basic ordering unit (pieces)				
UM6K1N	1	70			Ť
				Tr2 * C Gate O O (1) Protection (2) (3) Diode	
				(1) Tr1 Source * A protection diode has been b (2) Tr1 Gate in between the gate and the s; (3) Tr2 Drain to protect against static electri (4) Tr2 Source when the product is in use.	ource
	te maximum ratings ((5) Tr2 Gate Use the protection circuit when	n
<it is="" s<="" td="" the=""><td>same ratings for Tr1 an</td><td>d Tr2.></td><td></td><td>(6) Tr1 Drain rated voltages are exceeded.</td><td></td></it>	same ratings for Tr1 an	d Tr2.>		(6) Tr1 Drain rated voltages are exceeded.	

Limits

30

±20

±100

±400

150

150 -55 to +150

Drain current Total power dissipation Channel temperature Range of storage temperature

∗1 Pw≤10µs, Duty cycle≤1%

Drain-source voltage

Gate-source voltage

*2 With each pin mounted on the recommended lands

Parameter

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	833	°C / W / TOTAL
		1042	°C / W / ELEMENT

Symbol

VDSS

Vgss

 \mathbf{I}_{D}

Idp *2

 \mathbf{P}_{D}

Tch

Tstg

*1

Continuous

Pulsed

* With each pin mounted on the recommended lands.

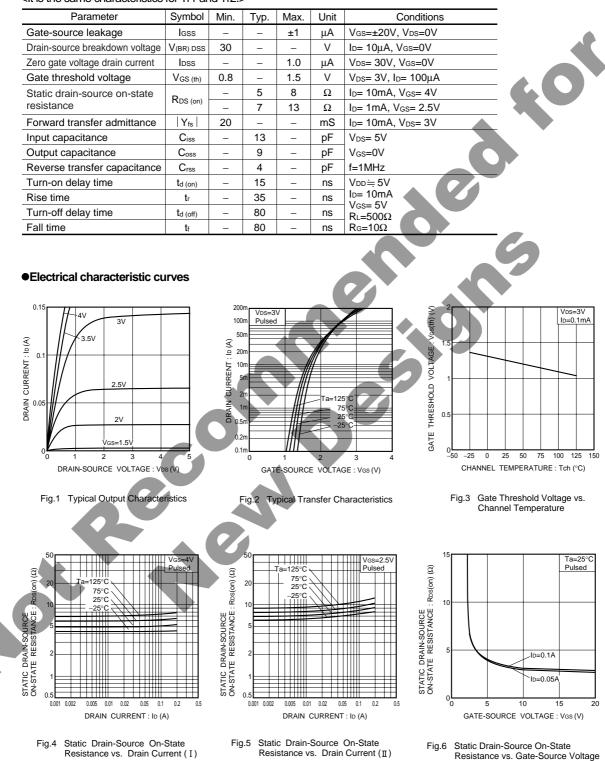


UM6K1N

Transistors

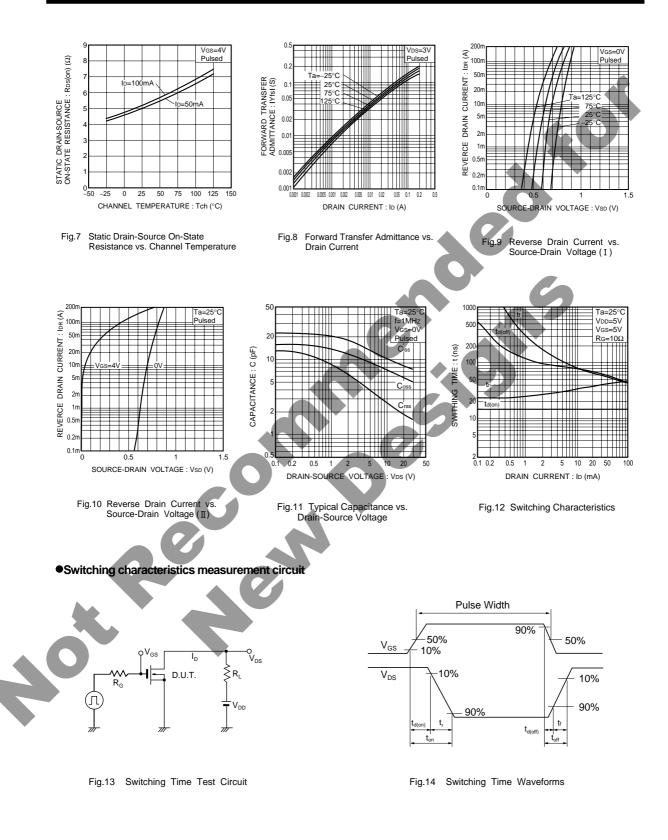
•Electrical characteristics (Ta=25°C)

< It is the same characteristics for Tr1 and Tr2.>



ROHM

Transistors



Rev.B

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