

General purpose (Dual digital transistors)

AEC-Q101 Qualified

Parameter	DTr1 and DTr2		
V _{CC}	50V		
I _{C(MAX.)}	100mA		
R ₁	22kΩ		
R ₂	22kΩ		

SOT-363 SC-88

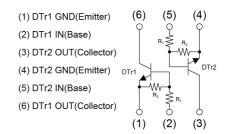
UMT6

Features

- 1)Two DTC124E chips in a UMT6 package.
- 2)Mounting possible with UMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

•Inner circuit

Outline



Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
UMH1N FHA	SOT-363 (UMT6)	2021	TN	180	8	3000	H1

● Absolute maximum ratings (T_a = 25°C)

<For DTr1 and DTr2 in common>

Parameter	Symbol	Values	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	-10 to 40	V
Output current	Io	30	mA
Collector current	I _{C(MAX)} *1	100	mA
Power dissipation	P _D *2*3	150	mW/TOTAL
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

<For DTr1 and DTr2 in common>

Daramatar	Cymah al	Conditions	Values			l le:4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input voltage	$V_{I(off)}$	V _{CC} = 5V, I _O = 100μA	-	-	0.5	V	
Input voltage	V _{I(on)}	V _O = 0.2V, I _O = 5mA	3	-	-		
Output voltage	V _{O(on)}	I _O = 10mA, I _I = 0.5mA	-	100	300	mV	
Input current	I _I	V _I = 5V	-	-	360	μA	
Output current	I _{O(off)}	V _{CC} = 50V, V _I = 0V	-	-	500	nA	
DC current gain	G _I	$V_{O} = 5V, I_{O} = 5mA$	56	-	-	-	
Input resistance	R ₁	-	15.4	22	28.6	kΩ	
Resistance ratio	R ₂ /R ₁	-	0.8	1.0	1.2	-	
Transition frequency	f _T *1	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	1	MHz	

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land

^{*3 120}mW per element must not be exceeded.

● Electrical characteristic curves (T_a = 25°C)

<For DTr1 and DTr2 in common>

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

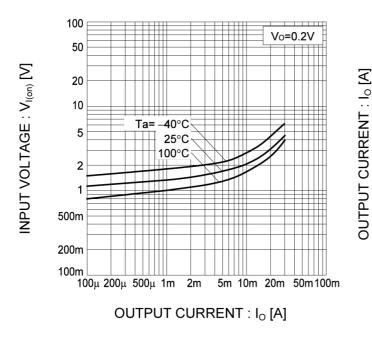
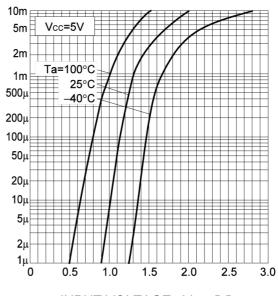


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)



INPUT VOLTAGE: V_{I(off)} [V]

Fig.3 Output Current vs. Output Voltage

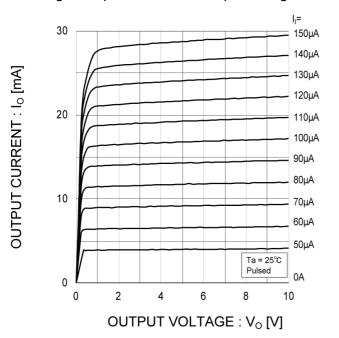
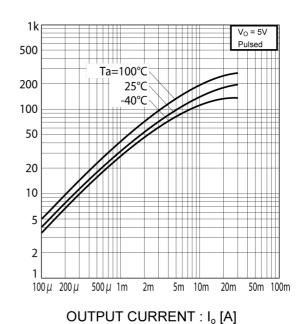


Fig.4 DC Current Gain vs. Output Current

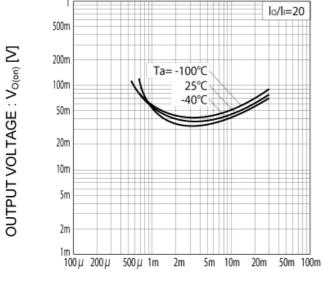


OC CURRENT GAIN: G

● Electrical characteristic curves (T_a = 25°C)

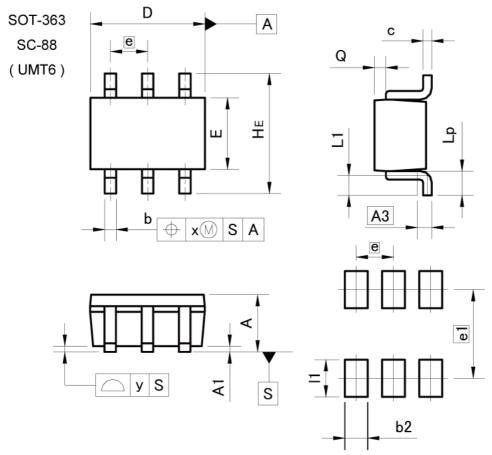
<For DTr1 and DTr2 in common>

Fig.5 Output Voltage vs. Output Current



OUTPUT CURRENT: Io [A]

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES		
DIM	MIN	MAX	MIN	MAX		
Α	0.80	1.00	0.031	0.039		
A1	0.00	0.10	0.000	0.004		
A3	0.	25	0.0	0.010		
b	0.15	0.30	0.006	0.012		
С	0.10	0.20	0.004	0.008		
D	1.90	2.10	0.075	0.083		
E	1.15	1.35	0.045	0.053		
е	0.	65	0.026			
HE	2.00	2.20	0.079	0.087		
L1	0.10	0.40	0.004	0.016		
Lp	0.25	0.55	0.010	0.022		
Q	0.10	0.30	0.004	0.012		
Х	-	0.10	-	0.004		
У	-)	0.10	-	0.004		
D.114	MILIM	ETERS	INC	HES		
DIM	MIN	MAX	MIN	MAX		
h2	_	0.40	_	0.016		

Dimension in mm/inches

e1 11



0.026

0.061

0.65

1.55

Notice

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Ť	JÁPAN	USA	EU	CHINA
	CLASSIII	CLASSIII	CLASS II b	СГУССШ
	CLASSIV	CLASSIII	CLASSIII	CLASSII

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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