## 60V Nch+Nch Power MOSFET

V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	153mΩ
I <sub>D</sub>	±3.0A
P <sub>D</sub>	2W

#### Features

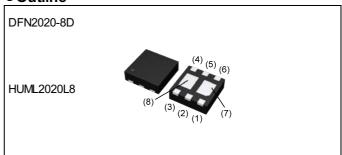
- 1) Low on resistance
- 2) Small Surface Mount Package
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free

# Application

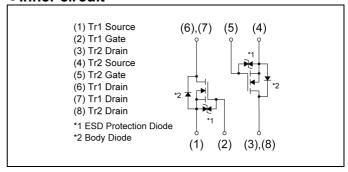
Switching

DC/DC Converter

## Outline



## •Inner circuit



Packaging specifications

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	Packing	Embossed Tape						
	Reel size (mm)	180						
Туре	Tape width (mm)	8						
	Basic ordering unit (pcs)	3000						
	Taping code	TR						
	Marking	K30						

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	60	V
Continuous drain current	I <sub>D</sub>	±3.0	Α
Pulsed drain current	I <sub>DP</sub> *1	±12	Α
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Avalanche current, single pulse	I <sub>AS</sub> *2	3.0	Α
Avalanche energy, single pulse	E <sub>AS</sub> *2	1.3	mJ
Power dissipation	P <sub>D</sub> *3	2	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Parameter	Symbol	Values			Lleit
- Farameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	1	-	62.5	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Davanatas	Curanh al	Conditions		Values	Unit		
Parameter	Symbol	Conditions	Min. Typ.		Max.	UIIIL	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	-	-	60	V	
Breakdown voltage	ΔV <sub>(BR)DSS</sub>	I <sub>D</sub> = 1mA		60		mV/°C	
temperature coefficient	ΔT <sub>j</sub>	referenced to 25°C	-	00	-	IIIV/ C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V		-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V		-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 50 \mu A$		-	2.7	V	
Gate threshold voltage	$\Delta V_{GS(th)}$	I <sub>D</sub> = 1mA		-5.6		mV/°C	
temperature coefficient	ΔTj	referenced to 25°C	1	-5.6	ı	mv/ C	
Static drain - source	D *4	$V_{GS} = 10V, I_D = 3.0A$	-	111	153	m0	
on - state resistance	R <sub>DS(on)</sub> *4	$V_{GS}$ = 4.5V, $I_D$ = 3.0A	-	162	223	mΩ	
Gate resistance	R <sub>G</sub>	f=1MHz, open drain	-	5.3	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5.0V, I <sub>D</sub> = 3A	1.7	-	-	S	

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> L  $\simeq$  0.2mH, V<sub>DD</sub> = 30V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*3</sup> Mounted on a Cu boad (40×40×0.8mm)

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Daramatar	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Ufill	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	110	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30V	-	21	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	5.3	-		
Turn - on delay time	t <sub>d(on)</sub> *4	V <sub>DD</sub> ≈ 30V,V <sub>GS</sub> = 10V	-	5.9	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 1.5A	-	6.4	-		
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 20\Omega$	-	12	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	9.8	-		

# ullet Gate charge characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions		Values	Unit	
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q <sub>g</sub> *4		-	2.1	-	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \simeq 30V$ , $I_D = 3.0A$ $V_{GS} = 10V$	-	0.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	1.00	-	0.4	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

# <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	1.67	^
Pulse forward current	I <sub>SP</sub> *1	<sup>-</sup> T <sub>a</sub> = 25°C	-	-	12	Α
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.67A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

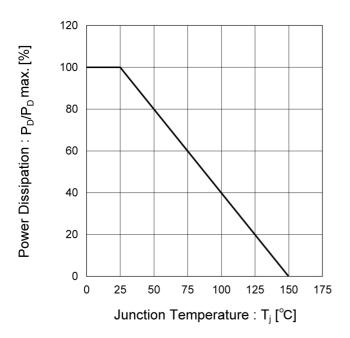
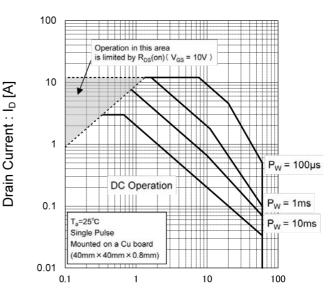


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage: V<sub>DS</sub>[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

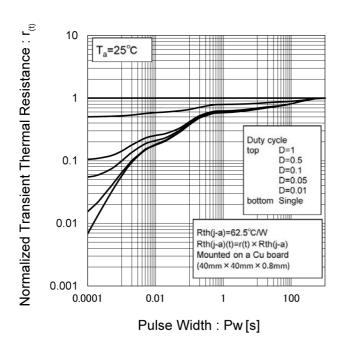


Fig.4 Single Pulse Maximum Power dissipation

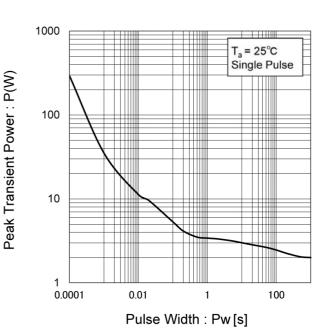
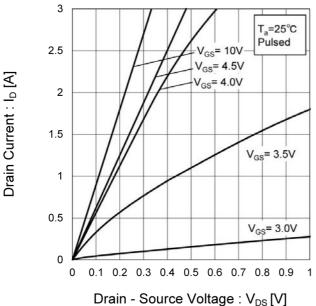
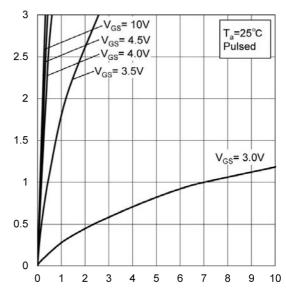


Fig.5 Typical Output Characteristics(I)



Diam Course voltage: VDS[V

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.

Junction Temperature

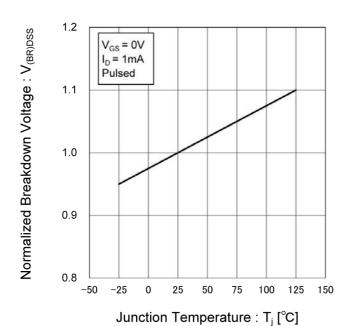


Fig.8 Typical Transfer Characteristics

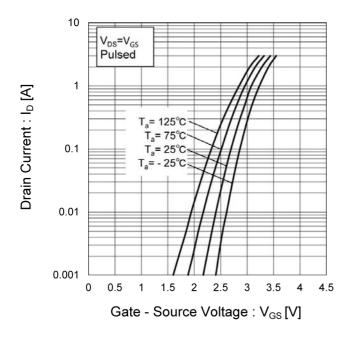
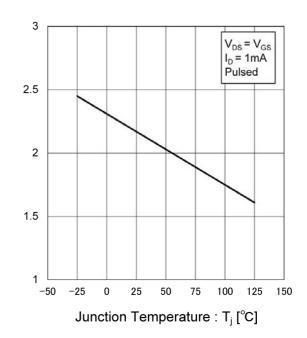


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage :  $V_{GS(th)}\left[V\right]$ 

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Fig.10 Forward Transfer Admittance vs.
Drain Current

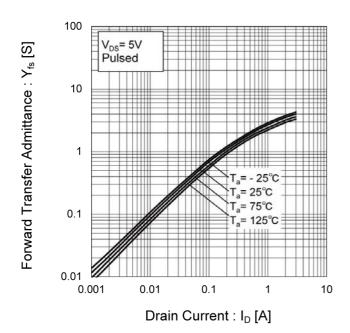


Fig.11 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

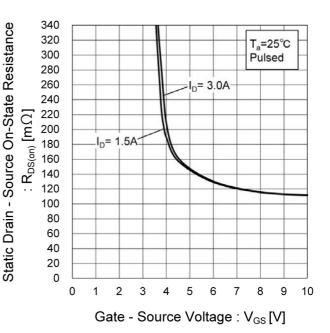


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

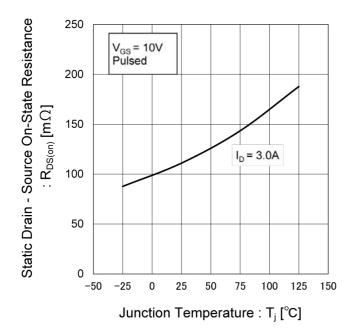


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

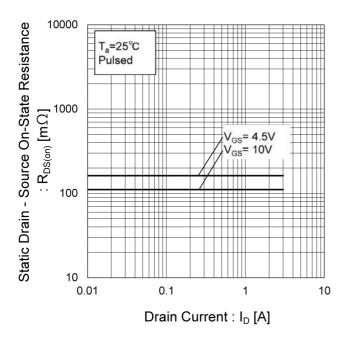


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

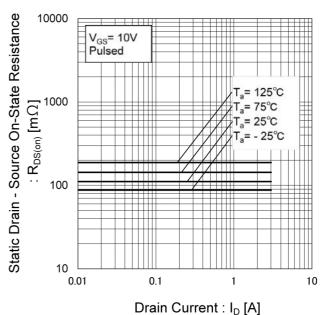
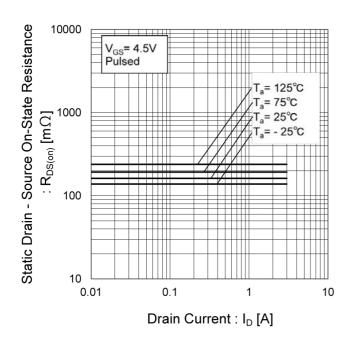


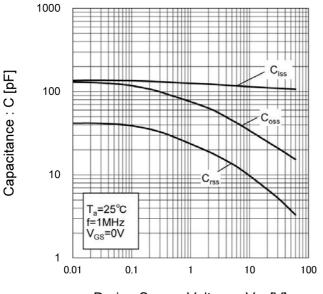
Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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Fig.17 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage :  $V_{DS}[V]$ 

Fig.18 Switching Characteristics

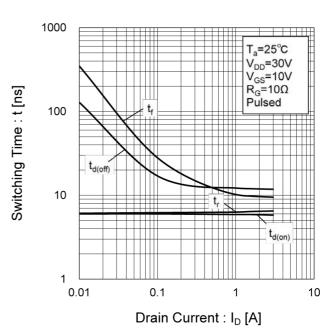


Fig.19 Dynamic Input Characteristics

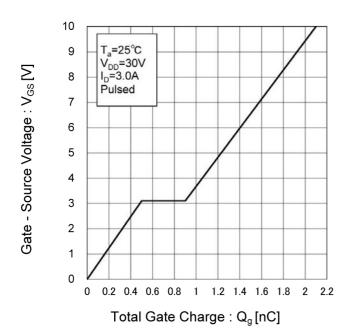
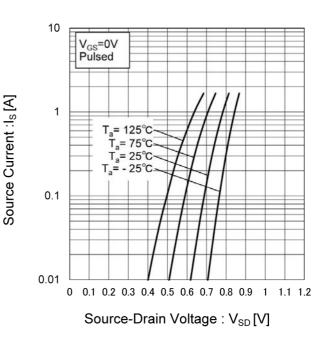


Fig.20 Source Current vs.

Source Drain Voltage



# • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

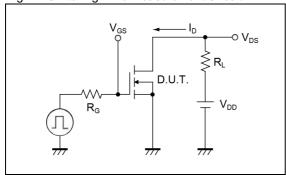


Fig.2-1 Gate Charge Measurement Circuit

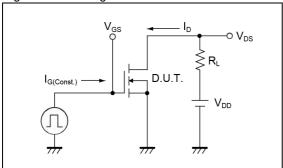


Fig.3-1 Avalanche Measurement Circuit

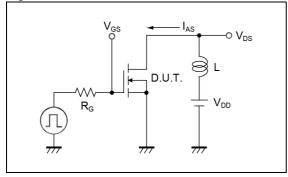


Fig.1-2 Switching Waveforms

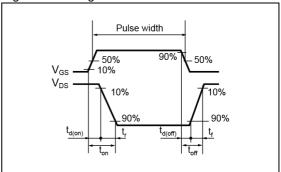


Fig.2-2 Gate Charge Waveform

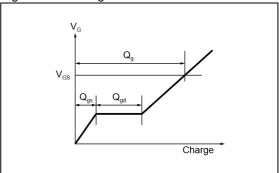
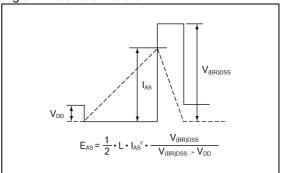


Fig.3-2 Avalanche Waveform

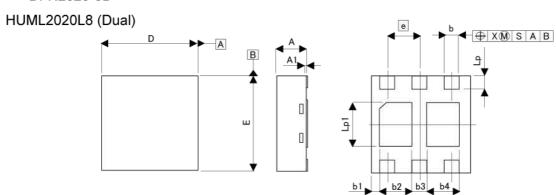


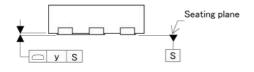
## Notice

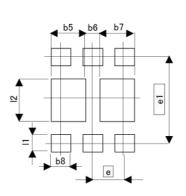
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

# Dimensions

## DFN2020-8D







Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	0.:	25	0.0	010
b2	0.60	0.70	0.024	0.028
b3	0	.3	0.0	)12
b4	0.60	0.70	0.024	0.028
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.	65	0.026	
Lp	0.225	0.325	0.009	0.013
Lp1	0.80	1.00	0.031	0.039
x	-	0.10	30 <del>4</del> 3	0.004
٧	-	0.10	9.90	0.004

DIM -	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
b5	-	0.70	5.51	0.028
b6	0.20	0.30	0.008	0.012
b7	-	0.70	0#0	0.028
b8		0.45	14	0.018
e1	1.7	1.725		068
11	-	0.425	9.50	0.017
12	*	1.00	(E)	0.039

Dimension in mm/inches



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  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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