

		●Outline	9		
V <sub>DSS</sub>	20V				
R <sub>DS(on)</sub> (Max.)	3.5Ω			<b>(6)</b>	
l <sub>D</sub>	±100mA	VMT6			
P <sub>D</sub>	150mW			(3)	
●Features		●Inner o	ircuit		
<ol> <li>Low on - resistance.</li> <li>Small package(VMT6)</li> <li>Low voltage drive(1.2V dr</li> </ol>	ive)		(2) Tr 2 Drain (2) Tr 2 Drain (4) Tr 2 Source (5) Tr 2 Gate (6) Tr 1 Drain *1 ESD Protect *2 Pody Died	the the transformed at the trans	(4) • • • • • • • • • • • • • • • • • • •
		•Packa	Packing	cifications	Embosse Tape
<ul> <li>Application</li> </ul>			Reel size	e (mm)	180
Switching		Туре	Tape wic	lth (mm)	8
			Basic or	dering unit (pcs)	8000
			Taping c	ode	T2R
			Marking		K01
•Absolute maximum ratin	$\mathbf{g} \in (\mathbf{T}_3 + 25^\circ \text{C})$ , unless other	wise specified)	<tr1 and="" t<="" td=""><td>r2&gt;</td><td></td></tr1>	r2>	
Para	neter	-	nbol	Value	Unit
Dr n - Ource voltage			DSS	20	V
Continuous drain current			D	±100	mA
Pulsed drain current			*1 )P	±400	mA
Gate - Source voltage		V	GSS	±8	V
Power dissipation	total element	— Р	D <sup>*2</sup>	150 120	mW
					-

Operating junction and storage temperature range

Junction temperature

150

-55 to +150

°C

°C

T<sub>j</sub> T<sub>stg</sub>

## •Thermal resistance

Parameter			Symbol		Values		- 1	
			Symbol	Min.	Тур.	Max.		
Thermal resistance, junction ·	ambient		total	R <sub>thJA</sub>	-	-	-	
	ambion		element	• • • INJA	-	-		
•Electrical characteristics	(T <sub>a</sub> = 25°C	<b>;) &lt;</b> Tr	1 and Tr2>					
Parameter	Symbol		Conditi	ons		Values		
Farameter	Symbol		Conditi	0115	Min.	тур.	Max.	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> :	= 0V, I <sub>D</sub> = 1	mA	5	-	-	
Breakdown voltage	ΔV <sub>(BR)DSS</sub>	I <sub>D</sub> = ′	1mA			29.0		n
temperature coefficient	ΔTj	refere	enced to 25	i°C	-	20.0		П
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> :	= 20V, V	= JV			1	
Gate - Source leakage current	I <sub>GSS</sub>	V <sub>DS</sub>	0V, V <sub>GS</sub> =	±8V		-	±10	
Gate threshold voltage	V <sub>GS(th</sub> )	VDS	– 10V, I <sub>D</sub> =	10044	0.3	-	1.0	
Gate threshold voltage		י = <i>ר</i> י			-	-1.6	-	n
temperature coefficient			enced to 2					_
		V <sub>GS</sub> :	= 4.5V, I <sub>D</sub> =		-	2.5	3.5	_
Static drain - source		VGS	= 2.5V, I <sub>D</sub> =		-	3.0	4.2	_
on - state resistance	R <sub>DS(on)</sub> *3		= 1.8V, I <sub>D</sub> =		-	3.8	5.3	_
		V <sub>GS</sub> :	= 1.5V, I <sub>D</sub> =	20mA	-	4.5	9.0	
		V <sub>GS</sub> :	= 1.2V, I <sub>D</sub> =	10mA	-	6.0	18.0	
Fc va Transfer A. "ance	Y <sub>fs</sub>  *3	V <sub>DS</sub> :	= 10V, I <sub>D</sub> =	100mA	180	-	-	



## •Electrical characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

Parameter	Sumbol	Conditions			Unit		
Parameter			Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	7.1	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	3.3		pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	1.7			
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} \simeq 10V, V_{GS} = 4.5V$	-	5	-		
Rise time	t <sub>r</sub> *3	I <sub>D</sub> = 50mA	-	4	-	20	
Turn - off delay time	t <sub>d(off)</sub> *3	R <sub>L</sub> = 200Ω		20	-	ns	
Fall time	t <sub>f</sub> *3	R <sub>G</sub> = 10Ω		38	-		

## • Body diode electrical characteristics (Source-Drain) (T = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Va	alues		Unit
			T	Гур.	Max.	Ofin
Forward voltage	V <sub>SD</sub> *3	$V_{GS} = 0V, I_S = 100 m^2$		-	1.2	V

## \*1 Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

- \*2 Each terminal mounted on a reference land.
- \*3 Pulsed



## Electrical characteristic curves

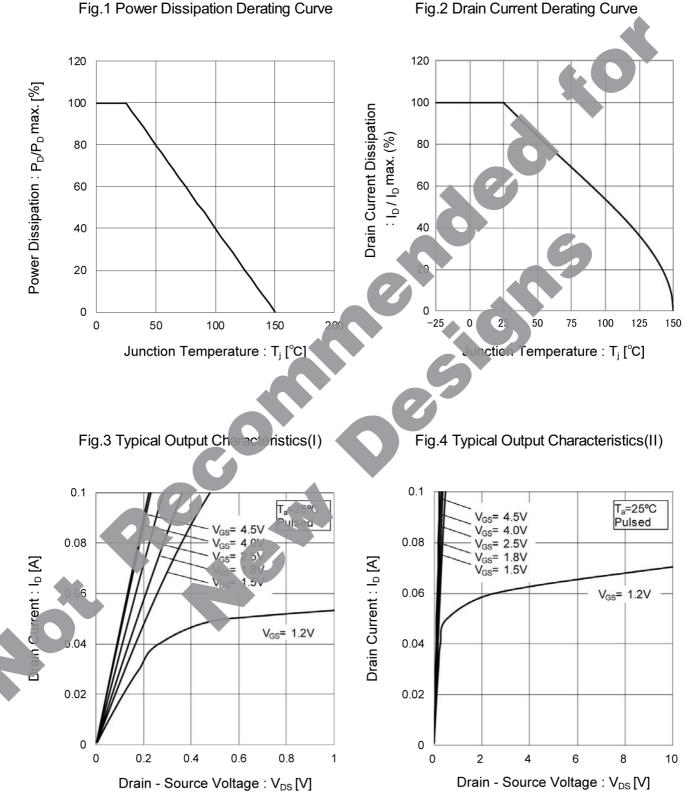
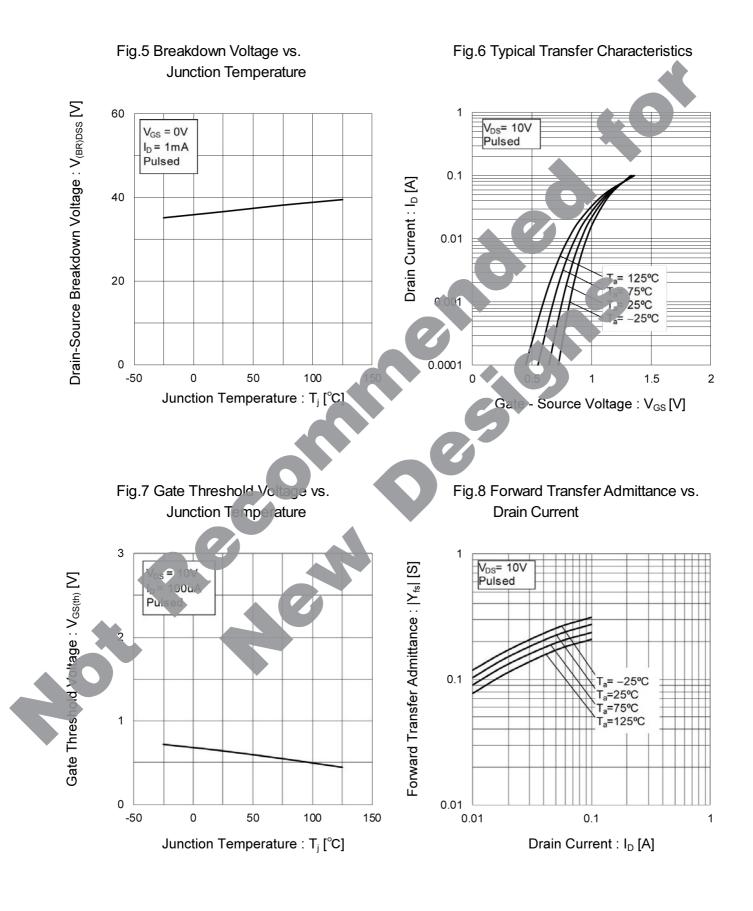


Fig.1 Power Dissipation Derating Curve



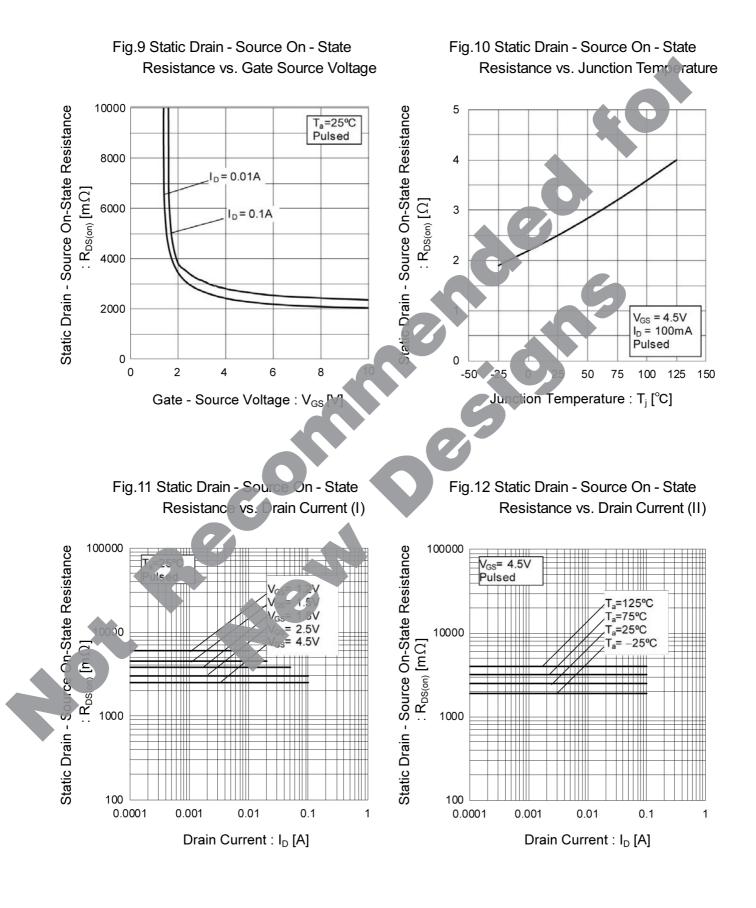
## • Electrical characteristic curves





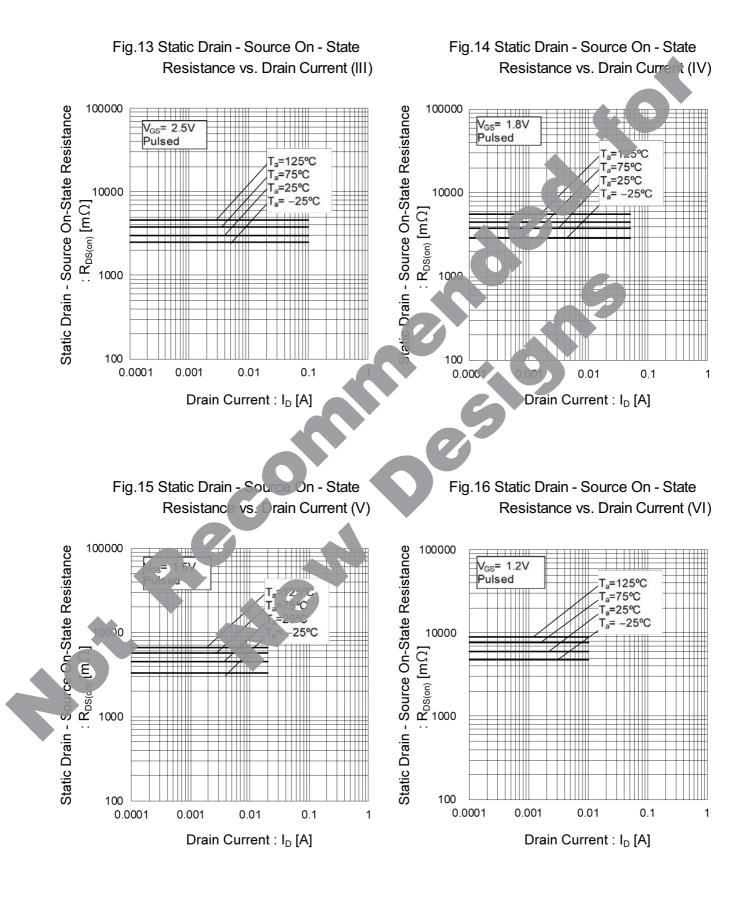


## • Electrical characteristic curves



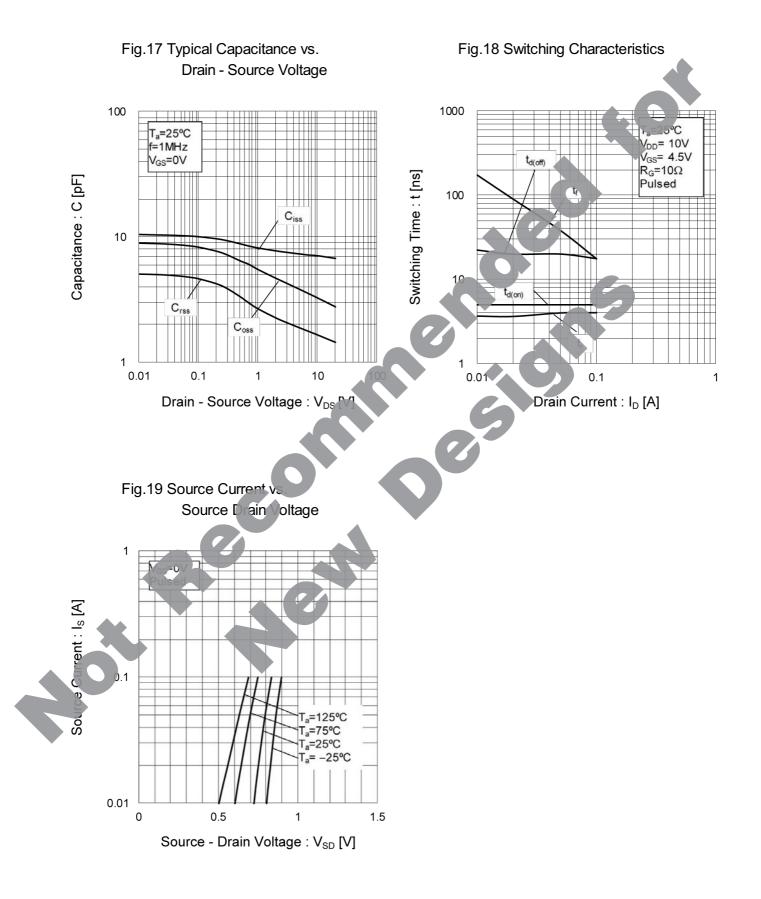


## •Electrical characteristic curves





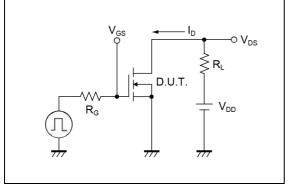
## • Electrical characteristic curves



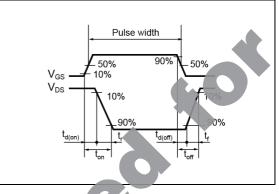


## Measurement circuits







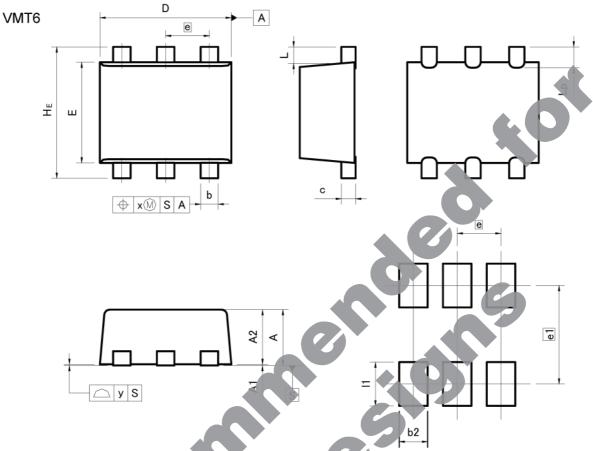


## Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	0.45	0.55	0.018	0.022
A	0.00	0.05	0.000	0.002
A2	0.40	0.60	0.016	0.024
b	0.11	0.21	0.004	0.008
С	0.08	0.18	0.003	0.007
D	1.152	1.248	0.045	0.049
F	0.82	1.02	0.032	0.04
e	0.4	40	0.0	16
HE	1.152	1.248	0.045	0.049
L	0.	14	0.0	06
Lp	0.10	0.30	0.004	0.012
x		0.05	1E	0.002
У		0.10	(1 <del></del> )	0.004

DIM	MILIMETERS		INC	HES
	MIN	MAX	MIN	MAX
b2		0.26	)—	0.010
e1	0.90		0.0	035
11		0.40		0.016

Dimension in mm/inches

20%



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CLASSⅣ	CLASS III	CLASSⅢ	CLASS

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   [c] Use of our Products in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to direct sunlight or dust

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