

# Power management (dual transistors)

#### **VT6X11**

#### Structure

NPN silicon epitaxial planar transistor

#### Features

- 1) Very small package with two transistors.
- 2) Suitable for current mirror circuits.

#### Applications

Current mirror circuits

#### Packaging specifications

	Package	Taping
	Code	T2R
Туре	Basic ordering unit (pieces)	8000
VT6X11		0

#### Absolute maximum ratings (Ta=25°C)

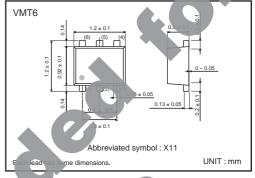
Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	20	V
Collector-emitter voltage	VCEO	20	V
Emitter-base voltage	VEBO	5	V
Collector current	lo	200	mA
Collector current	CP *1	400	mA
Down discipation Total	Pp *2	150	mW
Power dissipation	10	120	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

<sup>\*1</sup> Pw=1mS S

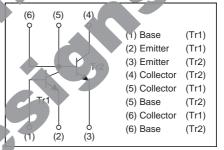
#### Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVceo	20	_	_	V	Ic=1mA
Collector-base breakdown voltage	ВУсво	20	_	_	V	Ic=50μA
Emitter-base breakdown voltage	ВУево	5	_	_	V	Iε=50μA
Collector cut-off current	Ісво	_	_	0.1	μΑ	Vcb=20V
Emitter cut-off current	Іево	_	_	0.1	μΑ	V <sub>EB</sub> =5V
Collector-emitter saturation voltage	VCE(sat)	_	0.12	0.30	V	Ic=100mA, I <sub>B</sub> =10mA
DC current gain	hfe	120	_	560	_	Vce=2V, Ic=1mA
DC current gain ratio	hfe (Tr1) / hfe (Tr2)	0.9	_	1.1	_	Vce=2V, Ic=1mA
Transition frequency	f⊤	_	400	_	MHz	VcE=10V, IE=-10mA, f=100MHz
Output capacitance	Cob	ı	2	_	pF	Vcb=10V, IE=0A, f=1MHz

#### ● Dimensions (Unit: mm)



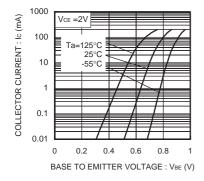
#### Inner circuit

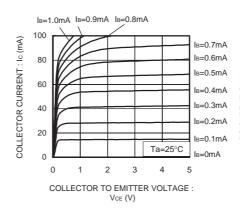


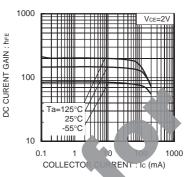
d on a recommended land \*2 Each terminal

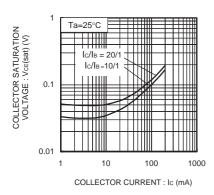
**VT6X11 Data Sheet** 

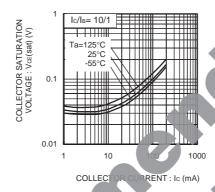
#### •Electrical characteristics curves

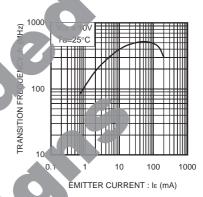


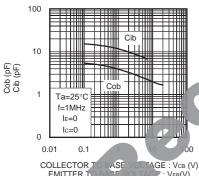












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