

# High Efficiency Power Inductor (HEPI) Specification Sheet



#### CIGT201610LMR68MNE (2016 / EIA 0806)

#### APPLICATION

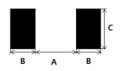
Mobile Phones, Tablet, LCD & AMOLED Display, Storage etc



#### FEATURES

High Current Type Low DC resistance Magnetically shielded structure Free of all RoHS-regulated substances Monolithic structure for high reliability

### RECOMMENDED LAND PATTERN



	Unit : mm
TYPE	2016
A	0.8
В	0.8
С	1.8

#### DIMENSION



TYPE	Dimension [mm]						
TIFE	L	W	T	D			
2016	2.0±0.2	1.6±0.2	1.0 max	0.5±0.2			

#### DESCRIPTION

Part no.	Size	Thickness	Inductance	Inductance	DC Resist	ance [mΩ]	Rated DC Cu	rrent (Isat) [A]	Rated DC C	urrent (Irms) \]
rait iio.	[inch/mm]	[mm] (max)	[uH]	tolerance (%)	Max.	Тур.	Max.	Тур.	Max.	Тур.
CIGT201610LMR68MNE	0806/2016	1.0	0.68	±20	53	46	3.2	3.8	2.7	2.9

- \* Inductance : Measured with a LCR meter 4991A(Agilent) or equivalent (Test Freq. 1MHz, Level 0.1V)
- \* DC Resistance : Measured with a Resistance HI-TESTER 3541(HIOKI) or equivalent
- \* Maximum allowable DC current: Value defined when DC current flows and the initial value of inductance has decreased by 30% or when current flows and temperature has risen to 40°C whichever is smaller. (Reference: ambient temperature is 25°C±10)

(Isat) : Allowable current in DC saturation : The DC saturation allowable current value is specified when the decrease of

the initial inductance value at 30% (Reference: ambient temperature is 25°C±10)

(Irms) : Allowable current of temperature rise : The temperature rise allowable current value is specified when temperature of

the inductor is raised 40°C by DC current. (Reference: ambient temperature is 25°C  $\pm 10)$ 

- \* Absolute maximum voltage : Absolute maximum voltage DC 20V.
- $^{\star}$  Operating temperature range : -40 to +125  $^{\circ}\text{C}$  (Including self-temperature rise)

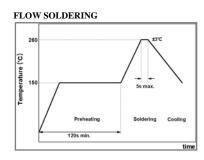
#### PRODUCT IDENTIFICATION

<u>CIG</u>	$\underline{\mathbf{T}}$	<u>2016</u>	<u>10</u>	$\underline{\mathbf{L}\mathbf{M}}$	<u>R68</u>	$\underline{\mathbf{M}}$	<u>N</u>	$\underline{\mathbf{E}}$
<b>(1)</b>	<b>(2)</b>	<b>(3)</b>	<b>(4)</b>	(5)	<b>(6)</b>	<b>(7</b> )	<b>(8)</b>	(9)

- (1) Power Inductor
- (3) Dimension (2016: 2.0mm ×1.6 mm)
- (5) Remark (Characterization Code)
- (7) Tolerance (M:±20%)
- (8) Internal Code
- (9) Packaging (C:paper tape, E:embossed tape)
- (2) Type (B: Automotive Type)
- (4) Thickness (10: 1.0mm)
- (6) Inductanc (R68: 0.68 uH)

#### RECOMMENDED SOLDERING CONDITION

# REFLOW SOLDERING 280 230 100 100 max. Preheating 60 ~ 120s Soldering Cooling 30 ~ 60s



IRON SOLDERING	
Temperature of	280°C max.
Soldering Iron Tip	200 Ciliax.
Preheating	150 °C min.
Temperature	130 CIIIII.
Temperature	ΔT≤130 °C
Differential	$\Delta 1 \simeq 130 \text{ C}$
Soldering Time	3sec max.
Wattage	50W max.

#### PACKAGING

Packaging Style	Quantity(pcs/reel)
Embossed Taping	3000 pcs

Item	Specified Value	Test Condition			
Solderability	More than 90% of terminal electrode should be soldered newly.	After being dipped in flux for $4\pm1$ seconds, and preheated at $150\sim180^{\circ}$ for $2\sim3$ min, the specimen shall be immersed in solder at $245\pm5^{\circ}$ for $4\pm1$ seconds.			
Resistance to Soldering	No mechanical damage. Remaining terminal Electrode: 75% min. Inductance change to be within ±20% to the initial.	After being dipped in flux for 4±1 seconds, and preheated at $150 \sim 180  ^{\circ}\!$			
Thermal Shock (Temperature Cycle test)	No mechanical damage Inductance change to be within ±20% to the initial.	Repeat 100 cycles under the following conditions. -40±3°C for 30 min → 85±3°C for 30 min			
High Temp. Humidity Resistance Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2 ℃, 85%RH, for 500±12 hours.  Measure the test items after leaving at normal temperature and humidity for 24 hours.			
Low Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PCB. Exposure at -55±2°C for 500±12 hours.  Measure the test items after leaving at normal temperature and humidity for 24hours.			
High Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PCB. Exposure at 125±2°C for 500±12 hours.  Measure the test items after leaving at normal temperature and humidity for 24hours.			
High Temp. Humidity Resistance Loading Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, Rated Current for 500±12 hours.  Measure the test items after leaving at normal temperature and humidity for 24 hours.			
High Temperature Loading Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, Rated Current for 500±12 hours. Measure the test items after leaving at normal temperature and humidity for 24 hours.			
Reflow Test	No mechanical damage Inductance change to be within ±20% to the initial	Peak 260±5 ℃, 3 times			
Vibration Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PCB. Vibrate as apply 10~55Hz, 1.5mm amplitude for 2 hours in each of three(X,Y,Z) axis (total 6 hours).			
	No mechanical damage	Bending Limit; 2mm Test Speed; 1.0mm/sec. Keep the test board at the limit point in 5 sec. PCB thickness: 1.6mm			
Bending Test	45	20 Unit ;mm  R340			
	No indication of peeling shall occur on the terminal electrode.	W(kgf) TIME(sec) 0.5 10±1			
Terminal Adhesion Test		w w			
Drop Test	No mechanical damage Inductance change to be within ±20% to the initial.	Random Free Fall test on concrete plate. 1 meter, 10 drops			
lpeak (AC+DC Load Life)	No mechanical damage Inductance change to be within ±20% to the initial	85±2℃, 85%RH, Load(Ipeak) for 120 hours. (Frequncy:1MHz, Load(Ipeak):1.5hr on / 0.5hr off) Measure the test items after leaving at normal temperature and humidity for 24 hours.  * Load(Ipeak) = Irms(max)×1.4			



## High Efficiency Power Inductor (HEPI) Data Sheet



#### 1. Model: CIGT201610LMR68MNE

#### 2. Description

Part no.	Size	Thickness	Inductance tolerance	Inductance	DC Resist	ance [mΩ]	Rated DC Cu	rrent (Isat) [A]	Rated DC C	
Fait no.	[inch/mm]	[mm] (max)	[uH]	(%)	Max.	Тур.	Max.	Тур.	Max.	Тур.
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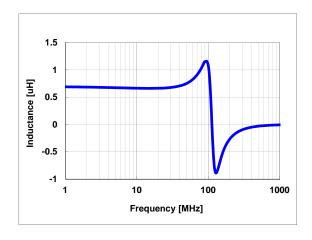
(Irms) : Allowable current of temperature rise : The temperature rise allowable current value is specified when temperature of the inductor is raised 40℃ by DC current. (Reference: ambient temperature is 25℃±10)

- \* Absolute maximum voltage : Absolute maximum voltage DC 20V.
- \* Operating temperature range : -40 to +125°C (Including self-temperature rise)

#### 3. Characteristics data

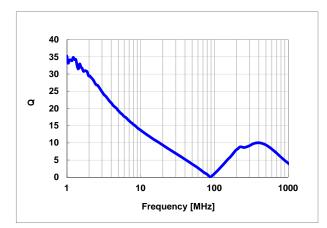
#### 1) Frequency characteristics (Ls)

Agilent E4294A +E4991A , 1MHz to 1,000MHz

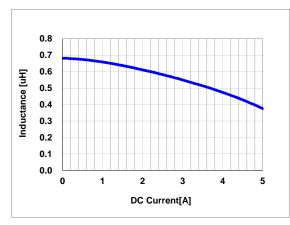


#### 2) Frequency characteristics (Q)

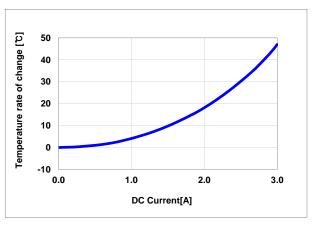
Agilent E4294A +E4991A , 1MHz to 1,000MHz



#### 3) DC Bias characteristics (Typ.)



#### 4) Temperature characteristics (Typ.)





Any data in this sheet are subject to change, modify or discontinue without notice The data sheets include the typical data for design reference only. If there is any question regarding the data sheets, please contact our sales personnel or application engineers

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CR54NP-820KC CR54NP-8R5MC 70F224AI MGDQ4-00004-P MHL1ECTTP18NJ MHQ1005P10NJ MHQ1005P1N0S MHQ1005P2N4S
MHQ1005P3N6S MHQ1005P5N1S MHQ1005P8N2J PE-51506NL PE-53601NL PE-53602NL PE-53630NL PE-53824SNLT PE-92100NL
PG0434.801NLT PG0936.113NLT 9220-20 9310-16 PM06-2N7 PM06-39NJ A01TK 1206CS-471XJ HC2LP-R47-R HC2-R47-R HC32R2-R HCF1305-3R3-R 1206CS-151XG RCH664NP-140L RCH664NP-4R7M RCH8011NP-221L RCP1317NP-332L RCP1317NP-391L