

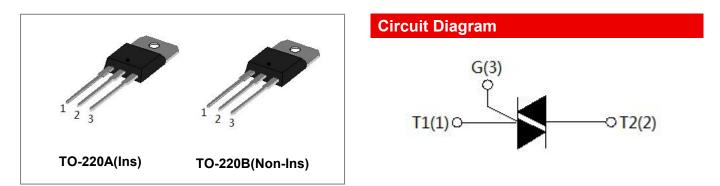
SST16 Series

Po

RoHS

Technical Data Data Sheet N2102, Rev. A

SST16 Series 16A TRIACs



Description

With high ability to withstand the shock loading of large current, SST16 series triacs provide high dv/dt rate with strong resistance to electromagnetic interface. With high commutation performances, 3 quadrants products especially recommended for use on inductive load.

Maximum Ratings:

Characteristics	Symbol	Condition	Max.	Units
Storage junction temperature range	T _{stg}	-	-40-150	°C
Operating junction temperature range	Tj	-	-40-125	°C
Repetitive peak off-state voltage(Tj=25 $^\circ\!\mathrm{C}$)	Vdrm	-	600/800	V
Repetitive peak reverse voltage(Tj=25 $^{\circ}$ C)	V _{RRM}	-	600/800	V
Non repetitive surge peak Off-state voltage	V _{DSM}	-	V _{DRM} +100	V
Non repetitive peak reverse voltage	V _{RSM}	-	V _{RRM} +100	V
RMS on-state current	I _(TRMS)	TO-220A(Ins)(T _C =86℃)	16	А
		TO-220B(Non-Ins)(T _C =107℃)	10	
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I _{TSM}	-	160	А
I ² t value for fusing (tp=10ms)	l ² t	-	128	A ² s
Critical rate of rise of on-state current $(I_G=2 \times I_{GT})$	dl/dt	-	50	A/µs
Peak gate current	I _{GM}	-	4	Α
Average gate power dissipation	P _{G(AV)}	-	1	W
Peak gate power	Р _{GM}	-	5	W

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Electrical Characteristics(Tj=25°C unless otherwise specified)

3 Quadrants

Symbol	Test Condition	Quadrant		Value		Unit
Symbol	Test Condition	Quadrant		BW	CW	Unit
I _{GT}	V -12V D -220	I - II -III	MAX	50	35	mA
V _{GT}	V_{GT} V_{D} =12V R _L =33Ω		MAX	1.3		V
V _{GD}	V _D =V _{DRM} T _j =125℃ R∟ =3.3KΩ	I - II -III	MIN	0.2		V
		I -III	I -III MAX	70	50	mA
IL IL	I _G =1.2I _{GT}	II		80	60	
Iн	I _T =100mA		MAX	60	40	mA
dV/dt	V_D =2/3 V_{DRM} Gate Open T _j =125 $^{\circ}$ C		MIN	1000	500	V/µs

4 Quadrants

Symphol	Test Condition	Quedrant		Value		Unit	
Symbol	Test Condition	Quadrant		В	С	Unit	
1		I - II -III	NAAX	50	25	mA	
Igt	$V_D = 12V R_L = 33\Omega$	IV	MAX	70	50		
Vgt		ALL	MAX	1.5		V	
V _{GD}	$V_D = V_{DRM} T_j = 125$ °C ALL ALL		MIN	0.2		V	
		I -III-IV	MAX	70	50	mA	
l l	$I_G = 1.2I_{GT}$	II		100	80		
I _H	I _T =100mA		MAX	60	40	mA	
dV/dt	V _D =2/3V _{DRM} Gate Open T _j =125℃		MIN	500	200	V/µs	

Static Characteristics

Symbol	Parameter		Value(MAX)	Unit
V _{TM}	I _™ =22.5A tp=380µs	Tj=25℃	1.5	V
IDRM	V _D =V _{DRM} V _R =V _{RRM}	Tj=25℃	5	μA
I _{RRM}		T j =125 ℃	2	mA

Thermal Resistances

Symbol	Condition		Value	Units
Rth(j-c) Junction	lumetice to coop(AC)	TO-220A(Ins)	2.1	°C/W
	Junction to case(AC)	TO-220B(Non-Ins)	1.2	°C /W

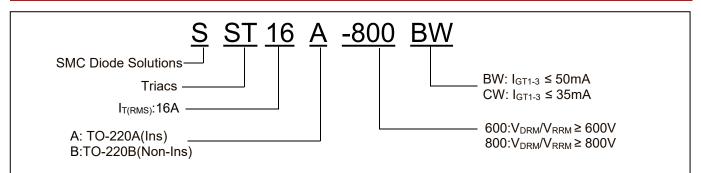
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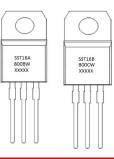


Ordering Information



Device	Package	Shipping
SST16A-600CW, SST16A-600BW SST16A-800CW, SST16A-800BW	TO-220A(Ins)	50pcs/ Tube
SST16B-600CW, SST16B-600BW SST16B-800CW, SST16B-800BW	TO-220B(Non-Ins)	50pcs/ Tube

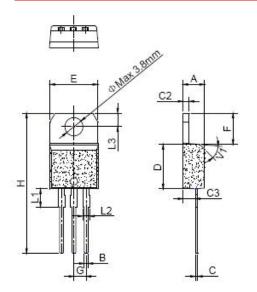
Marking Diagram



Where XXXXX is YYWWL

SST16A-800BW	= Part name
YY	= Year
WW	= Week
L	= Lot Number

Mechanical Dimensions TO-220A(Ins)



SYMBOL	Millimeters				Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
A	4.40		4.60	0.173		0.181
В	0.61		0.88	0.024		0.035
С	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.60		10.4	0.378		0.409
F	6.55		6.95	0.258		0.274
G		2.54			0.1	
Н	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

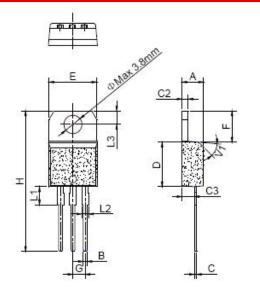
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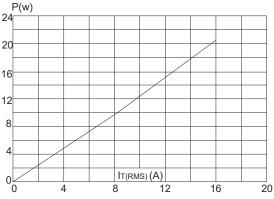
Mechanical Dimensions TO-220B(Non-Ins)

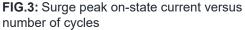


SYMBOL	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	4.40		4.60	0.173		0.181
В	0.61		0.88	0.024		0.035
С	0.46		0.70	0.018		0.028
C2	1.21		1.32	0.048		0.052
C3	2.40		2.72	0.094		0.107
D	8.60		9.70	0.339		0.382
E	9.60		10.4	0.378		0.409
F	6.20		6.60	0.244		0.260
G		2.54			0.1	
Н	28.0		29.8	1.102		1.173
L1		3.75			0.148	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
V1		45°			45°	

Ratings and Characteristics Curves

FIG.1 Maximum power dissipation versus RMS on-state current





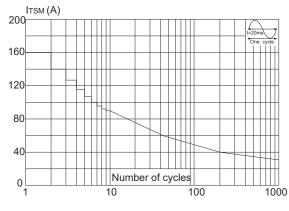


FIG.2: RMS on-state current versus case temperature

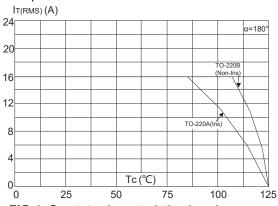
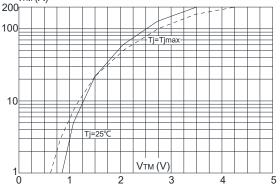


FIG.4: On-state characteristics (maximum values)





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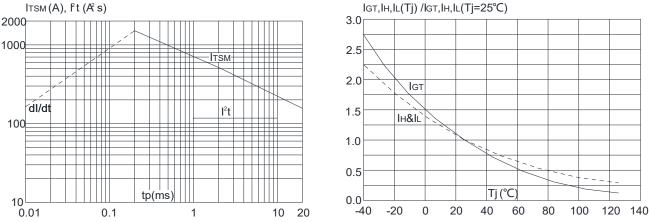


FIG.6: Relative variations of gate trigger current,

holding current and latching current versus

junction temperature

FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of 1 t (dl/dt < 50Aus)



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