

SEMITRANS[®] 4

SKM800GA125D

Features

- Homogeneous Si
- NPT-IGBT
- V_{CE(sat)} with positive temperature coefficient
- + High short circuit capability, self limiting to 6 x ${\rm I_C}$

Typical Applications*

- Resonant inverters up to 100 kHz
- Inductive heating
- Electronic welders at f_{sw} > 20 kHz

Remarks

- $I_{DC} \le 500$ A limited by terminals
- Take care of over-voltage caused by stray inductances



Absolut	e Maximum Ratir	igs		
Symbol	Conditions		Values	Unit
IGBT				
V _{CES}	T _j = 25 °C		1200	V
lc	T _j = 150 °C	T _c = 25 °C	760	А
		T _c = 80 °C	530	А
I _{Cnom}		-	600	A
I _{CRM}	$I_{CRM} = 2 x I_{Cnom}$		1200	А
V _{GES}			-20 20	V
t _{psc}	V _{CC} = 600 V V _{GE} ≤ 15 V V _{CES} ≤ 1200 V	T _j = 125 °C	10	μs
Tj			-40 150	°C
Inverse	diode			•
l _F	T 150 °C	T _c = 25 °C	720	А
	$-1_j = 150$ °C	T _c = 80 °C	500	A
I _{Fnom}			600	А
I _{FRM}	I _{FRM} = 2xI _{Fnom}		1200	А
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		5760	A
Tj			-40 150	°C
Module				•
I _{t(RMS)}			500	А
T _{stg}			-40 125	°C
V.	AC sinus 50 Hz, t = 1 min		4000	V

Symbol	Conditions		min.	typ.	max.	Unit		
IGBT								
V _{CE(sat)}	I _C = 600 A V _{GE} = 15 V chiplevel	T _j = 25 °C		3.20	3.70	V		
		T _j = 125 °C		4.00	4.80	V		
V _{CE0}	chinlevel	T _j = 25 °C		1.50	1.75	V		
	chiplevel	T _j = 125 °C		1.70	1.95	V		
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		2.83	3.25	mΩ		
		T _j = 125 °C		3.83	4.75	mΩ		
V _{GE(th)}	$V_{GE}=V_{CE}, I_C=24 \text{ mA}$		4.5	5.5	6.5	V		
I _{CES}	$V_{GE} = 0 V$	T _j = 25 °C			0.6	mA		
	V _{CE} = 1200 V			-		mA		
Cies	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		37.2		nF		
Coes		f = 1 MHz		5.6		nF		
C _{res}		f = 1 MHz		2.80		nF		
Q_{G}	V _{GE} = - 8 V+ 15 V			4200		nC		
R _{Gint}	T _j = 25 °C			0.5		Ω		
t _{d(on)}	$V_{CC} = 600 V$ $I_{C} = 600 A$ $V_{GE} = +15/-15 V$ $R_{G on} = 0.5 \Omega$ $R_{G off} = 0.5 \Omega$	T _j = 125 °C		480		ns		
t _r		T _j = 125 °C		116		ns		
Eon		T _j = 125 °C		88		mJ		
t _{d(off)}		T _j = 125 °C		666		ns		
t _f		T _j = 125 °C		58		ns		
E _{off}		T _j = 125 °C		48		mJ		
R _{th(j-c)}	per IGBT				0.03	K/W		



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Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Inverse d	iode		•			•	
$V_F = V_{EC}$	$I_{\rm F} = 600 {\rm A}$	T _j = 25 °C		2.3	2.58	V	
V _{GE} = chipl	V _{GE} = 0 V chiplevel	T _j = 125 °C		1.87	2.38	V	
V _{F0}	chiplevel	T _j = 25 °C		1.10	1.45	V	
		T _j = 125 °C		0.85	1.20	V	
Γ _F	chiplevel	T _j = 25 °C		1.61	1.88	mΩ	
		T _j = 125 °C		1.70	1.96	mΩ	
I _{RRM}	I _F = 600 A	T _j = 125 °C		370		А	
Q _{rr}	V _{GF} = ±15 V	T _j = 125 °C		83		μC	
Err	$V_{CC} = 600 V$	T _j = 125 °C		28		mJ	
R _{th(j-c)}	per diode	I			0.07	K/W	
Module							
L _{CE}				15		nH	
R _{CC'+EE'}	terminal chin	T _C = 25 °C		0.18		mΩ	
	terminal-chip	T _C = 125 °C		0.22		mΩ	
R _{th(c-s)}	per module			0.02	0.038	K/W	
Ms	to heat sink M6		3		5	Nm	
Mt	to torminals	M6	2.5		5	Nm	
		M4	1.1		2	Nm	
W					330	g	



























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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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