## 1N4148W

## Silicon Epitaxial Planar Switching Diode

## Features

- SOD-123 package
- Fast switching
- These diodes are also available in other case style including the DO-35 case with the type designation 1N4148, the MiniMELF case with the type designation LL4148 and the MicroMELF case

PINNING

| PIN | DESCRIPTION |
| :---: | :--- |
| 1 | Cathode |
| 2 | Anode |



Absolute Maximum Ratings $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Peak Reverse Voltage | $V_{\text {RM }}$ | 100 | V |
| Reverse Voltage | $\mathrm{V}_{\mathrm{R}}$ | 75 | V |
| Average Rectified Forward Current | $\mathrm{I}_{\text {( }}$ (AV) | 150 | mA |
| Non-repetitive Peak Forward Surge Current at $t=1 \mathrm{~s}$ <br>  at $t=1 \mathrm{~ms}$ <br>  at $t=1 \mu \mathrm{~s}$ | $\mathrm{I}_{\text {FSM }}$ | $\begin{gathered} 0.5 \\ 1 \\ 4 \\ \hline \end{gathered}$ | A |
| Power Dissipation | $\mathrm{P}_{\text {tot }}$ | 400 | mW |
| Thermal Resistance from Junction to Ambient Air | $\mathrm{R}_{\text {өJA }}$ | 312 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Junction Temperature | $\mathrm{T}_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -65 to + 150 | ${ }^{\circ} \mathrm{C}$ |

Characteristics at $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Min. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Reverse Breakdown Voltage at $I_{R}=1 \mu \mathrm{~A}$ | $V_{(B R) R}$ | 75 | - | V |
| $\begin{aligned} & \text { Forward Voltage } \\ & \text { at } I_{F}=1 \mathrm{~mA} \\ & \text { at } I_{F}=10 \mathrm{~mA} \\ & \text { at } I_{F}=50 \mathrm{~mA} \\ & \text { at } I_{F}=150 \mathrm{~mA} \end{aligned}$ | $V_{F}$ |  | $\begin{gathered} 0.715 \\ 0.855 \\ 1 \\ 1.25 \end{gathered}$ | V |
| $\begin{aligned} & \text { Peak Reverse Current } \\ & \text { at } \mathrm{V}_{R}=75 \mathrm{~V} \\ & \text { at } \mathrm{V}_{\mathrm{R}}=20 \mathrm{~V} \\ & \text { at } \mathrm{V}_{\mathrm{R}}=75 \mathrm{~V}, \mathrm{~T}_{J}=150^{\circ} \mathrm{C} \\ & \text { at } \mathrm{V}_{\mathrm{R}}=25 \mathrm{~V}, \mathrm{~T}_{J}=150^{\circ} \mathrm{C} \end{aligned}$ | $I_{R}$ |  | $\begin{gathered} 1 \\ 25 \\ 50 \\ 30 \end{gathered}$ | $\mu \mathrm{A}$ <br> nA <br> $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ |
| Total Capacitance at $\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\mathrm{T}}$ | - | 2 | pF |
| Reverse Recovery Time at $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{If}}=1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{R}}=6 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=100 \Omega$ | $t_{\text {rr }}$ | - | 4 | ns |



Power Dissipation vs Ambient Temperature


Reverse capacitance vs. reverse voltage




Leakage current vs. junction temperature

## PACKAGE OUTLINE



| UNIT | A | $\mathrm{b}_{\mathrm{p}}$ | c | D | E | $\mathrm{H}_{\mathrm{E}}$ | v | $\angle$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| mm | 1.15 | 0.6 | 0.135 | 2.7 | 1.65 | 3.85 | 0.2 | $5^{\circ}$ |

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