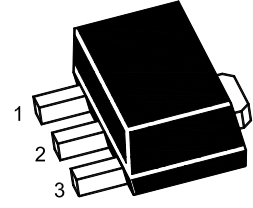


2SA1797U

PNP Silicon Epitaxial Planar Transistor

Medium power transistor



1.Base 2.Collector 3.Emitter
SOT-89 Plastic Package

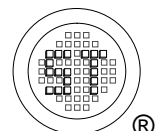
Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Collector Base Voltage	$-V_{\text{CBO}}$	50	V
Collector Emitter Voltage	$-V_{\text{CEO}}$	50	V
Emitter Base Voltage	$-V_{\text{EBO}}$	6	V
Collector Current	$-I_{\text{C}}$	2	A
Collector Current ($P_w = 20 \text{ ms}$)	$-I_{\text{CP}}$	3	A
Collector Power Dissipation	P_{C}	0.5	W
Junction Temperature	T_{j}	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient ¹⁾	$R_{\theta\text{JA}}$	250	$^\circ\text{C/W}$

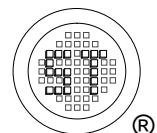
¹⁾ Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



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Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $-V_{CE} = 2\text{ V}$, $-I_C = 500\text{ mA}$ Current Gain Group	P h_{FE}	82	-	180	-
	Q h_{FE}	120	-	270	-
	R h_{FE}	180	-	390	-
Collector Base Cutoff Current at $-V_{CB} = 50\text{ V}$	$-I_{CBO}$	-	-	100	nA
Emitter Base Cutoff Current at $-V_{EB} = 5\text{ V}$	$-I_{EBO}$	-	-	100	nA
Collector Base Breakdown Voltage at $-I_C = 50\text{ }\mu\text{A}$	$-V_{(BR)CBO}$	50	-	-	V
Collector Emitter Breakdown Voltage at $-I_C = 1\text{ mA}$	$-V_{(BR)CEO}$	50	-	-	V
Emitter Base Breakdown Voltage at $-I_E = 50\text{ }\mu\text{A}$	$-V_{(BR)EBO}$	6	-	-	V
Collector Emitter Saturation Voltage at $-I_C = 1\text{ A}$, $-I_B = 50\text{ mA}$	$-V_{CE(sat)}$	-	-	0.35	V
Base Emitter Saturation Voltage at $-I_C = 1\text{ A}$, $-I_B = 50\text{ mA}$	$-V_{BE(sat)}$	-	-	1.1	V
Transition Frequency at $-V_{CE} = 2\text{ V}$, $-I_C = 500\text{ mA}$, $f = 100\text{ MHz}$	f_T	-	200	-	MHz
Collector Output Capacitance at $-V_{CB} = 10\text{ V}$, $f = 1\text{ MHz}$	C_{ob}	-	20	-	pF



Electrical Characteristics Curves

Fig. 1 Output Characteristics Curve

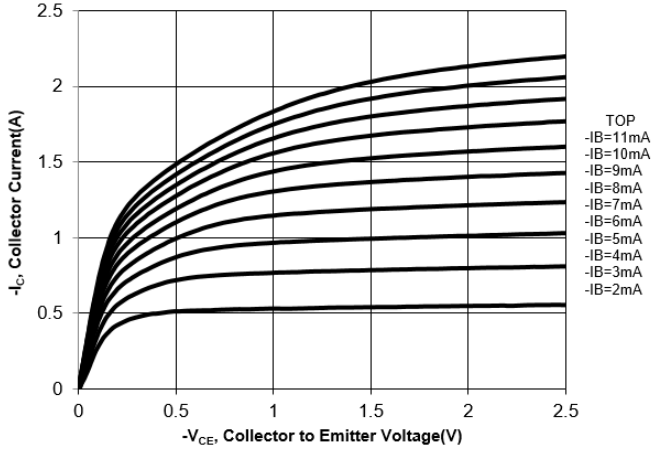


Fig 2. Power Derating Curve

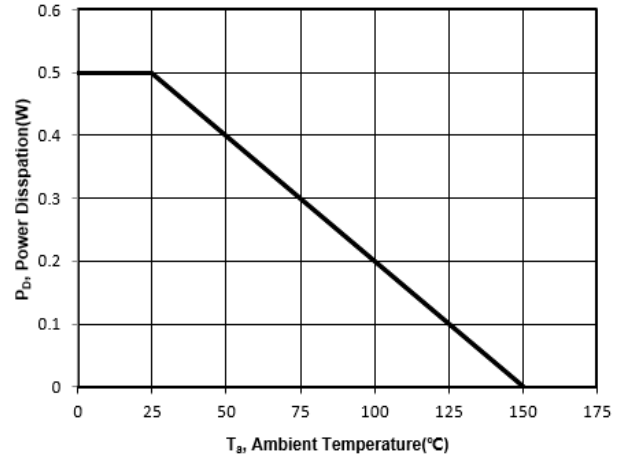


Fig. 3 Collector Current vs. V_{BE}

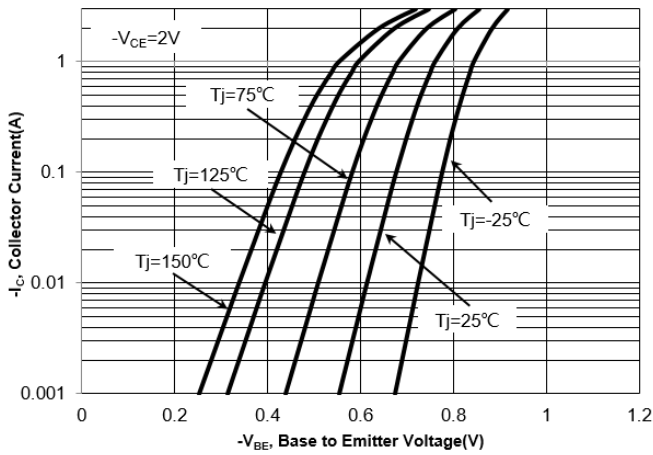
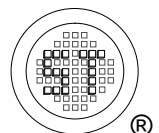
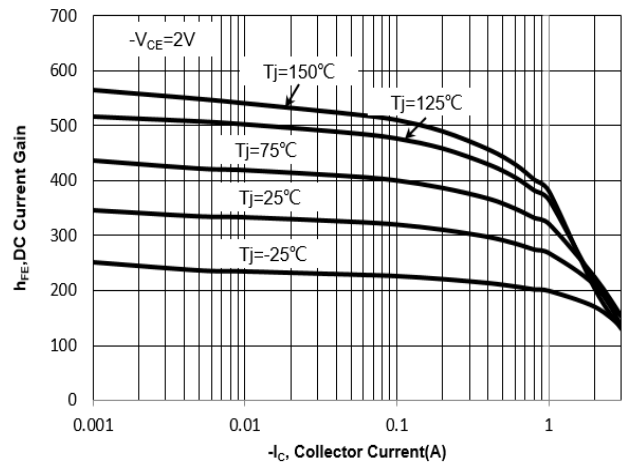


Fig 4. DC Current Gain vs. Collector Current



Electrical Characteristics Curves

Fig 5. $V_{BE(sat)}$ vs. Collector Current

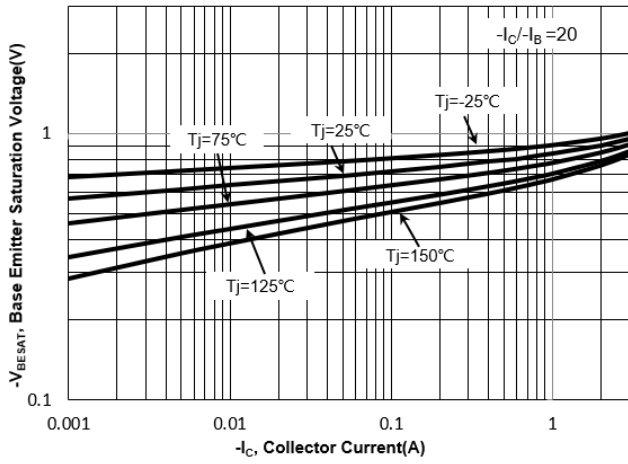


Fig 6. $V_{CE(sat)}$ vs. Collector Current

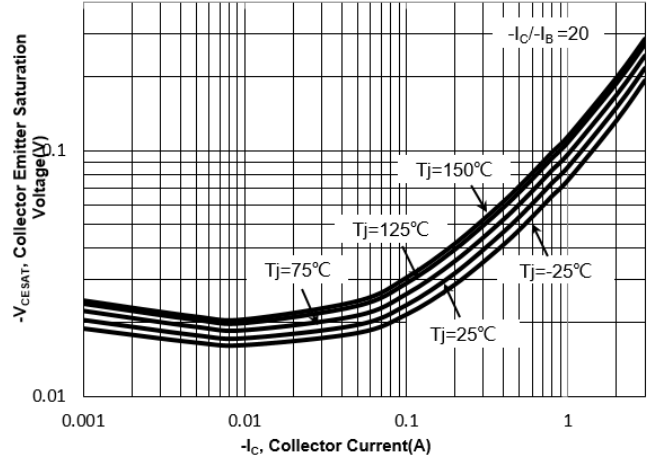
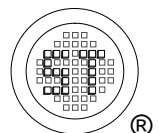
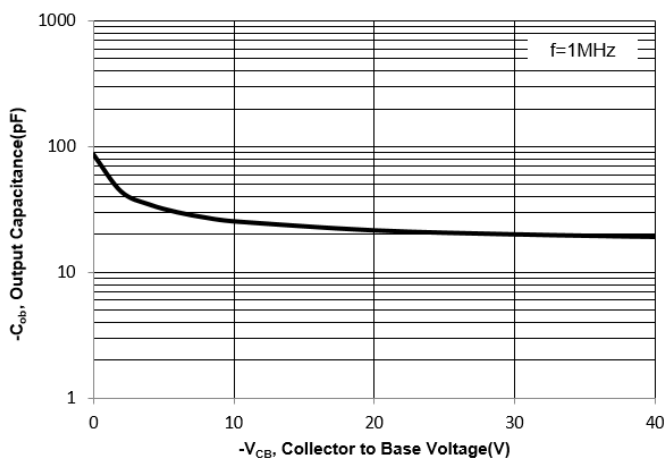


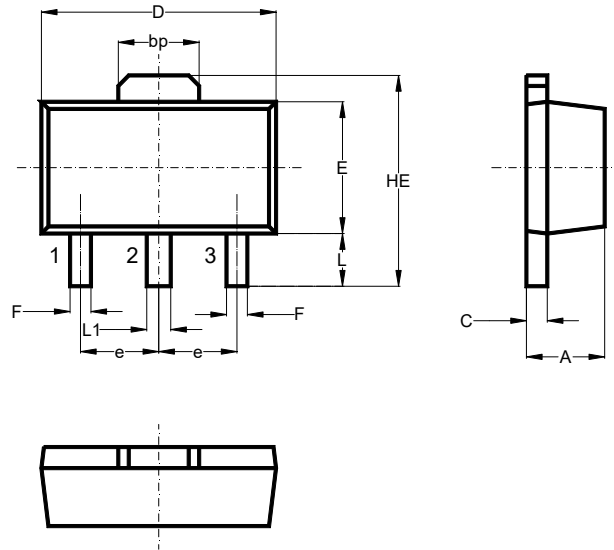
Fig 7. Output Capacitance



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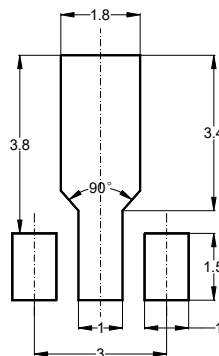
Package Outline (Dimensions in mm)

SOT-89



Unit	A	bp	C	D	E	F	HE	e	L	L1
mm	1.6	1.60	0.5	4.6	2.6	0.45	4.25	1.5	1.05	0.51
	1.4	1.50	0.3	4.4	2.4	0.35	3.75	typ.	0.95	0.41

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-89	12	8 ± 0.1	0.315 ± 0.004	178	7	1,000
				330	13	4,000

Marking information

" 2SA1797*U " = Part No. (" * " = HFE grouping Code)

"YM" = Date Code Marking

"Y" = Year

"M" = Month

Font type: Arial

