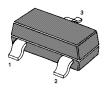
PNP Silicon Epitaxial Planar Transistor

for high voltage amplifier applications



Base 2. Emitter 3. Collector
 TO-236 Plastic Package

Absolute Maximum Ratings (T_a = 25 °C)

Symbol	Value	Unit
-V _{CBO}	160	V
-V _{CEO}	150	V
-V _{EBO}	5	V
-I _C	600	mA
P _{tot}	350	mW
T _j	150	°C
T _{stg}	- 55 to + 150	°C
	-V _{CBO} -V _{CEO} -V _{EBO} -I _C P _{tot}	-V _{CBO} 160 -V _{CEO} 150 -V _{EBO} 5 -I _C 600 -P _{tot} 350 -T _j 150

Characteristics at T_a = 25 °C

Parameter	Symbol	Min.	Max.	Unit
DC Current Gain at $-V_{CE} = 5 \text{ V}$, $-I_{C} = 1 \text{ mA}$ at $-V_{CE} = 5 \text{ V}$, $-I_{C} = 10 \text{ mA}$ at $-V_{CE} = 5 \text{ V}$, $-I_{C} = 50 \text{ mA}$	h _{FE} h _{FE} h _{FE}	50 60 50	- 240 -	- - -
Collector Base Cutoff Current at -V _{CB} = 120 V	-I _{CBO}	-	50	nA
Emitter Base Cutoff Current at -V _{EB} = 3 V	-I _{EBO}	-	50	nA
Collector Base Breakdown Voltage at $-I_C = 100 \mu A$	-V _{(BR)CBO}	160	-	V
Collector Emitter Breakdown Voltage at -I _C = 1 mA	-V _{(BR)CEO}	150	-	V
Emitter Base Breakdown Voltage at $-I_E = 10 \mu A$	-V _{(BR)EBO}	5	-	V
Collector Emitter Saturation Voltage at $-I_C = 10$ mA, $-I_B = 1$ mA at $-I_C = 50$ mA, $-I_B = 5$ mA	-V _{CE(sat)}	-	0.2 0.5	V
Base Emitter Saturation Voltage at $-I_C = 10$ mA, $-I_B = 1$ mA at $-I_C = 50$ mA, $-I_B = 5$ mA	-V _{BE(sat)}	-	1 1	V
Gain Bandwidth Product at $-V_{CE} = 10 \text{ V}$, $-I_C = 10 \text{ mA}$, $f = 100 \text{ MHz}$	f _T	100	300	MHz
Output Capacitance at $-V_{CB} = 10 \text{ V}$, f = 1 MHz	C _{obo}	-	6	pF



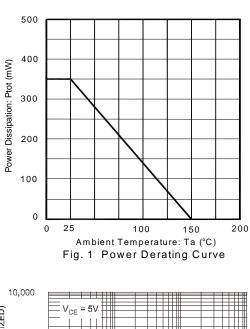


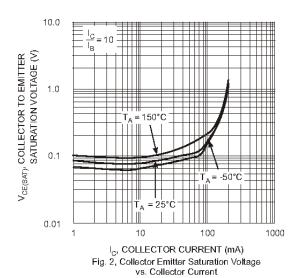


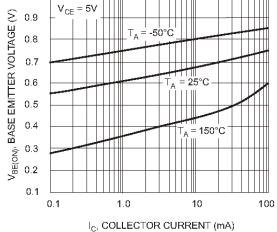












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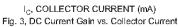
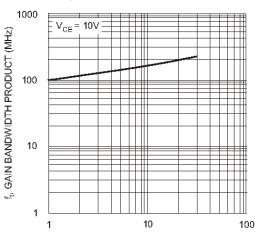


Fig. 4, Base Emitter Voltage vs. Collector Current



I_C, COLLECTOR CURRENT (mA)
Fig. 5, Gain Bandwidth Product vs Collector Current











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