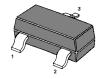
PNP Silicon Epitaxial Planar Transistors

for switching and amplifier applications.



 Base 2. Emitter 3. Collector TO-236 Plastic Package

Absolute Maximum Ratings (T_a = 25 °C)

Parameter	Symbol	Value	Unit
Collector Base Voltage	-V _{CBO}	40	V
Collector Emitter Voltage	-V _{CEO}	30	V
Emitter Base Voltage	-V _{EBO}	5	V
Collector Current	-l _C	500	mA
Power Dissipation	P _{tot}	200	mW
Junction Temperature	T _j	150	°C
Storage Temperature Range	T _{stg}	- 55 to + 150	°C

Characteristics at T₂ = 25 °C

Characteristics at T _a = 25 °C				
Parameter	Symbol	Min.	Max.	Unit
DC Current Gain at $-V_{CE} = 1 \text{ V}$, $-I_{C} = 50 \text{ mA}$ Current Gain Group G	h _{FF}	100	250	-
at $-V_{CE} = 1 \text{ V}$, $-I_C = 500 \text{ mA}$	h _{FE}	160 40	400	-
Collector Base Cutoff Current	h _{FE}	40	-	-
at -V _{CB} = 35 V	-I _{CBO}	-	100	nA
Emitter Base Cutoff Current at -V _{EB} = 5 V	-I _{EBO}	-	100	nA
Collector Base Breakdown Voltage at -I _C = 100 µA	-V _{(BR)CBO}	40	-	V
Collector Emitter Breakdown Voltage at -I _C = 1 mA	-V _{(BR)CEO}	30	-	V
Emitter Base Breakdown Voltage at $-I_E = 100 \mu A$	-V _{(BR)EBO}	5	-	V
Collector Emitter Saturation Voltage at $-I_C = 500$ mA, $-I_B = 50$ mA	-V _{CE(sat)}	ı	0.6	V
Base Emitter Saturation Voltage at $-I_C = 500$ mA, $-I_B = 50$ mA	-V _{BE(sat)}	ı	1.2	V
Base Emitter Voltage at $-V_{CE} = 1 \text{ V}$, $-I_{C} = 100 \text{ mA}$	-V _{BE}	-	1	V
Gain Bandwidth Product at $-V_{CE} = 6 \text{ V}$, $-I_{C} = 20 \text{ mA}$	f _⊤	100	-	MHz











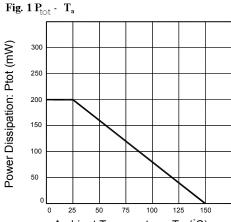
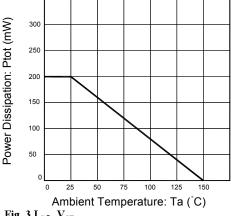


Fig. 3 $I_{\rm \,C}\text{--}\,V_{\rm CE}$



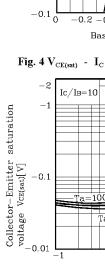


Fig. 2 $I_{\rm C}~$ - $~V_{\rm BE}$

 $V_{CE}=-1V$

-500 -300

-100

-30-10-5

-1.0

-0.5

Collector current Ic [mA]

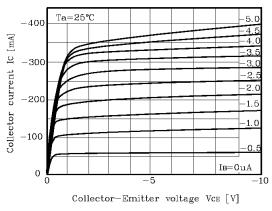


Fig. 5 $h_{FE}~$ - ~ $I_{\rm C}$

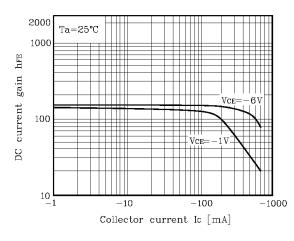
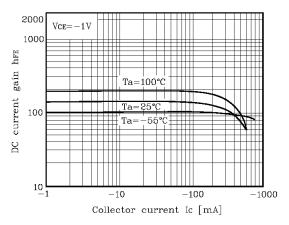


Fig. 6 $h_{FE}\,$ - $\,$ $\,I_{\rm C}$



-10

Collector current Ic [mA]

-100

-1000

-0.2 -0.4 -0.6 -0.8 -1.0 -1.2 -1.4 -1.6Base-Emitter voltage VBE [V]













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