

# MMBTRA101SS...MMBTRA106SS

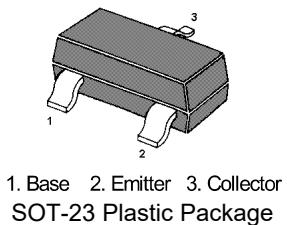
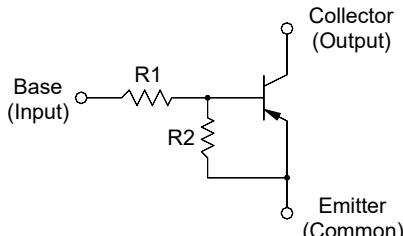
## PNP Silicon Epitaxial Planar Digital Transistor

for switching and interface circuit and drive circuit

applications

### Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process



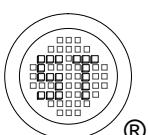
### Resistor Values

Type	R1 (KΩ)	R2 (KΩ)
MMBTRA101SS	4.7	4.7
MMBTRA102SS	10	10
MMBTRA103SS	22	22
MMBTRA104SS	47	47
MMBTRA105SS	2.2	47
MMBTRA106SS	4.7	47

### Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Value	Unit
Output Voltage	$-V_O$	50	V
Input Voltage	MMBTRA101SS	20, -10	V
	MMBTRA102SS	30, -10	
	MMBTRA103SS	40, -10	
	MMBTRA104SS	40, -10	
	MMBTRA105SS	12, -5	
	MMBTRA106SS	20, -5	
Output Current	$-I_O$	100	mA
Total Power Dissipation	$P_{tot}$	200	mW
Thermal Resistance - Junction to Ambient <sup>1)</sup>	$R_{\theta JA}$	625	°C/W
Junction Temperature	$T_j$	150	°C
Storage Temperature Range	$T_{stg}$	- 55 to + 150	°C

<sup>1)</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



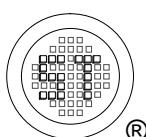
# MMBTRA101SS...MMBTRA106SS

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## Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $-V_O = 5 \text{ V}$ , $-I_O = 10 \text{ mA}$	$G_I$	30	-	-	-
		50	-	-	-
		70	-	-	-
		80	-	-	-
		80	-	-	-
		80	-	-	-
Output Cutoff Current at $-V_O = 50 \text{ V}$	$-I_{O(\text{OFF})}$	-	-	500	nA
Input Current at $-V_I = 5 \text{ V}$	$-I_I$	-	-	1.8	mA
		-	-	0.88	
		-	-	0.36	
		-	-	0.18	
		-	-	3.6	
		-	-	1.8	
Output Voltage at $-I_O = 10 \text{ mA}$ , $-I_I = 0.5 \text{ mA}$	$-V_{O(\text{ON})}$	-	-	0.3	V
Input Voltage (ON) at $-V_O = 0.2 \text{ V}$ , $-I_O = 5 \text{ mA}$	$-V_{I(\text{ON})}$	-	-	2	V
		-	-	2.4	
		-	-	3	
		-	-	5	
		-	-	1.1	
		-	-	1.3	
Input Voltage (OFF) at $-V_O = 5 \text{ V}$ , $-I_O = 0.1 \text{ mA}$	$-V_{I(\text{OFF})}$	1	-	-	V
		0.5	-	-	
Transition Frequency at $-V_O = 10 \text{ V}$ , $-I_O = 5 \text{ mA}$	$f_T^{(1)}$	-	200	-	MHz

<sup>1)</sup> Characteristic of transistor only.



# MMBTRA101SS...MMBTRA106SS

## Electrical characteristic curve (MMBTRA101SS)

Fig. 1 Output Current vs. Input On Voltage

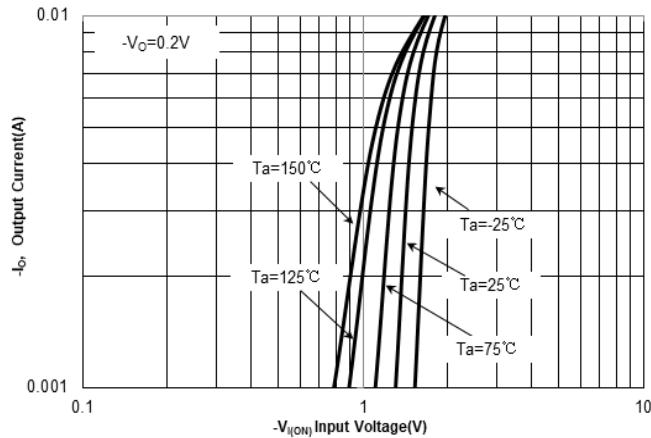


Fig. 2 Output Current vs. Input Off Voltage

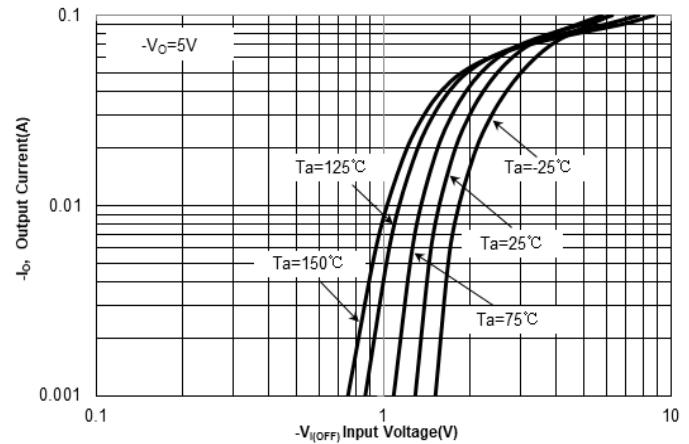


Fig. 3 DC Current Gain vs. Output Current

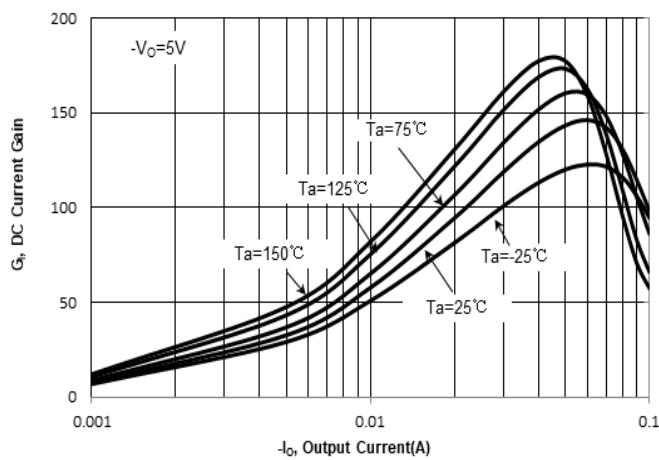
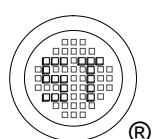
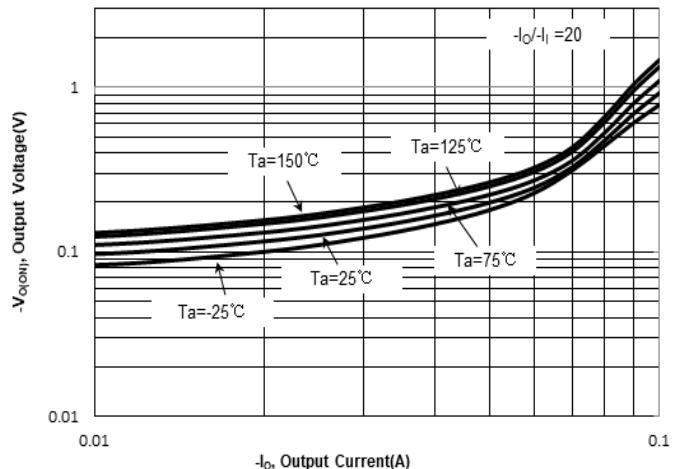


Fig. 4 Output Voltage vs. Output Current



# MMBTRA101SS...MMBTRA106SS

## Electrical characteristic curve (MMBTRA102SS)

Fig. 1 Output Current vs. Input On Voltage

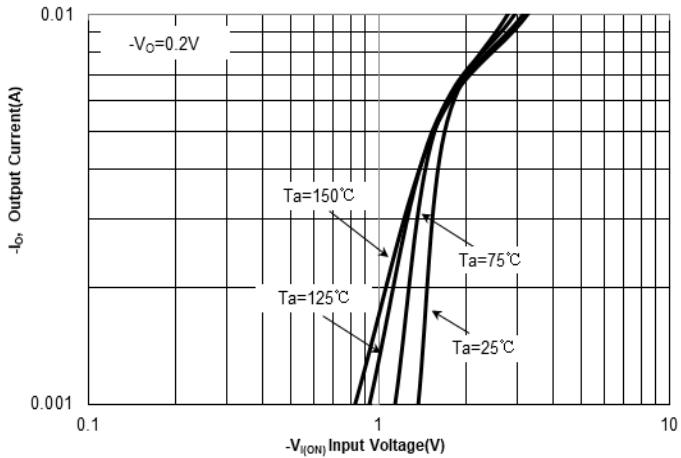


Fig. 2 Output Current vs. Input Off Voltage

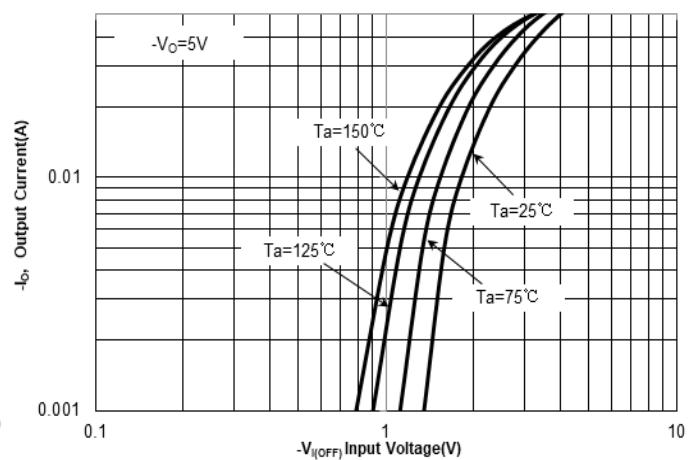


Fig. 3 DC Current Gain vs. Output Current

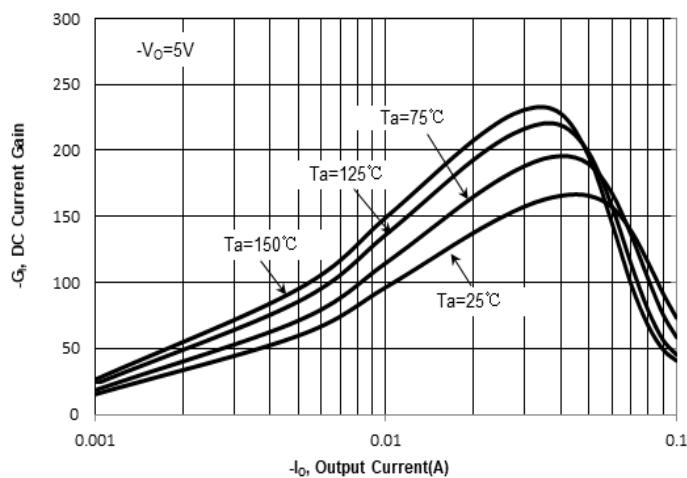
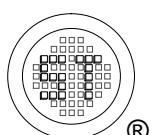
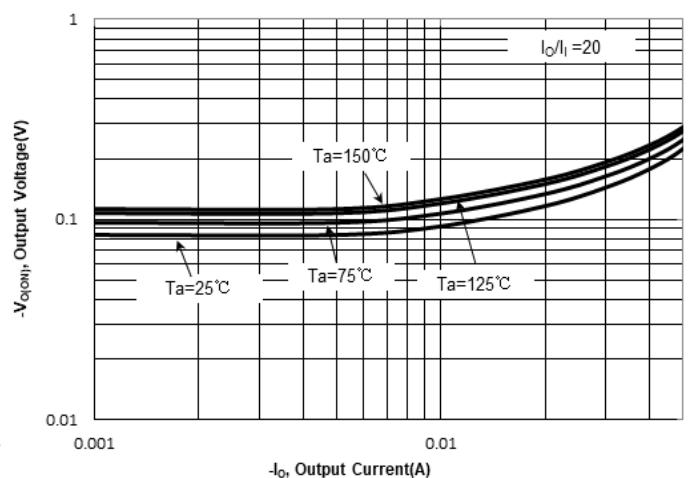


Fig. 4 Output Voltage vs. Output Current



# MMBTRA101SS...MMBTRA106SS

## Electrical characteristic curve (MMBTRA103SS)

Fig. 1 Output Current vs. Input On Voltage

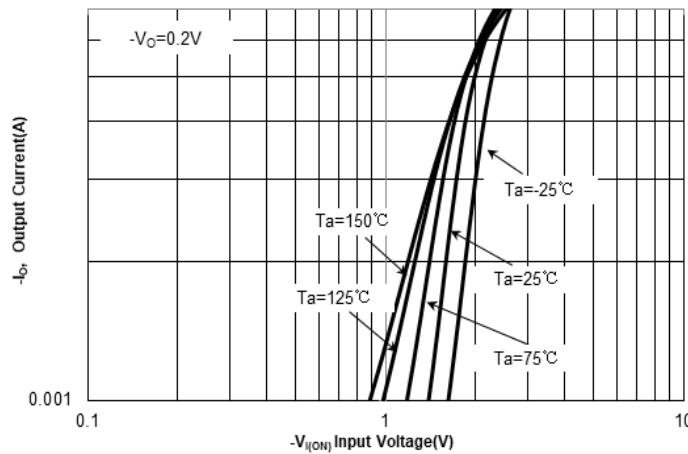


Fig. 2 Output Current vs. Input Off Voltage

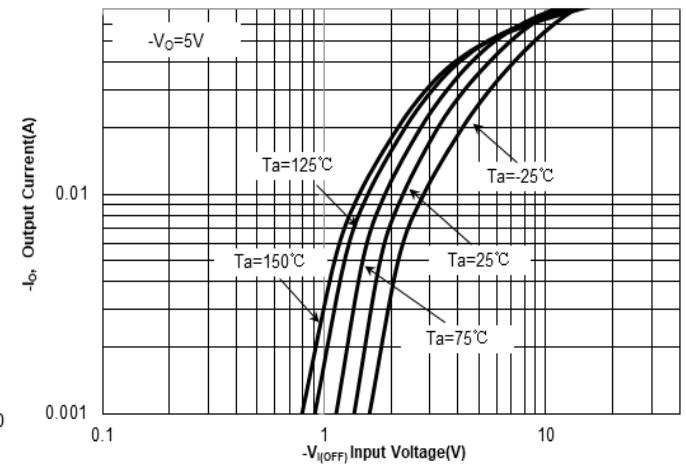


Fig. 3 DC Current Gain vs. Output Current

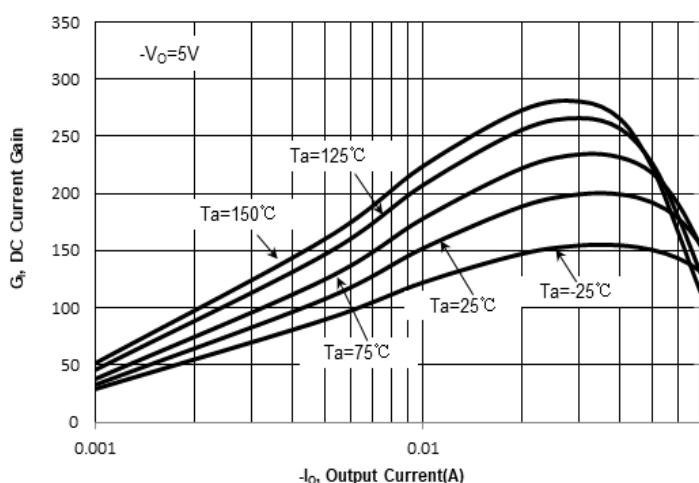
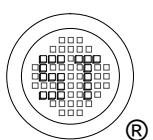
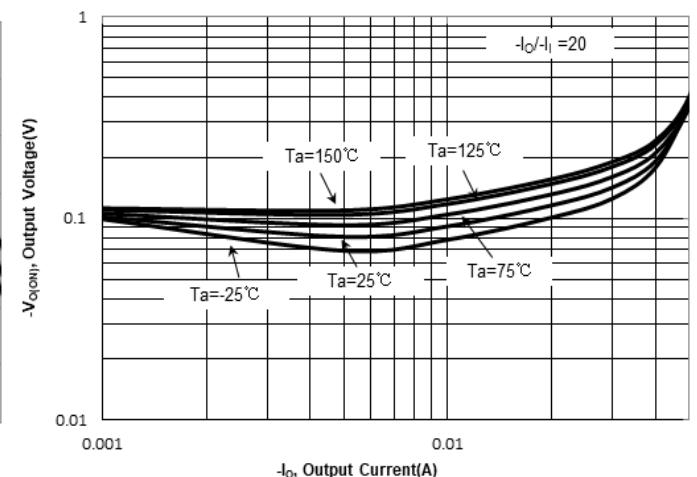


Fig. 4 Output Voltage vs. Output Current



# MMBTRA101SS...MMBTRA106SS

## Electrical characteristic curve (MMBTRA104SS)

Fig. 1 Output Current vs. Input On Voltage

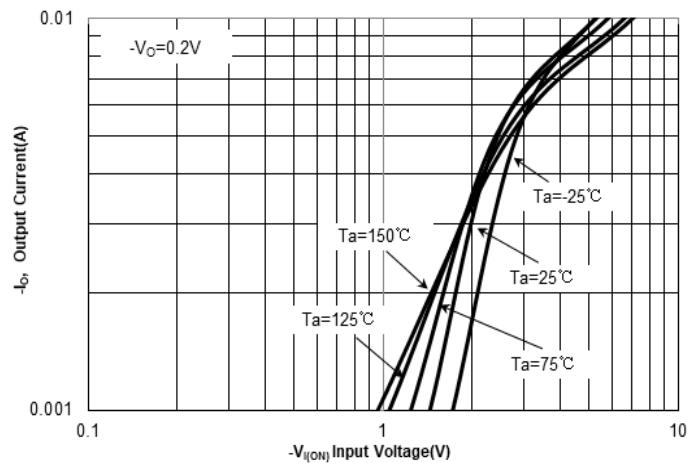


Fig. 2 Output Current vs. Input Off Voltage

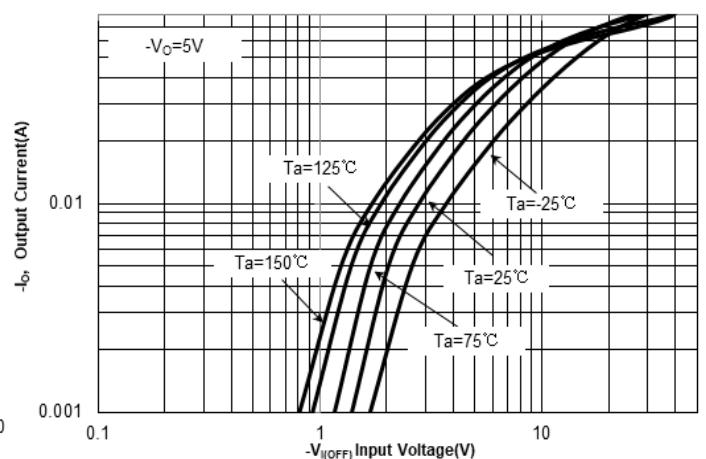


Fig. 3 DC Current Gain vs. Output Current

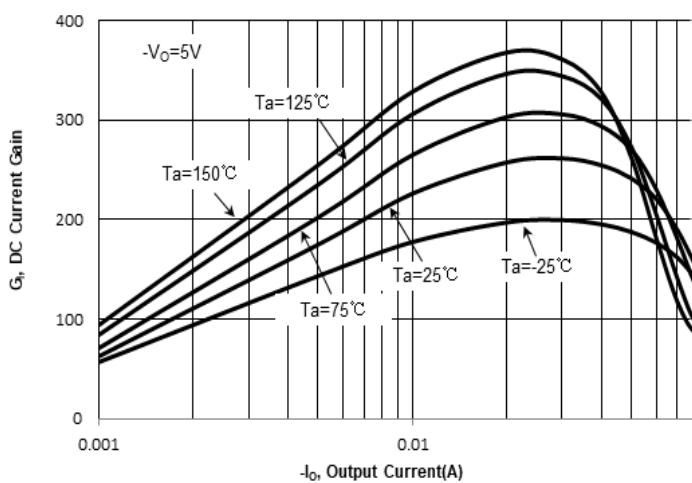
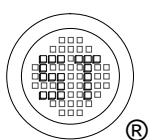
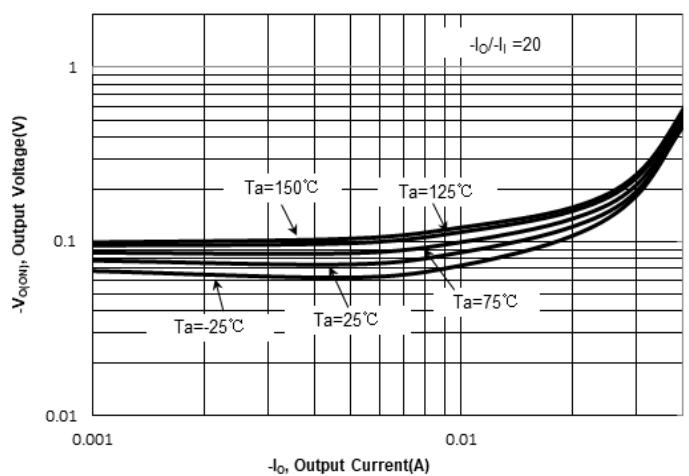


Fig. 4 Output Voltage vs. Output Current



# MMBTRA101SS...MMBTRA106SS

## Electrical characteristic curve (MMBTRA105SS)

Fig. 1 Output Current vs. Input On Voltage

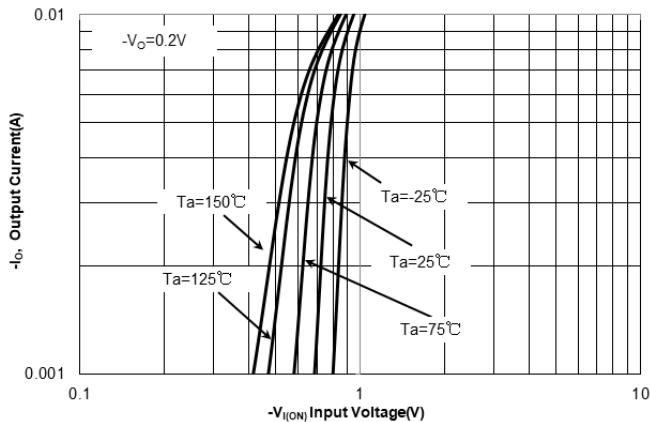


Fig. 2 Output Current vs. Input Off Voltage

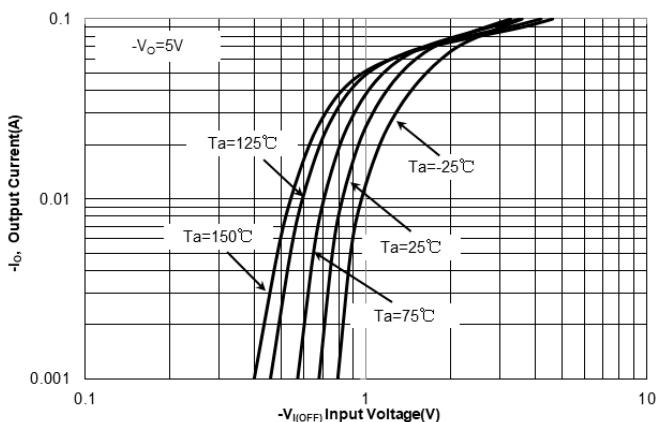


Fig. 3 DC Current Gain vs. Output Current

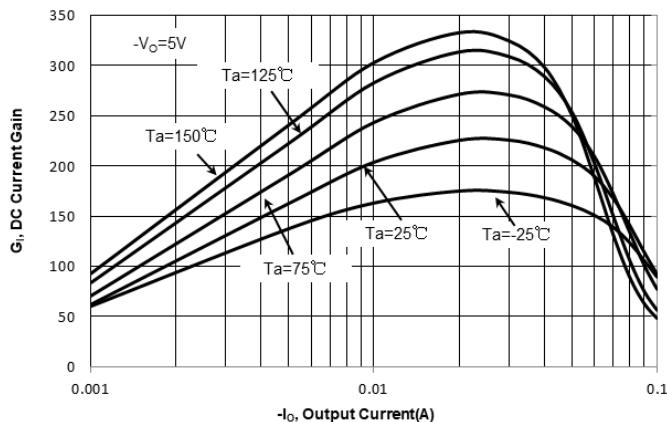
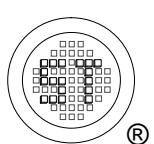
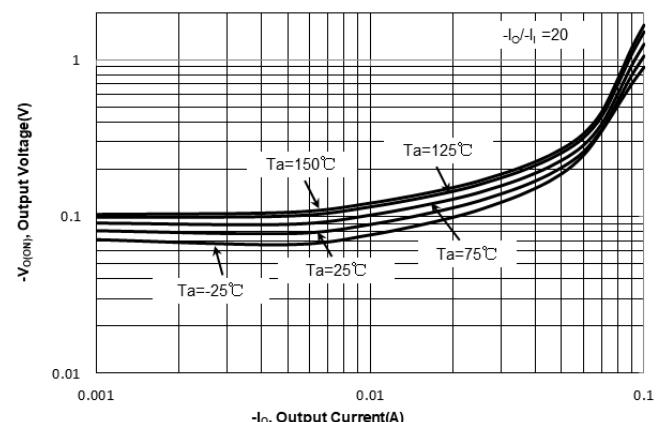


Fig. 4 Output Voltage vs. Output Current



# MMBTRA101SS...MMBTRA106SS

## Electrical characteristic curve (MMBTRA106SS)

Fig. 1 Output Current vs. Input On Voltage

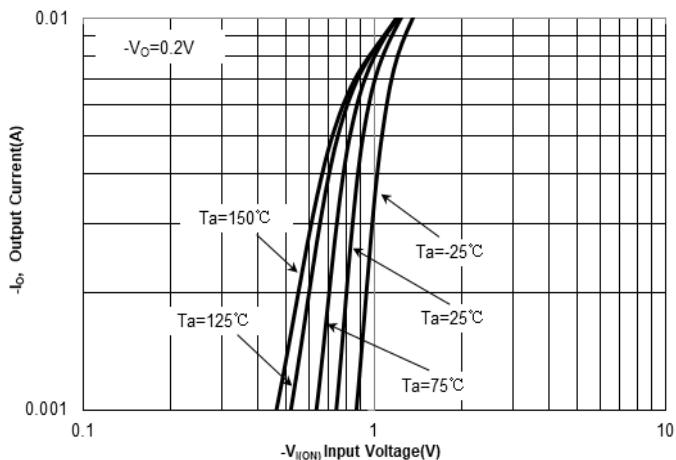


Fig. 2 Output Current vs. Input Off Voltage

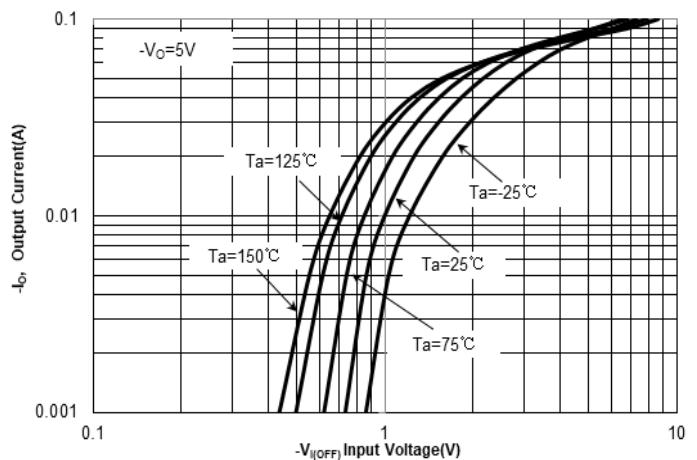


Fig. 3 DC Current Gain vs. Output Current

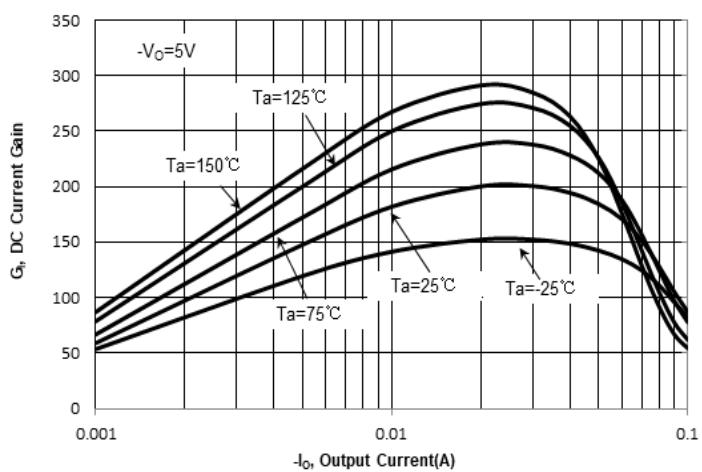
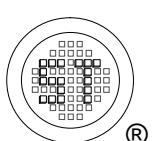
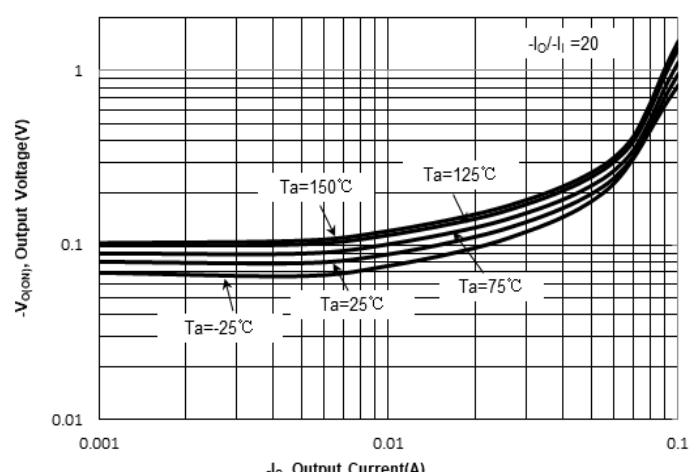


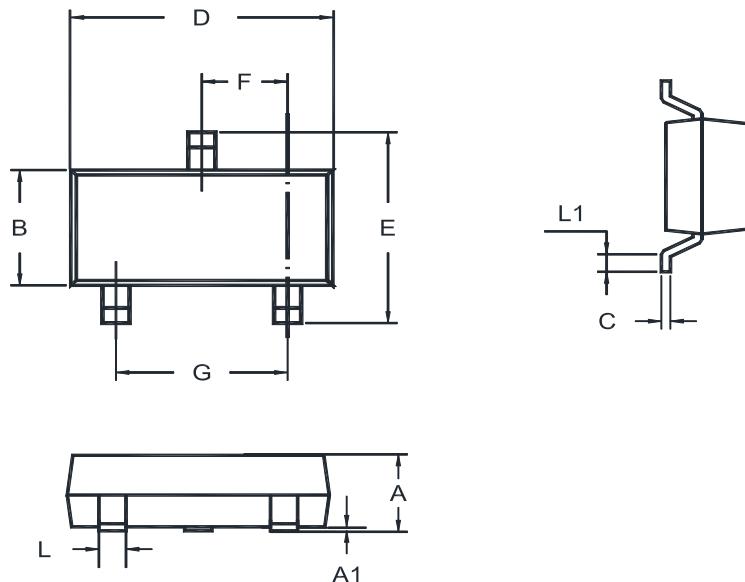
Fig. 4 Output Voltage vs. Output Current



# MMBTRA101SS...MMBTRA106SS

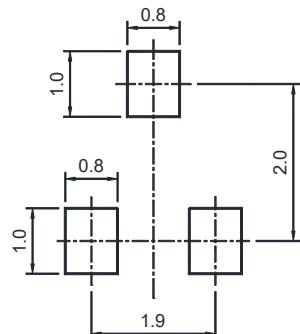
## Package Outline (Dimensions in mm)

SOT-23



Unit	A	A1	B	C	D	E	F	G	L	L1
mm	1.20 0.89	0.100 0.013	1.40 1.20	0.19 0.08	3.04 2.80	2.6 2.2	1.02 0.89	2.04 1.78	0.51 0.37	0.2 MIN

## Recommended Soldering Footprint



## Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
SOT-23	8	4 ± 0.1	0.157 ± 0.004	178	7	3,000

## Marking information

" \*\* " = Part No.

" YM " = Date Code Marking

" Y " = Year

" M " = Month

Font type: Arial

