

PROTECTION PRODUCTS - RailClamp[®] Description

RailClamps[®] are surge rated diode arrays designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by **ESD** (electrostatic discharge), **CDE** (Cable Discharge Events), and **EFT** (electrical fast transients).

The unique design incorporates surge rated, low capacitance steering diodes and a TVS diode in a single package. During transient conditions, the steering diodes direct the transient to either the positive side of the power supply line or to ground. The internal TVS diode prevents over-voltage on the power line, protecting any downstream components.

The low capacitance array configuration allows the user to protect four high-speed data or transmission lines. The low inductance construction minimizes voltage overshoot during high current surges. This device is optimized for ESD protection of portable electronics. They may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (\pm 15kV air, \pm 8kV contact discharge).

Features

- Transient protection for high-speed data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns)
- Array of surge rated diodes with internal TVS Diode
- Small package saves board space
- Protects up to four I/O lines & power line
- Low capacitance (<3pF) for high-speed interfaces
- No insertion loss to 2.0GHz
- Low leakage current and clamping voltage
- Low operating voltage: 5.0V
- Solid-state silicon-avalanche technology

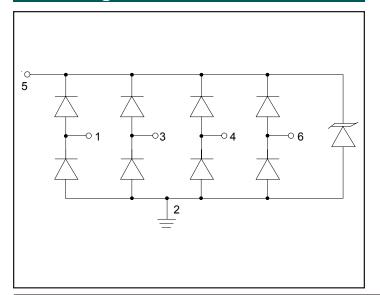
Mechanical Characteristics

- ◆ JEDEC SOT-23 6L package
- Nominal Dimensions: 2.9x2.8mm
- RoHS/WEEE Compliant
- Molding compound flammability rating: UL 94V-0
- Marking: Marking Code
- Packaging: Tape and Reel

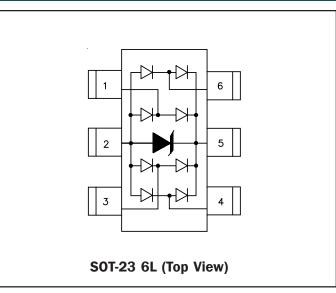
Applications

- ◆ USB 2.0
- USB OTG
- Monitors and Flat Panel Displays
- Digital Visual Interface (DVI)
- ♦ Gigabit Ethernet
- ♦ IEEE 1394 Firewire Ports

Circuit Diagram



Schematic & PIN Configuration



RClamp0504S



Absolute Maximum Rating

SEMTECH

Rating	Symbol	Value	Units	
Peak Pulse Power (tp = $8/20\mu s$)	P _{pk}	150	Watts	
Peak Pulse Current (tp = 8/20µs)	I _{pp}	6	A	
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	15 8	kV	
Operating Temperature	T,	-55 to +125	°C	
Storage Temperature	Т _{sтg}	-55 to +150 °C		

Electrical Characteristics (T=25°C)

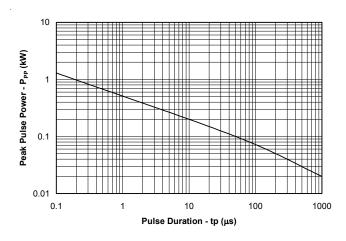
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}	Pin 5 to 2			5	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA Pin 5 to 2	6			V
Reverse Leakage Current	I _R	V _{RWM} = 5V, T=25°C Pin 5 to 2			3	μA
Clamping Voltage	V _c	I _{PP} = 1A, tp = 8/20µs Any pin to pin 2			15	V
Clamping Voltage	V _c	I _{pp} = 6A, tp = 8/20μs Any pin to pin 2			25	V
Junction Capacitance	C _j	V _R = 0V, f = 1MHz Any I/O pin to pin 2			3	pF
		V _R = 0V, f = 1MHz Between I/O pins			1.5	pF

Note 1: I/O pins are pin 1, 3, 4, and 6

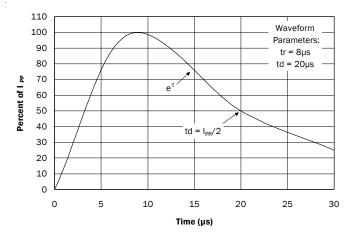


Typical Characteristics

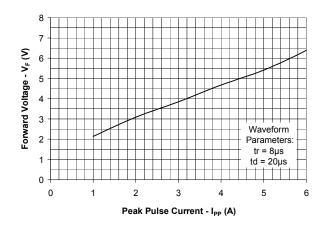
Non-Repetitive Peak Pulse Power vs. Pulse Time



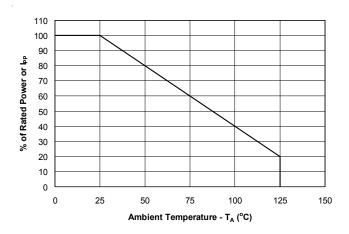
Pulse Waveform



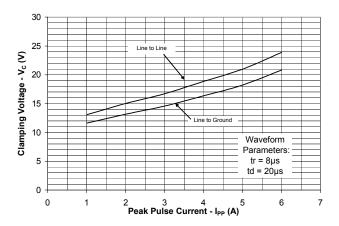
Forward Voltage vs. Peak Pulse Current



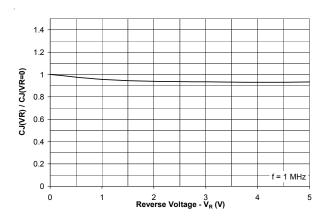
Power Derating Curve



Clamping Voltage vs. Peak Pulse Current

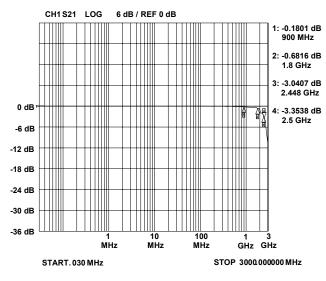




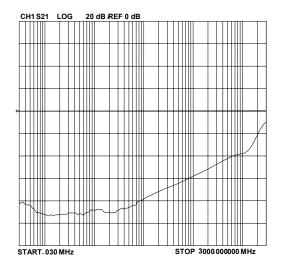




Insertion Loss S21 (I/O to Pin 2)



Analog Crosstalk



6 dB / REF 0 dB CH1S21 LOG 1: -0.1063 dB 900 MHz 2: -0.2660 dB 1.8 GHz 3: -0.5657 dB 2.5 GHz 0 dB -6 dB -12 dB -18 dB -24 dB -30 dB -36 dB 100 10 MHz MHz MHz GHz GHz START.030 MHz STOP 3000.00000 MHz **ESD Clamping** (+8kV Contact per IEC 61000-4-2) Tek Run: 1.00GS/s Sample C1 Max 26.5 V

Insertion Loss S21 (I/O to I/O)

Note: Data is taken with a 10x attenuator

2.5 V

1⇒



Applications Information

Device Connection Options for Protection of Four High-Speed Data Lines

This device is designed to protect data lines by clamping them to a fixed reference. When the voltage on the protected line exceeds the reference voltage the steering diodes are forward biased, conducting the transient current away from the sensitive circuitry. Data lines are connected at pins 1, 3, 4 and 6. Pin 2 should be connected directly to a ground plane. The path length is kept as short as possible to minimize parasitic inductance.

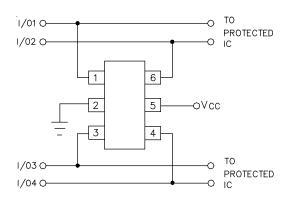
The positive reference is connected at pin 5. The options for connecting the positive reference are as follows:

- 1. To protect data lines and the power line, connect pin 5 directly to the positive supply rail (V_{cc}). In this configuration the data lines are referenced to the supply voltage. The internal TVS diode prevents over-voltage on the supply rail.
- In applications where the supply rail does not exit the system, the internal TVS may be used as the reference. In this case, pin 5 is not connected. The steering diodes will begin to conduct when the voltage on the protected line exceeds the working voltage of the TVS (plus one diode drop).
- 3. In applications where complete supply isolation is desired, the internal TVS is again used as the reference and V_{cc} is connected to one of the I/O inputs. An example of this configuration is the protection of a SIM port. The Clock, Reset, I/O, and VCC lines are connected at pins 1, 3, 4, and 6. Pin 2 is connected to ground and pin 5 is not connected.

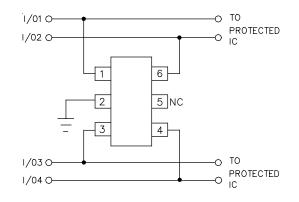
Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

Protection of Four Data Lines and Power Supply Line

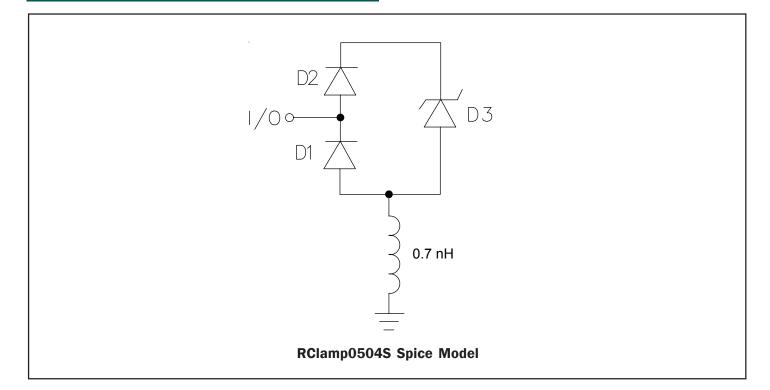


Protection of Four Data Lines Using Internal TVS Diode as Reference





Applications Information - Spice Model

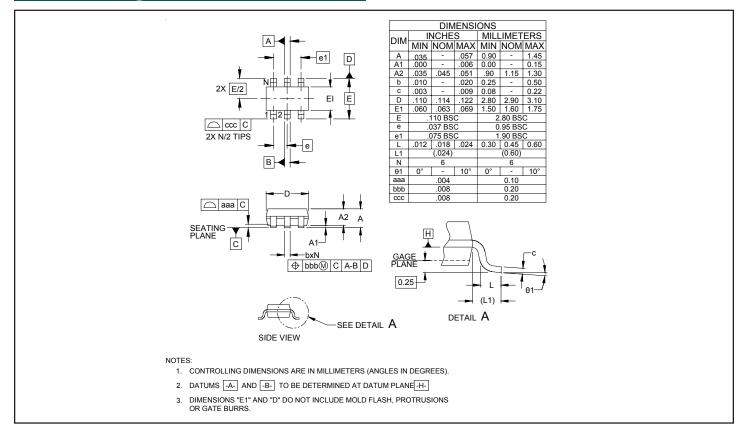


RClamp0504S Spice Parameters								
Parameter Unit		D1 (LCRD)	D2 (LCRD)	D3 (TVS)				
IS	Amp	1.29E-18	1.53E-16	1.78E-10				
BV	Volt	100	100	7.75				
VJ	VJ Volt O.		0.81	0.59				
RS	Ohm	0.787	0.951	0.771				
IBV	Amp	1E-3	1E-3	1E-3				
CJO	Farad	1.0E-12	0.8E-12	111E-12				
TT	sec	2.541E-19	2.541E-19	2.541E-9				
М		0.215	0.100	0.313				
N		1.1	1.1	1.1				
EG	eV	1.11	1.11	1.11				

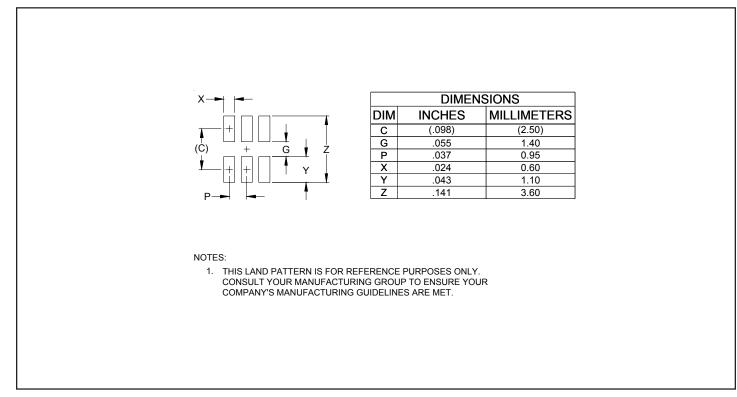




Outline Drawing - SOT-23 6L



Land Pattern - SOT-23 6L





RClamp0504S

Marking

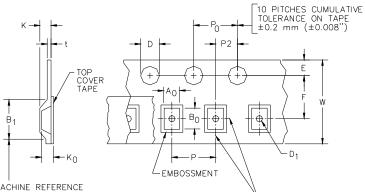
•S54

Ordering Information

Part Number	Qty per Reel	Reel Size		
RClamp0504S.TCT	3,000	7 Inch		

Note: This is a lead-free RoHS/WEEE compliant device RailClamp and RClamp are marks of Semtech Corporation

Tape and Reel Specification



FOR MACHINE REFERENCE ONLY INCLUDING DRAFT AND RADII CONCENTRIC AROUND BO

CENTER LINES OF CAVITY

USER DIRECTION OF FEED

User Direction of feed

AO	В0	ко		
3.23 +/-0.05 mm	3.17 +/-0.05 mm	1.37 +/-0.05 mm		

Tape Width	B, (Max)	D	D1	E	F	K (MAX)	Ρ	PO	P2	T(MAX)	w
8 mm	4.2 mm (.165)	1.5 + 0.1 mm - 0.0 mm	1.0 mm ±0.05	1.750±.10 mm	3.5±0.05 mm	2.4 mm	4.0±0.1 mm	4.0±0.1 mm	2.0±0.05 mm	0.4 mm	8.0 mm + 0.3 mm - 0.1 mm

Contact Information

Semtech Corporation Protection Products Division 200 Flynn Rd., Camarillo, CA 93012 Phone: (805)498-2111 FAX (805)498-3804

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