

PROTECTION PRODUCTS

Description

μClamp[°] TVS diodes are designed to protect sensitive electronics from damage or latch-up due to EOS, lightning, CDE, and ESD. They feature large crosssectional area junctions for conducting high transient currents. These devices offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

 μ Clamp^{*}3381P features extremely good protection characteristics highlighted by high surge current capability (25A, tp=8/20us), low peak ESD clamping voltage, and high ESD withstand voltage (+/-30kV contact per IEC 61000-4-2). Typical dynamic resistance is among the industry's best at 0.025 Ohms. Each device will protect one data or power line operating at 3.3 Volts.

 μ Clamp3381P is in a 2-pin SGP1006N2 package measuring 1.0 x 0.6 x 0.5mm. Leads are spaced at a pitch of 0.65mm and feature a lead-free finish. The combination of small size, low operating voltage, and high ESD surge capability makes them ideal for protection of voltage bus lines in optical modules, LCD televisions, and tablet computers.

Nominal Dimensions

Features

- Transient protection for VBus and data lines to
 - IEC 61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
 - IEC 61000-4-4 (EFT) 40A (5/50ns)
 - IEC 61000-4-5 (Lightning) 25A (8/20µs)
- Protects one line
- Low ESD clamping voltage
- Working voltage: 3.3V
- Low leakage current
- Extremely low dynamic resistance: 0.025 Ohms (Typ)
- Solid-state silicon-avalanche technology

Mechanical Characteristics

- SGP1006N2 Package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Nominal Dimensions: 1.0 x 0.60 x 0.50 mm
- Lead Finish: NiPdAu
- Marking : Marking Code
- Packaging : Tape and Reel

Applications

- Optical Modules
- 3.3V VBus Protection
- LCD TV
- Tablet PC
- Instrumentation
- CCTV Cameras

Schematic and Pin Configuration



Absolute Maximum Ratings

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P _{PK}	275	W
Peak Pulse Current ($t_p = 8/20\mu s$)	I _{PP}	25	А
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V _{ESD}	±30 ±30	kV
Operating Temperature	T,	-40 to +125	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}	Pin 1 to 2 or Pin 2 to 1				3.3	V
Reverse Breakdown Voltage	V _{BR}	I _{BR} = 1mA, Pin 1 to 2 or Pin2 to 1		4.5	8.5	10	V
Reverse Leakage Current	I _R	V _{RWM} = 3.3V			1	100	nA
Clamping Voltage	V _c	$I_{pp} = 25A, t_p = 8/20\mu s,$				11.5	V
ESD Clamping Voltage ²	V _c	$I_{pp} = 4A, t_p = 0.2/100$ ns (TLP)			8.3		V
ESD Clamping Voltage ²	V _c	$I_{pp} = 16A, t_p = 0.2/100$ ns (TLP)			8		V
Dynamic Resistance ^{2, 3}	R _{DYN}	t _p = 0.2/100ns (TLP)			0.025		Ohms
Junction Capacitance	C	$V_{R} = 0V, f = 1MHz$	$T = 25^{\circ}C$		30	35	pF

Notes:

(1): Measured with a 20dB attenuator, 50 Ohm scope input impedance, 2GHz bandwidth. ESD gun return path connected to Ground Reference Plane (GRP)

(2): Transmission Line Pulse Test (TLP) Settings: tp = 100ns, tr = 0.2ns, I_{TLP} and V_{TLP} averaging window: $t_1 = 70$ ns to $t_2 = 90$ ns.

(3): Dynamic resistance calculated from $\rm I_{_{TLP}}$ = 4A to $\rm I_{_{TLP}}$ = 16A

Typical Characteristics

Non-Repetitive Peak Pulse Power vs. Pulse Time



TLP Charateristic



ESD Clamping (8kV Contact per IEC 61000-4-2)





Typical Breakdown Voltage vs. Temperature



ESD Clamping (-8kV Contact per IEC 61000-4-2)



Clamping Voltage vs. Peak Pulse Current (tp=8/20µs)

Outline Drawing - SGP1006N2



Land Pattern - SGP1006N2



Marking Code

	81	

Notes:

- 1. Device is electrically symmetrical.
- 2. Marking will also include line matrix date code.
- 3. Bar indicates Pin 1 location.

Tape and Reel Specification



Ordering Information

Part Number	Qty per Reel	Reel Size	
µClamp3381P.TFT	15,000	7 Inch	
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