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APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
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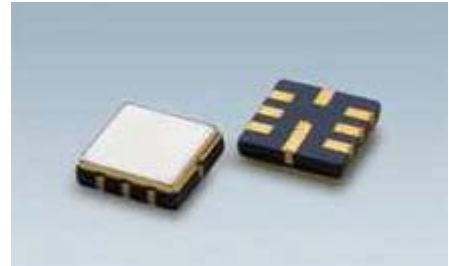


Part No.	:	SFR315A
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Prepared by:	
Checked by:	
Approved by:	

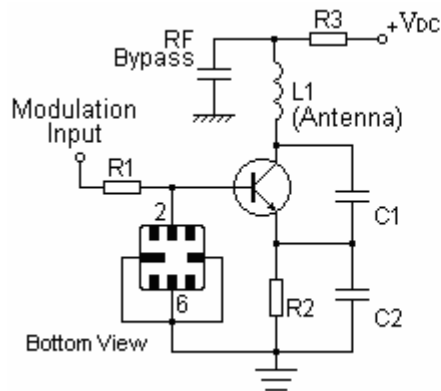
Features

- 1-port Resonator
- Ceramic Package for **Surface Mounted Technology (SMT)**
- **RoHS** compatible
- Package size 5.00x5.00x1.50mm³
- Package Code QCC8C
- **Electrostatic Sensitive Device(ESD)**

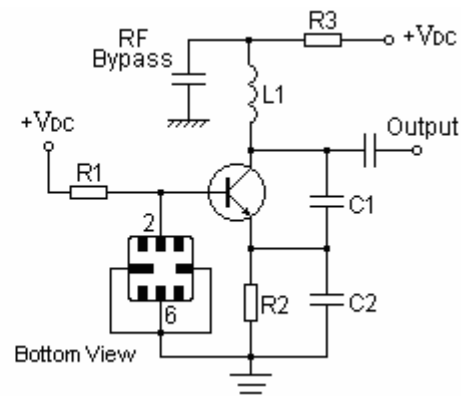


Application

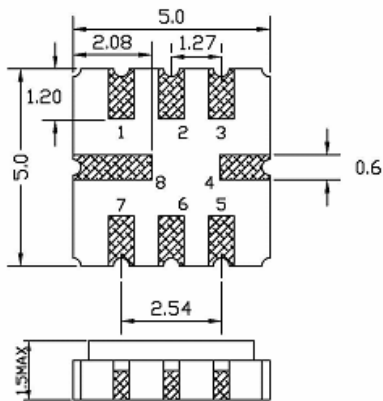
Typical Low-Power Transmitter Application



Typical Local Oscillator Application

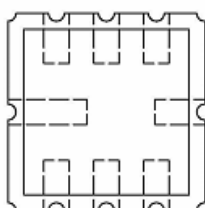


Package Dimensions (QCC8C)

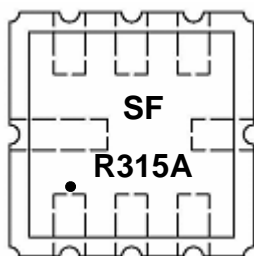


Pin Configuration

2	Input
6	Output
1,3,4,5,7,8	Ground

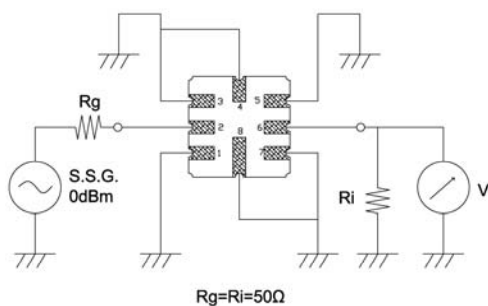


Marking

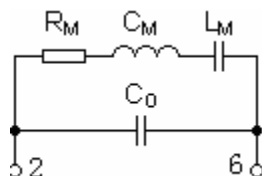


SF	Trademark
R	SAW Resonator
315A	Part number

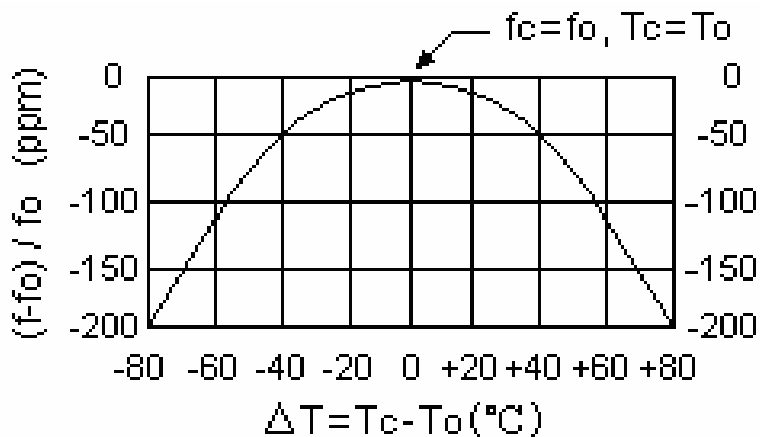
Test Circuit



Equivalent LC Model



Temperature Characteristics



The curve shown above accounts for resonator contribution only and does not include LC component temperature contributions.

Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V_{DC}	± 30	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T_{stg}	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

Electronic Characteristics

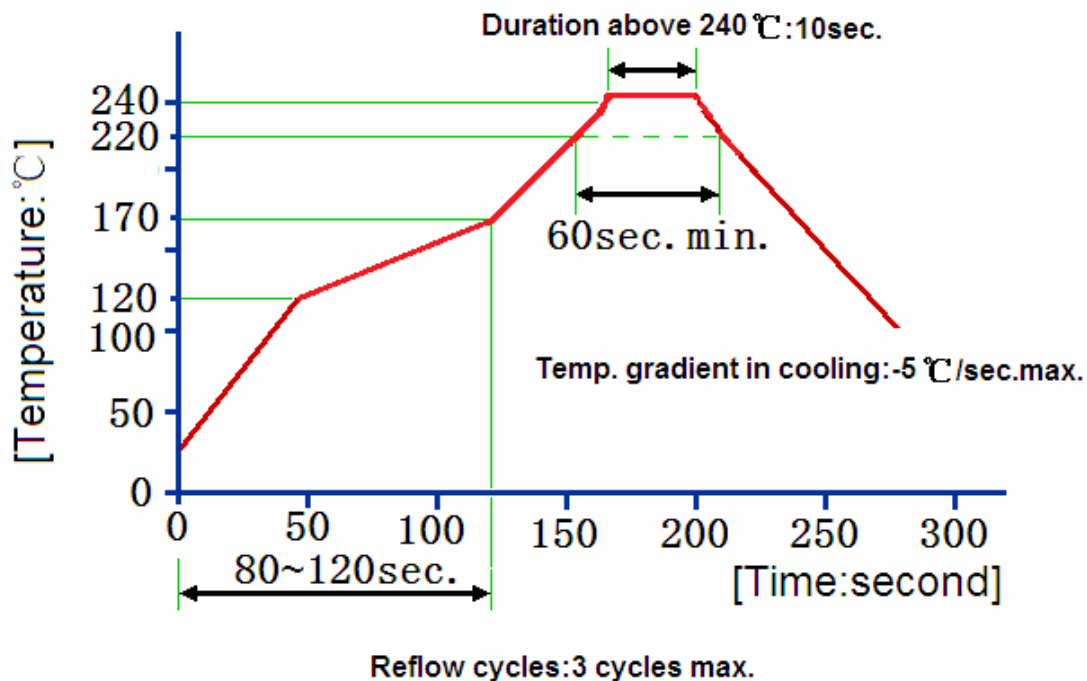
Test Temperature: $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

Item			Minimum	Typical	Maximum	Unit
Center Frequency	Absolute Frequency	f_c		315.00		MHz
	Tolerance from 315.00MHz	Δf_c		± 75		KHz
Insertion Loss(min)		IL		1.1	1.6	dB
Quality Factor	Unloaded Q	Q_U		17824		
	50Ω Loaded Q	Q_L		1925		
Temperature Stability	Turnover Temperature	T_0	25	40	55	°C
	Turnover Frequency	f_0		f_c		
	Frequency Temperature Coefficient	FTC		0.032		ppm/°C
Frequency Aging	Absolute Value during the First Year	$ f_A $		≤ 10		ppm/yr
DC Insulation Resistance between Any Two Pins			1.0			MΩ
RF Equivalent RLC Model	Motional Resistance	R_M		13.0	20.0	Ω
	Motional Inductance	L_M		109.1		μH
	Motional Capacitance	C_M		2.34		fF
	Static Capacitance	C_0	2.9	3.2	3.5	pF

Recommended Reflow Soldering Diagram



Notes

1. As a result of the particularity of inner structure of SAW products, it is easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.

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