## Approval Sheet

## Customer Information

| Customer |  |  |  |
| :--- | :--- | :--- | :---: |
| Part Name |  |  |  |
| Part No. |  |  |  |
| Model No. |  |  |  |
| COMPANY |  | PURCHASE |  |
|  |  | R\&D |  |
|  |  |  |  |

Vendor Information

| Name | SFI Electronics Technology Inc. |
| :--- | :---: |
| Part Name | Chip TVS |
| Part No. | SFI0603-120E330NP-LF |
| Lot No. |  |

## SFI Electronics Technology Inc.

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| ISO 9001:2008 ISO 14001:2004 ISO/TS 16949:2009 Management System <br> www.tuv.com ID 1100008833 |  | REV : I | Prepared | Check |
|  |  |  |  |  |


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## PART NO. SFIO603-120E330NP-LF

| 1.1 Technology Data | Symbol | Value | Unit |  |
| :--- | :---: | :---: | :---: | :---: |
| Maximum allowable continuous AC voltage at $50-60 \mathrm{~Hz}$ | $\mathrm{~V}_{\text {RMS }}$ | 8 | V |  |
| Maximum allowable continuous DC voltage | $\mathrm{V}_{\mathrm{DC}}$ | 12 | V |  |
| Varistor breakdown voltage | $\mathrm{Vv}_{\mathrm{V}}$ | $20 \sim 30$ | V |  |
| Typical capacitance value measured at 1 MHz | C | 33 | pF |  |
| Typical capacitance value tolerance | t |  | $\pm 30$ | $\%$ |
| Maximum allowable clamping voltage | $\mathrm{V}_{\mathrm{C}}$ |  | 55 | V |
| Leakage current at $\mathrm{V}_{\mathrm{DC}}$ (at initial state) | $\mathrm{I}_{\text {LDC }}$ | $<$ | 1 | $\mu \mathrm{~A}$ |
| Leakage current at $\mathrm{V}_{\mathrm{DC}}$ (after ESD test) | $\mathrm{I}_{\text {LDCA }}$ | $<$ | 2 | $\mu \mathrm{~A}$ |

### 1.2 Reference Data

| Response time | $\mathrm{T}_{\text {rise }}$ | $<$ | 1 |
| :--- | :---: | :---: | :---: |
| Operation ambient temperature | $\mathrm{T}_{\text {OPT }}$ | ns |  |
| Storage temperature range | $\mathrm{T}_{\text {STG }}$ | $-50 \sim+85$ | ${ }^{\circ} \mathrm{C}$ |
| ESD testing | IEC61000-4-2 | $-50 \sim+125$ | ${ }^{\circ} \mathrm{C}$ |

ESD testing
IEC61000-4-2
Level 4

### 1.3 Other Data

| Body | ZnO |  |
| :--- | :---: | :---: |
| End termination | $\mathrm{Ag} / \mathrm{Ni} / \mathrm{Sn}$ |  |
| Packaging | Reel |  |
| Complies with standard | IEC61000-4-2 |  |
| Complies with RoHs standard | Yes |  |
| Lead content | $<$ | 1000 |
| Marking | None |  |

Notes:

* 1 The varistor breakdown voltage was measured at 1 mA .
$* 2$ The clamping voltage was measured at $8 / 20 \mu$ s standard current.
* 3 The leakage current was measured at working voltage.
* 4 The components shall be employed within 1 year, in the nitrogen condition.

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## 2. Size

| Model | $\mathbf{0 6 0 3 ( 1 6 0 8 )}$ |
| :---: | :---: |
| Length(L) | $1.60 \pm 0.15$ |
| Width(W) | $0.80 \pm 0.10$ |
| Thickness(T) | 0.90 max. |
| Termination(a) | $0.30 \pm 0.10$ |



## 3. ESD Wave Form



## IEC61000-4-2 Standards

| SEVERITY LEVEL | AIR DIRCHARGE | DIRECT <br> DISCHARGE |
| :---: | :---: | :---: |
| 1 | 2 KV | 2 KV |
| 2 | 4 KV | 4 KV |
| 3 | 8 KV | 6 KV |
| 4 | 15 KV | 8 KV |

IEC61000-4-2 compliant ESD current pulse waveform

## 4. Environment Reliability Test

| Characteristic | Test Method and Description |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| High Temperature Storage | The specimen shall be subjected to $125 \pm 2^{\circ} \mathrm{C}$ for $1000 \pm 2$ hours without load and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within $10 \%$. |  |  |  |
| Temperature Cycle | The temperature cycle of specified temperature shall be repeated five times and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within $10 \%$ and mechanical damage shall be examined. | Step | Temperature | Period |
|  |  |  | $-40 \pm 3^{\circ} \mathrm{C}$ | $30 \pm 3$ min |
|  |  | 2 | room temperature | 1 hour |
|  |  | 3 | $125 \pm 3^{\circ} \mathrm{C}$ | $30 \pm 3$ min |
|  |  | 4 | room temperature | 1 hour |
| High Temperature Load | After being continuously applied the maximum allowable voltage at $85 \pm 2^{\circ} \mathrm{C}$ for $1000 \pm 2$ hours, the specimen shall be stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within $10 \%$. |  |  |  |
| Damp Heat Load/ Humidity Load | The specimen should be subjected to $40 \pm 2^{\circ} \mathrm{C}$ and $90 \sim 95 \% \mathrm{RH}$, the maximum allowable voltage applied for $1000 \pm 2$ hours and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within $10 \%$. |  |  |  |
| Low Temperature Storage | The specimen should be subjected to $-40 \pm 2^{\circ} \mathrm{C}$ for $1000 \pm 2$ hours without load and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within $10 \%$. |  |  |  |


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## 5. Soldering Recommendations

5.1 Recommended solder pad layout
(Unit : mm)

|  | A | B | C |
| :---: | :---: | :---: | :---: |
| 0603 | $0.9 \sim 1.2$ | $0.9 \sim 1.2$ | $0.8 \sim 1.0$ |


5.2 The SIR test of the solder paste shall be done (Based on JIS-Z-3284)
5.3 Steel plate and foot distance printing

| Foot distance printing $(\mathrm{mm})$ | Steel plate thickness $(\mathrm{mm})$ |
| :---: | :---: |
| $\geqq 0.65 \mathrm{~mm}$ | 0.18 mm |
| $0.50 \sim 0.65 \mathrm{~mm}$ | 0.15 mm |
| $0.40 \sim 0.50 \mathrm{~mm}$ | 0.12 mm |
| $\leqq 0.40 \mathrm{~mm}$ | 0.10 mm |

5.4 The IR reflow and temperature of soldering for Pb free process

$\mathcal{T}$ reflow Pb free process suggestion profile
(1) The solder recommend is Sn96.5/Ag3.5 and thickness recommend as shown in table 5.3
(2) Ramp-up rate $\left(217^{\circ} \mathrm{C}\right.$ to peak) $+3^{\circ} \mathrm{C} /$ second max.
(3) Temp. maintain at $175 \pm 25^{\circ} \mathrm{C} \quad 180$ seconds max.
(4) Temp. maintain above $217^{\circ} \mathrm{C} \quad 60 \sim 150$ seconds

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(5) Peak temperature range $245+20 /-10^{\circ} \mathrm{C}$ within $5^{\circ} \mathrm{C}$ of actually peak temperature ( $\mathrm{t}_{\mathrm{p}}$ ) $10 \sim 20$ seconds
(6) Ramp down rate $-6^{\circ} \mathrm{C} /$ second max.
※Perform adequate test in advance as the reflow temperature profile will vary according to the conditions of the manufacturing process and the specification of the reflow furnace.
5.5 Resistance to soldering heat and high temperature resistance : $260^{\circ} \mathrm{C}, 10 \mathrm{sec} 3$ times
5.6 Hand soldering

In hand soldering of the varistors, large temperature gradient between preheated the varistors and the tip of soldering iron may cause electrical failures and mechanical damages such as cracking or breaking of the devices. The soldering shall be carefully controlled and carried out, so that the temperature gradient is kept minimum with following recommended conditions for hand soldering.
5.6.1 Recommended soldering condition 1 (with preheating)
(1) Solder
0.12~0.18mm thread solder (Sn96.5:Ag3.5) with soldering flux in the core rosin-based and non-activated flux is recommended.
(2) Preheating

The varistors shall be preheated so that temperature gradient between the devices and the tip of soldering iron is $150^{\circ} \mathrm{C}$ or below.
(3) Soldering iron

Rated power of 20W max. with 3 mm soldering tip in diameter
Temperature of soldering iron tip $380^{\circ} \mathrm{C}$ max., $3 \sim 5 \mathrm{sec}$ (The required amount of solder shall be melted in advance on the soldering tip.)
(4) Cooling

After soldering, the varistors shall be cooled gradually at room ambient temperature.
5.6.2 Recommended soldering condition 2 (without preheating)
(1) Solder iron tip shall not directly touch to ceramic dielectrics.
(2) Solder iron tip shall be fully preheated before soldering while soldering iron tip to the external electrode of varistors.

### 5.7 Post soldering cleaning

5.7.1 Residues of corrosive soldering fluxes on the PC board after cleaning may greatly have influences on the electrical characteristic and the reliability (such as humidity resistance) of the varistors which have been mounted on the board. It shall be confirmed that the characteristic and the reliability of the devices are not affected by the applied cleaning conditions.
5.7.2 When an ultrasonic cleaning is applied to the mounted varistors on PC boards. Following conditions are recommended for preventing failures or damages of the devices due to the large vibration energy and the resonance conditions caused by the ultrasonic waves.
(1) Frequency 29 MHz max.
(2) Radiated power 20W/liter max.
(3) Period 5 minutes max.

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## 6. Packaging Specification

6.1 Carrier tape and transparent cover tape should be heat-sealed to carry the products, and the reel should be used to reel the carrier tape.
6.2 The adhesion of the heat-sealed cover tape shall be $40+20 /-15$ grams.
6.3 Both the head and the end portion of the taping shall be empty for reel package and SMT auto-pickup machine. And a normal paper tape shall be connected in the head of taping for the operator to handle.

(Unit : mm)

| Symbol | $\mathbf{A}_{0}$ <br> $\pm 0.05$ | $\mathbf{B}_{0}$ <br> $\pm 0.05$ | $\mathbf{K}_{0}$ <br> $\pm 0.05$ | $\mathbf{D}$ <br> $\mathbf{+ 0 . 1 0}$ <br> -0.05 | $\mathbf{P}$ <br> $\pm 0.10$ | $\mathbf{P}_{2}$ <br> $\pm 0.10$ | $\mathbf{P}_{0}$ <br> $\pm 0.10$ | $\mathbf{W}$ <br> $\pm 0.10$ | $\mathbf{E}$ <br> $\mathbf{0} .10$ | $\mathbf{F}$ <br> $\pm 0.05$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathbf{0 6 0 3}$ | 1.05 | 1.90 | 0.95 | 1.56 | 4.00 | 2.00 | 4.00 | 8.00 | 1.75 | 3.50 |


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## 7. Reel Dimension


(Unit : mm)

| Symbol | A | B | C | D | E | W | $\mathbf{W}_{\mathbf{1}}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathbf{0 6 0 3}$ | $178.0 \pm 1.0$ | $60.0 \pm 0.5$ | $13.0 \pm 0.2$ | $21.0 \pm 0.2$ | $2.0 \pm 0.5$ | $9.0 \pm 0.5$ | $1.5 \pm 0.1$ |

## 8. Standard Packaging

| Size | $\mathbf{0 6 0 3}$ |
| :---: | :--- |
| Pcs | 4000 |


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