## SGM2554

Power Distribution Switch

## GENERAL DESCRIPTION

The SGM2554 is an integrated $100 \mathrm{~m} \Omega$ (TYP) power switch for self-powered and bus-powered Universal Series Bus (USB) applications. A built-in charge pump is used to drive the N -Channel MOSFET that is free of parasitic body diode to eliminate any reversed current flow across the switch when it is powered off. Low quiescent supply current and small package are particularly suitable in battery-powered portable equipment.

Several protection functions include soft-start to limit inrush current during plug-in, current limiting at 1.85A to meet USB power requirement, and thermal shutdown to protect damage under over current conditions.

The SGM2554 is available in a Green SOT-23-5 package and is rated over the $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ temperature range.

## FEATURES

- 100m $\Omega$ (TYP) High-side N-Channel MOSFET
- Guaranteed 1.2A Continuous Current
- 1.85A Current Limit
- Soft-Start Function
- Thermal Shutdown Protection
- Low $21 \mu \mathrm{~A}$ Supply Current $\qquad$
- Less than $1 \mu \mathrm{~A}$ in Shutdown Mode
- Under-Voltage Lockout Protection for $\mathrm{V}_{\mathrm{IN}}$
- Input Voltage Range: 2.2V to 5.5 V
- No Reversed Leakage Current
- Available in Green SOT-23-5 Package


## APPLICATIONS

Motherboard USB Power Switch
USB Device Power Switch
Hot-Plug Power Supplies

## Load Switch

## TYPICAL APPLICATION



* $470 \mu \mathrm{~F}$, Low ESR Electrolytic


## PACKAGE/ORDERING INFORMATION

| MODEL | PIN- <br> PACKAGE | SPECIFIED <br> TEMPERATURE <br> RANGE | ORDERING <br> NUMBER | PACKAGE <br> MARKING | PACKAGE <br> OPTION |
| :---: | :---: | :---: | :---: | :---: | :---: |
| SGM2554A | SOT-23-5 | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | SGM2554AYN5G/TR | SI4XX | Tape and Reel, 3000 |
| SGM2554B | SOT-23-5 | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | SGM2554BYN5G/TR | SI5XX | Tape and Reel, 3000 |

NOTE: XX = Date Code.

## MARKING INFORMATION



For example: SI4CA (2012, January)

## ABSOLUTE MAXIMUM RATINGS



Operating Temperature Range........................... $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
Storage Temperature Range.......................... $65^{\circ} \mathrm{C}$ to $-150^{\circ} \mathrm{C}$
Power Dissipation, $\mathrm{R}_{\mathrm{D}} @ \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$
SOT-23-5.............................................................0.25W
Package Thermal Resistance

Lead Temperature (Soldering, 10s) ................................ $260^{\circ} \mathrm{C}$
ESD Susceptibility

NOTE:
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to obserye proper handling and installation procedures can Cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

PIN CONFIGURATIONS (TOP VIEW)


## PIN DESCRIPTION

| PIN |  | NAME | FUNCTION |
| :---: | :---: | :---: | :---: |
| SGM2554A | SGM2554B |  |  |
| 1, 5 | (1,5 | VOUT | Output Voltage. |
| 2 | 2 | GND | Ground. < |
| 3 | 3, 4 | VIN | Power Input Voltage. |
| 4 | - - | EN | Chip Enable. When EN = "High", chip is enabled. |

## FUNCTION BLOCK DIAGRAM



## ELECTRICAL CHARACTERISTICS

( $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}, \mathrm{C}_{\text {IN }}=\mathrm{C}_{\text {out }}=1 \mu \mathrm{~F}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$, unless otherwise noted.)

| PARAMETER |  | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage Range |  | $\mathrm{V}_{\text {IN }}$ |  | 2.2 |  | 5.5 | V |
| $\mathrm{R}_{\mathrm{DS}(\mathrm{ON})}$ of $\mathrm{N}-\mathrm{MOSFET}$ | SGM2554A | $\mathrm{R}_{\mathrm{DS}(\mathrm{ON})}$ | $\mathrm{L}=1 \mathrm{~A}$ |  | 100 |  | $\mathrm{m} \Omega$ |
|  | SGM2554B |  | $\mathrm{L}=1 \mathrm{~A}$ |  | 100 |  |  |
| Quiescent Supply Current |  | $\mathrm{I}_{\mathrm{Q}}$ | $\mathrm{V}_{\text {IN }}=3 \mathrm{~V}$ |  | 18 |  | $\mu \mathrm{A}$ |
|  |  | $\mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}$ |  | 21 |  |  |
| Shutdown Supply Current (SGM2554A) |  |  | $I_{\text {Sb }}$ | $\mathrm{V}_{\text {EN }}=0 \mathrm{~V}$ |  | 0.1 |  | $\mu \mathrm{A}$ |
| Output Turn-On Rising Time |  | $\mathrm{T}_{\mathrm{R}}$ | $\mathrm{R}_{\mathrm{L}}=30 \Omega, 90 \%$ Settl |  | 1.1 |  | ms |
| Current Limit Threshold |  | limit | $\mathrm{R}_{\mathrm{L}}=2 \Omega$ |  | 1.85 |  | A |
| Short-Circuit Fold Back Current |  | $\bigcirc \mathrm{I}_{\text {SHORT }}$ | $\mathrm{V}_{\text {OUT }}=0 \mathrm{~V}, \mathrm{~V}_{\text {IN }}=3 \mathrm{~V}$ |  | 1.3 |  | A |
| EN Input High Threshold (SGM2554A) |  |  |  | 1.5 |  |  | V |
| EN Input Low Threshold (SGM2554A) |  |  | $<$ |  |  | 0.5 | V |
| Output Leakage Current (SGM2554A |  | $I_{\text {leakage }}$ | $V_{\text {EN }}=0 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=0 \mathrm{~V}$ |  | 0.2 |  | $\mu \mathrm{A}$ |
| Under-Voltage Lockout Threshold |  | UVLO |  |  | 1.65 |  | V |
| Under-Voltage Lockout Threshold Hysteresis |  |  | , |  | 50 |  | mV |
| Thermal Shutdown Temperature |  |  |  |  | 125 |  | ${ }^{\circ} \mathrm{C}$ |
| Thermal Shutdown Temperature Hysteresis |  |  |  |  | 20 |  | ${ }^{\circ} \mathrm{C}$ |



## TYPICAL PERFORMANCE CHARACTERISTICS



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## TEST CIRCUITS



NOTE: Test Circuit 2 is performed by charging an external tank of bulk capacitor to the input then applying this voltage to the input of the unit.

## APPLICATION INFORMATION

The SGM2554 is a high-side single N-Channel MOSFET switch and SGM2554A has active-high enable input.

## Input and Output

VIN (input) is the power supply connection to the circuitry and the drain of the output MOSFET. VOUT (output) is the source of the output MOSFET. In a typical circuit, current flows through the switch from VIN to VOUT toward the load. Both VOUT pins must be short on the board and connected to the load and so do both VIN pins but connected to the power source.

## Thermal Shutdown

Thermal shutdown shuts off the output MOSFET if the die temperature exceeds $125^{\circ} \mathrm{C}$ until the die temperature drops to $105^{\circ} \mathrm{C}$.

## Soft-Start

In order to eliminate the upstream voltage droop caused by the large inrush current during hot-plug events, the "soft-start" feature effectively isolates power supplies from such highlycapacitive loads.

## Under-Voltage Lockout (UVLO)

UVLO prevents the MOSFET switch from turning on until input voltage exceeds 1.65 V (typical). If input voltage drops below 1.6 V (typical), UVLO shuts off the MOSFET
switch.
$=1 \mu \mathrm{~F}, \mathrm{C}_{\text {out }}=470 \mu \mathrm{~F}$ (Low ESR) on M/B
$\mathrm{C}_{1 \mathrm{~N}}=1 \mu \mathrm{~F}, \mathrm{Cour}^{2}=470 \mu \mathrm{~F}$ (Low ESR) on M/B
$C_{N}=10 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{ol}}=330 \mu \mathrm{~F}$ (Low ESR) on Notebook
$\mathrm{C}_{\mathrm{IN}}=10 \mu \mathrm{~F}$, $\mathrm{Cout}_{\text {out }}=1 \mu \mathrm{~F}$ on USB Device

Figure 1. High-side Power Switch

## Current Limiting and Short Protection

The current limit circuit is designed to protect the system supply. The current limit threshold is set internally to allow a minimum of 1.3A through the MOSFET but limits the output current to approximately 1.85A (typical). When the output is shorted to ground, it will be limited to a constant current of 1.2A until thermal shutdown or short condition is removed.


Figure 2. High-side Power Switch with Chip Enable Control

## Reverse Yoltage Protection

The reverse yoltage protection feature turns off the N -Channel MOSFET switch whenever the output voltage exceeds the input voltage by 70 mV (typical). The exceeds the input voltage by 70 mV (typical). The
SGM2554 keeps the N-Channel MOSFET turned off until the output voltage is lower than the input voltage by 30 mV (typical) or the chip enable is toggled.

## APPLICATION INFORMATION

## Filtering

To limit the input voltage drop during hot-plug events, connect a $1 \mu \mathrm{~F}$ ceramic capacitor from VIN to GND. However, higher capacitor values will further reduce the voltage drop at the input.

Connect a sufficiently large capacitor from VOUT to GND. This capacitor helps to prevent inductive parasitics from pulling VOUT negative during turn-off or EMI damage to other components during the hot-detachment. It is also necessary for meeting the USB specification during hot plug-in operation. If SGM2554 is implanted in device end application, minimum $1 \mu \mathrm{~F}$ capacitor from VOUT to GND is recommended and higher capacitor values are also preferred.

In choosing these capacitors, special attention must be paid to the Effective Series Resistance (ESR) of the capacitors, to minimize the IR drop across the capacitor ESR. A lower ESR on this capacitor can get a lower IR drop during the operation.

Ferrite beads in series with all power and ground lines are recommended to eliminate or significantly reduce EMI. In selecting a ferrite bead, the DC resistance of the wire used must be kept to a minimum to reduce the voltage drop.


## Layout and Thermal Dissipation

- Place the switch as close to the USB connector as possible. Keep all traces as short as possible to reduce the effect of undesirable parasitic inductance.
- Place the output capacitor and ferrite beads as close as possible to the USB connector.
- If ferrite beads are used, use wires with minimum resistance and large'solder pads to minimize connection resistance.
- If the package is with dual VOUT or VIN pins, short both the same function pins as Figure 1 or Figure 2 to reduce the internal turn-on resistance. If the output power will be delivered to two individual ports, it is specially necessary to short both VOUT pins at the switch output side in order to protect the switch when each port is plug-in separately.
- Under normal operating conditions, the package can dissipate the channel heat away. Wide power-bus planes connected to VIN and VOUT and a ground plane in contact with the device will help dissipate additional heat.


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## PACKAGE OUTLINE DIMENSIONS

## SOT-23-5


RECOMMENDED LAND PATTERN (Unit: mm)


## TAPE AND REEL INFORMATION

## REEL DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.
KEY PARAMETER LIST OF TAPE AND REEL

| Package Type | Reel Diameter | $\begin{gathered} \text { Reel Width } \\ \text { W1 } \\ (\mathrm{mm}) \\ \hline \end{gathered}$ | AO $(\mathrm{mm})$ | $\underset{(\mathrm{mm})}{\mathrm{BO}}$ | $\begin{gathered} \text { K0 } \\ (\mathrm{mm}) \end{gathered}$ | $\begin{gathered} \text { P0 } \\ (\mathrm{mm}) \end{gathered}$ | $\begin{gathered} \mathrm{P} 1 \\ (\mathrm{~mm}) \end{gathered}$ | $\begin{gathered} \text { P2 } \\ (\mathrm{mm}) \end{gathered}$ | $\begin{gathered} \mathrm{W} \\ (\mathrm{~mm}) \end{gathered}$ | Pin1 Quadrant |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SOT-23-5 | $57^{\prime \prime}$ |  | 3.17 | 3.23 | 1.37 | 4.0 | 4.0 | 2.0 | 8.0 | Q3 |

## CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

| Reel Type | Length <br> $(\mathrm{mm})$ | Width <br> $(\mathrm{mm})$ | Height <br> $(\mathrm{mm})$ | Pizza/Carton |
| :---: | :---: | :---: | :---: | :---: |
| $7 "$ (Option) | 368 | 227 | 224 | 18 |
| $7 "$ | 442 | 410 | 224 | 18 |



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