

GENERAL DESCRIPTION

The SGM804 low-power micro-processor supervisor circuit monitors system voltages from 1.6V to 5V. This device performs a single function: it asserts a reset signal whenever the V_{CC} supply voltage falls below its reset threshold. The reset output remains asserted for the reset timeout period after V_{CC} rises above the reset threshold. The reset timeout is externally set by a capacitor to provide more flexibility.

The SGM804 has an active-low, push-pull reset output. It is available in Green SOT-23-5 package and is specified over an ambient temperature range of -40°C to $+85^{\circ}\text{C}$.

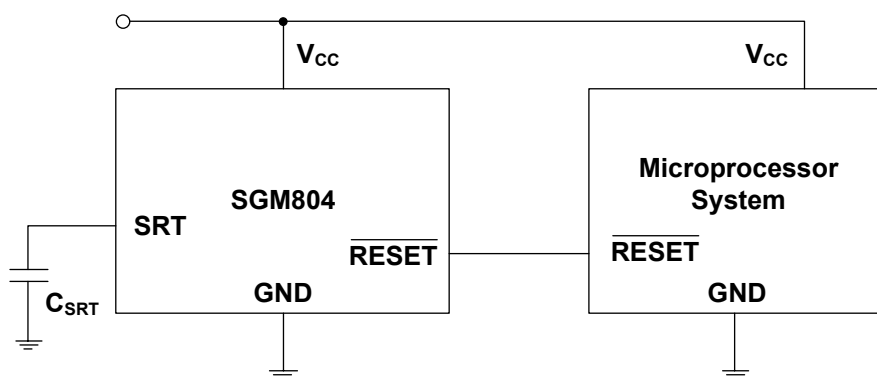
FEATURES

- Monitor System Voltages from 1.6V to 5V
- Capacitor-Adjustable Reset Timeout Period
- Low Quiescent Current: 3 μ A (TYP)
- Push-Pull $\overline{\text{RESET}}$ Output Option
- Guaranteed $\overline{\text{RESET}}$ Valid to $V_{CC} = 1\text{V}$
- Immune to Short V_{CC} Transients
- Available in Green SOT-23-5 Package

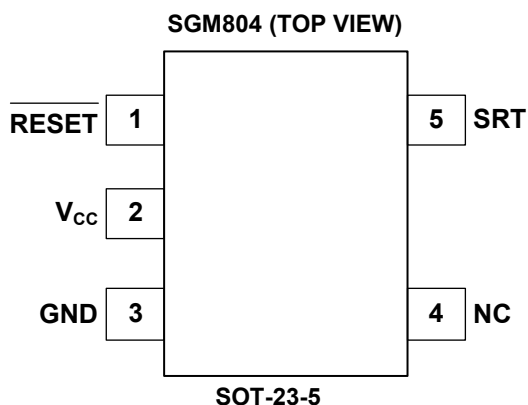
APPLICATIONS

- Portable Equipment
- Battery-Powered Computers/Controllers
- Automotive
- Medical Equipment
- Intelligent Instruments
- Embedded Controllers
- Critical μ P Monitoring
- Set-Top Boxes
- Computers

TYPICAL APPLICATION



PIN CONFIGURATION



PIN DESCRIPTION

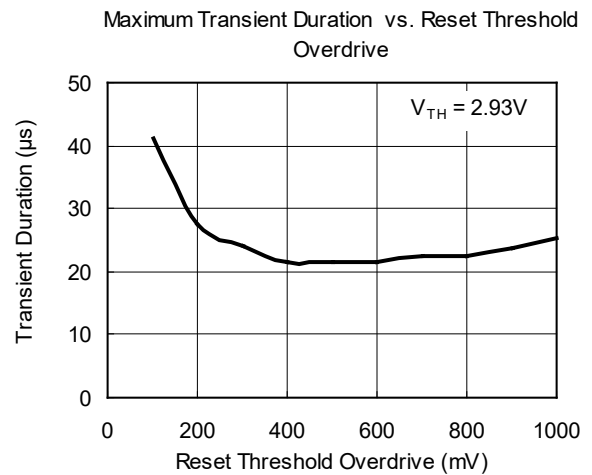
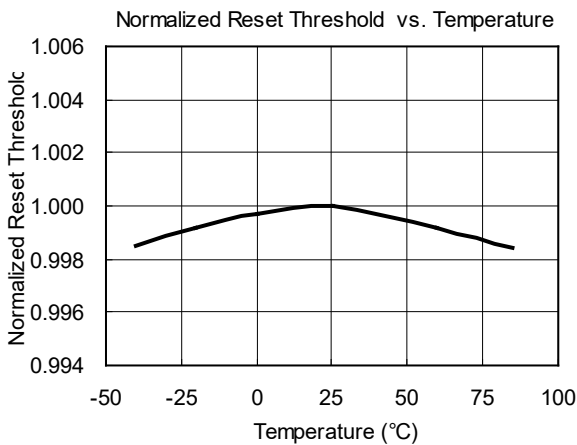
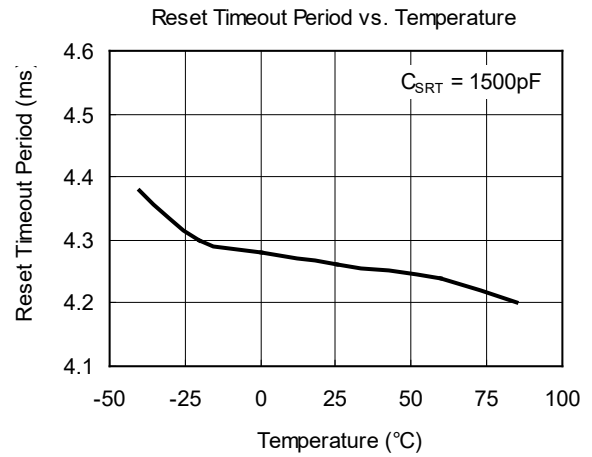
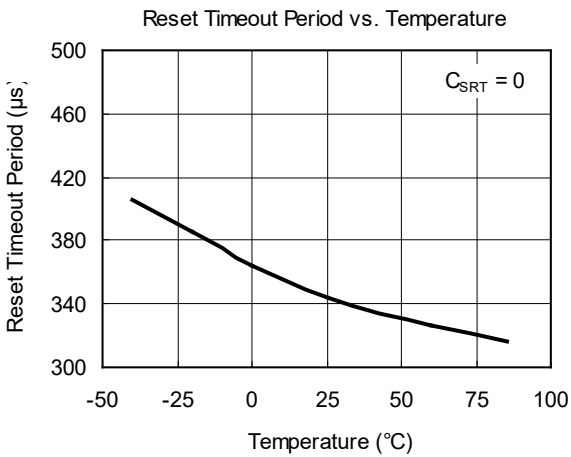
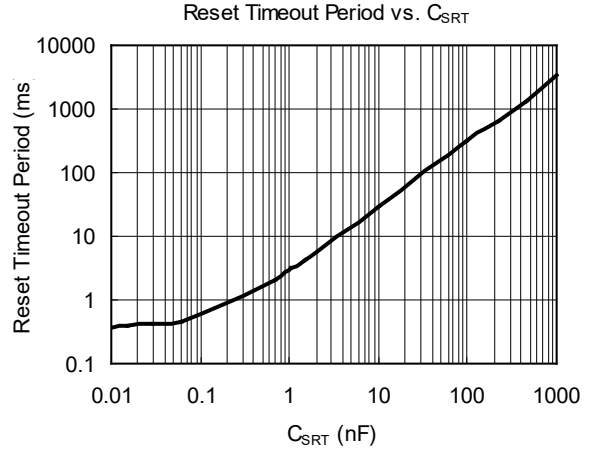
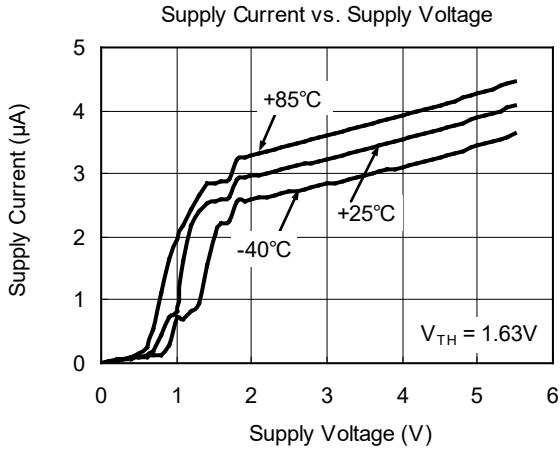
PIN	NAME	FUNCTION
1	$\overline{\text{RESET}}$	$\overline{\text{RESET}}$ changes from high to low whenever V_{CC} drops below the selected reset threshold voltage. $\overline{\text{RESET}}$ remains low for the reset timeout period after V_{CC} exceeds the reset threshold.
2	V_{CC}	Supply Voltage and Reset Threshold Monitor Input.
3	GND	Ground.
4	NC	Not Internally Connected. Can be connected to GND.
5	SRT	Set Reset Timeout Input. Connect a capacitor between SRT and ground to set the timeout period. Determine the period as follows: $t_{\text{RP}} = 2.6 \times 10^6 \times C_{\text{SRT}} + 340 \times 10^{-6}$ with t_{RP} in seconds and C_{SRT} in farads.

ELECTRICAL CHARACTERISTICS(V_{CC} = 1V to 5.5V, T_A = -40°C to +85°C, typical values are at V_{CC} = 5V and T_A = +25°C, unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage Range	V _{CC}		1.0		5.5	V
Supply Current	I _{CC}	V _{CC} ≤ 5.0V		3.9	7.0	μA
		V _{CC} ≤ 3.3V		3.4	5.5	
		V _{CC} ≤ 2.0V		3.0	4.8	
V _{CC} Reset Threshold Accuracy	V _{TH}	T _A = +25°C	V _{TH} - 2.5%		V _{TH} + 2.5%	V
		T _A = -40°C to +85°C	V _{TH} - 3.5%		V _{TH} + 3.5%	
Hysteresis	V _{HYST}			4 × V _{TH}		mV
V _{CC} to Reset Delay	t _{RD}	V _{CC} falling at 1mV/μs		80		μs
Reset Timeout Period	t _{RP}	C _{SRT} = 1500pF	3.00	4.25	5.75	ms
		C _{SRT} = 0		0.34		
V _{SRT} Ramp Current	I _{RAMP}	V _{SRT} = 0V to 0.65V, V _{CC} = 1.6V to 5V		210		nA
V _{SRT} Ramp Threshold	V _{TH-RAMP}	V _{CC} = 1.6V to 5V (V _{RAMP} rising)		0.6		V
$\overline{\text{RESET}}$ Output Voltage Low	V _{OL}	V _{CC} ≥ 1.0V, I _{SINK} = 50μA			0.3	V
		V _{CC} ≥ 2.7V, I _{SINK} = 1.2mA			0.3	
		V _{CC} ≥ 4.5V, I _{SINK} = 3.2mA			0.4	
$\overline{\text{RESET}}$ Output Voltage High, Push-Pull	V _{OH}	V _{CC} ≥ 1.8V, I _{SOURCE} = 200μA	0.8 × V _{CC}			V
		V _{CC} ≥ 2.25V, I _{SOURCE} = 500μA	0.8 × V _{CC}			
		V _{CC} ≥ 4.5V, I _{SOURCE} = 800μA	0.8 × V _{CC}			

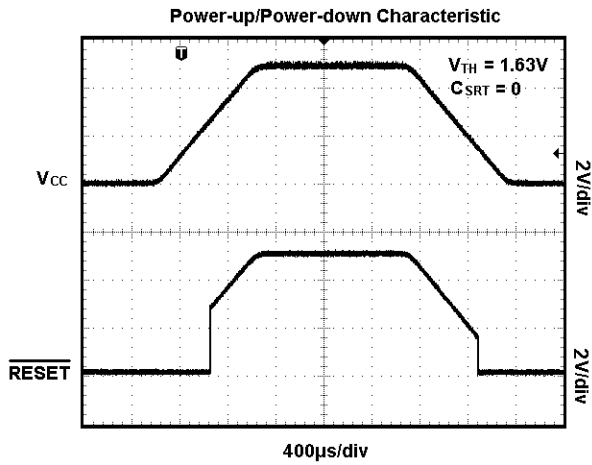
TYPICAL PERFORMANCE CHARACTERISTICS

$V_{CC} = 5V$, $C_{SRT} = 1500pF$, $T_A = +25^\circ C$, unless otherwise noted.



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{CC} = 5V$, $C_{SRT} = 1500pF$, $T_A = +25^\circ C$, unless otherwise noted.



DETAILED DESCRIPTION

Reset Output

The reset output is typically connected to the reset input of a μ P. A μ P's reset input starts or restarts the μ P in a known state. The SGM804 μ P supervisory circuit provides the reset logic to prevent code-execution errors during power-up, power-down, and brownout conditions.

$\overline{\text{RESET}}$ changes from high to low whenever V_{CC} drops below the threshold voltage. Once V_{CC} exceeds the threshold voltage, $\overline{\text{RESET}}$ remains low for the capacitor-adjustable reset timeout period.

This device output is guaranteed valid for $V_{CC} > 1V$.

Operating as a Voltage Detector

The SGM804 can be operated in a voltage detector mode by floating the SRT pin. The reset delay times for V_{CC} rising above or falling below the threshold are not significantly different. The reset output is deasserted smoothly without false pulses.

Selecting a Reset Capacitor

The reset timeout period is adjustable to accommodate a variety of μ P applications. Adjust the reset timeout period (t_{RP}) by connecting a capacitor (C_{SRT}) between SRT and ground. Calculate the reset timeout capacitor as follows:

$$C_{SRT} = (t_{RP} - 340 \times 10^{-6}) / (2.6 \times 10^6)$$

where t_{RP} is in seconds and C_{SRT} is in farads.

The reset delay time is set by a current/capacitor-controlled ramp compared to an internal 0.6V reference. An internal 210nA ramp current source charges the external capacitor. The charge to the capacitor is cleared when a reset condition is detected. Once the reset condition is removed, the voltage on the capacitor ramps according to the formula: $dV/dt = I/C$. The C_{SRT} capacitor must ramp to 0.6V to deassert the reset. C_{SRT} must be a low-leakage ($<10nA$) type capacitor; ceramic is recommended.

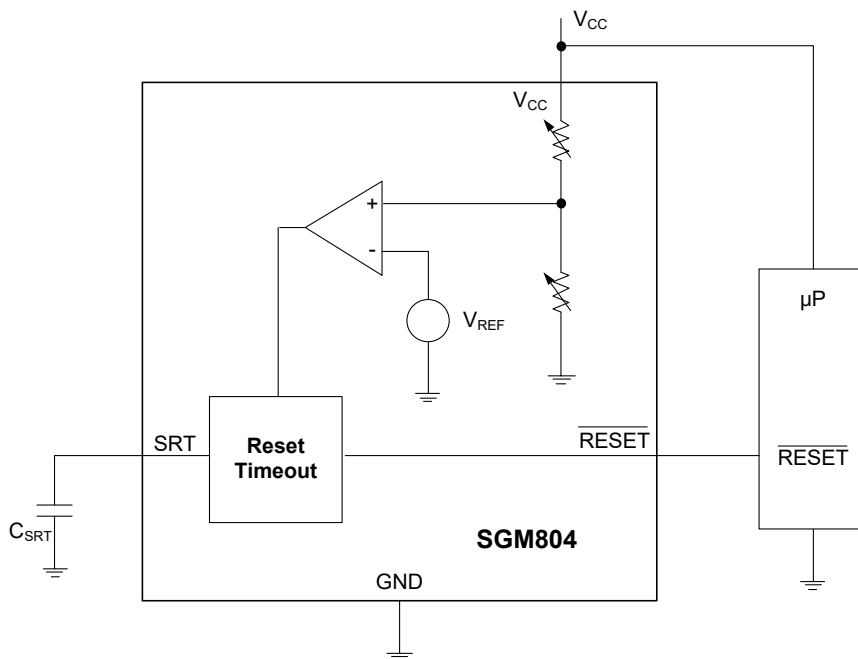


Figure 1. Typical Operating Circuit

APPLICATION INFORMATION

Negative-Going V_{CC} Transients

In addition to issuing a reset to the μ P during power-up, power-down, and brownout conditions, this supervisor is relatively immune to short-duration negative-going transients (glitches). The graph Maximum Transient Duration vs. Reset Threshold Overdrive in the Typical Performance Characteristics shows this relationship.

The area below the curve of the graph is the region in which these devices typically do not generate a reset pulse. This graph was generated using a negative-going pulse applied to V_{CC} , starting above the actual reset threshold (V_{TH}) and ending below it by the magnitude indicated (reset-threshold overdrive). As the magnitude of the transient decreases (further below the reset threshold), the maximum allowable pulse width-decreases. Typically, a V_{CC} transient that goes 100mV below the reset threshold and lasts 50 μ s or less does not cause a reset pulse to be issued.

Ensuring a Valid \overline{RESET} Down to $V_{CC} = 0$

When V_{CC} falls below 1V, \overline{RESET} current-sinking (sourcing) capabilities decline drastically. In the case of the SGM804, high-impedance CMOS-logic inputs connected to \overline{RESET} can drift to undetermined voltages. This presents no problems in most applications, since most μ Ps and other circuitry do not operate with V_{CC} below 1V.

In those applications where \overline{RESET} must be valid down to zero, adding a pull-down resistor between \overline{RESET} and ground sinks any stray leakage currents, holding \overline{RESET} low (Figure 2). The value of the pull-

down resistor is not critical; 100k Ω is large enough not to load \overline{RESET} and small enough to pull \overline{RESET} to ground.

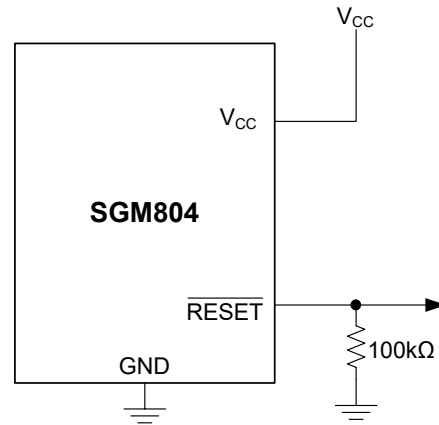


Figure 2. Ensuring \overline{RESET} Valid to $V_{CC} = 0$

Layout Consideration

SRT is a precision current source. When developing the layout for the application, be careful to minimize board capacitance and leakage currents around this pin. Traces connected to SRT should be kept as short as possible. Traces carrying high-speed digital signals and traces with large voltage potentials should be routed as far from SRT as possible. Leakage current and stray capacitance (e.g., a scope probe) at this pin could cause errors in the reset timeout period. When evaluating these parts, use clean prototype boards to ensure accurate reset periods.

REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

JANUARY 2013 – REV.A to REV.A.1

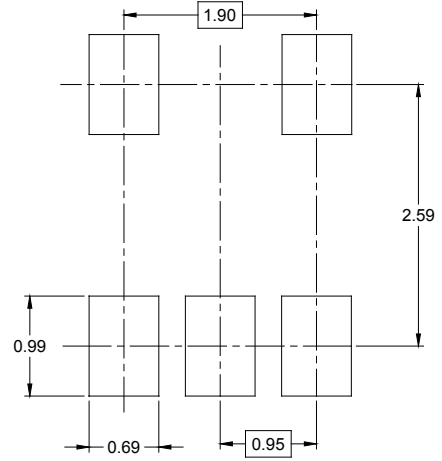
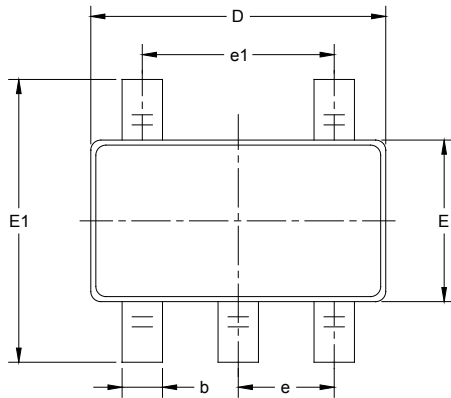
Added Tape and Reel Information section 10, 11

Changes from Original (MARCH 2012) to REV.A

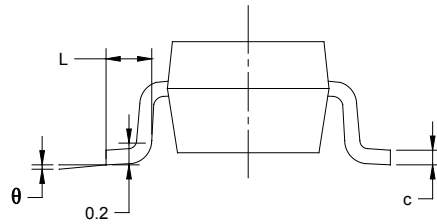
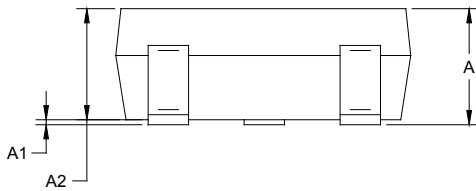
Changed from product preview to production data All

PACKAGE OUTLINE DIMENSIONS

SOT-23-5



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 BSC		0.037 BSC	
e1	1.900 BSC		0.075 BSC	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

PACKAGE INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23-5	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3

000001

PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18

DD0002

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[NLVHCT132ADTR2G](#) [NL17SG86P5T5G](#) [NL17SZ05P5T5G](#) [NLV74VHC00DTR2G](#) [NLVVHC1G02DFT1G](#) [NLV74HC86ADR2G](#)
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