

Features

- ❑ Transient protection for high-speed data lines
 - IEC 61000-4-2 (ESD) $\pm 30\text{kV}$ (Air)
 - $\pm 30\text{kV}$ (Contact)
 - IEC 61000-4-4 (EFT) 40A (5/50 ns)
 - Cable Discharge Event (CDE)
- ❑ Package optimized for high-speed lines
- ❑ Ultra-small package (0.6mm×0.3mm×0.27mm)
- ❑ Protects one data, control or power line
- ❑ Low capacitance: 0.6pF (Typical)
- ❑ Low leakage current: $0.1\mu\text{A}$ @ V_{RWM} (Typical)
- ❑ Low clamping voltage
- ❑ Each I/O pin can withstand over 1000 ESD strikes for $\pm 8\text{kV}$ contact discharge
- ❑ ROHS compliant

Description

TT0511TAX is an ultra low-capacitance Transient Voltage Suppressor (TVS) designed to provide electrostatic discharge (ESD) protection for high-speed data interfaces. With typical capacitance of 0.6pF only, TT0511TAX is designed to protect parasitic-sensitive systems against over-voltage and over-current transient events. It complies with IEC 61000-4-2 (ESD ($\pm 10\text{kV}$ air, $\pm 10\text{kV}$ contact discharge), IEC 61000-4-4 (electrical fast transient - EFT) (40A, 5/50 ns), very fast charged device model (CDM) ESD and cable discharge event (CDE), etc.

TT0511TAX use DFN0603-2L package. Each TT0511TAX device can protect one high-speed data line. It offers system designers flexibility to protect single data line where space is a premium concern. The combined features of low capacitance, ultra-small size and high ESD robustness make TT0511TAX ideal for high-speed data port and high-frequency line (e.g., HDMI & antenna line) applications, such as cellular phones and HD visual devices.

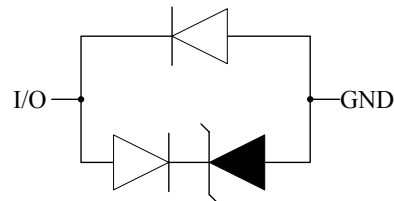
Applications

- ❑ Serial ATA
- ❑ PCI Express
- ❑ Desktops, Servers and Notebooks
- ❑ Cellular Phones
- ❑ MDDI Ports
- ❑ USB2.0/3.0 Power and Data Line Protection
- ❑ Display Ports
- ❑ High Definition Multi-Media Interface (HDMI)
- ❑ Digital Visual Interfaces (DVI)

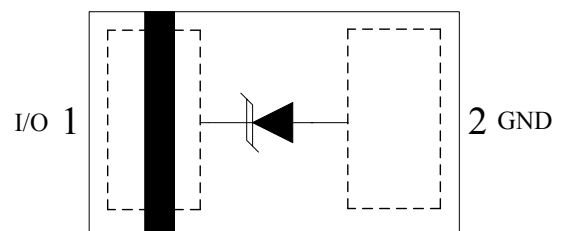
Mechanical Characteristics

- ❑ DFN0603-2L package
- ❑ Flammability Rating: UL 94V-0
- ❑ Marking: Part number
- ❑ Packaging: Tape and Reel

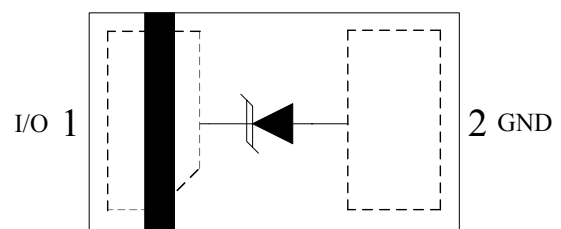
Circuit Diagram



Pin Configuration



OR



DFN0603-2L
(Top View)

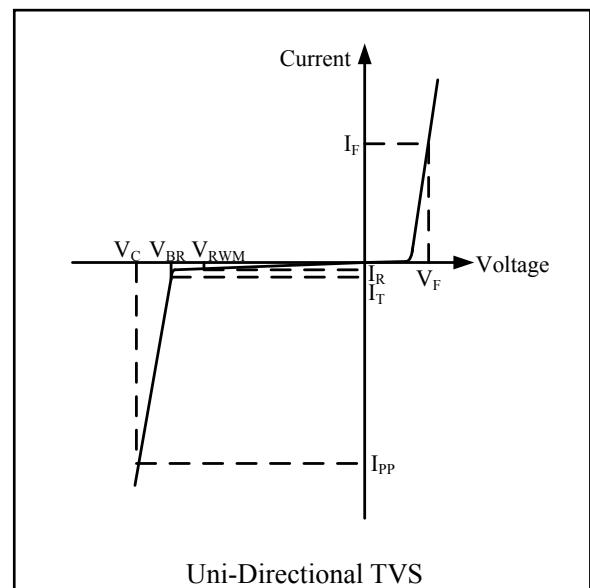
service@jy-electronics.com.cn

Absolute Maximum Rating

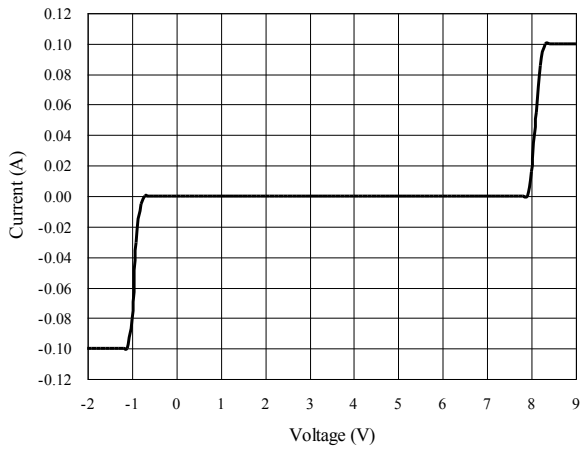
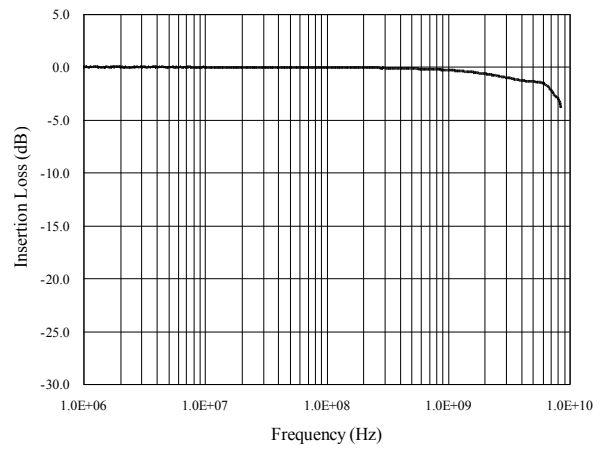
Symbol	Parameter	Value	Units
I_{PP}	Peak Pulse Current($t_p=8/20\mu s$)	5	A
V_{ESD}	ESD per IEC 61000-4-2(Air) ESD per IEC 61000-4-2 (Contact)	± 30 ± 30	kV
T_{OPT}	Operating Temperature	-55/+125	°C
T_{STG}	Storage Temperature	-55/+150	°C

Electrical Characteristics (T = 25°C)

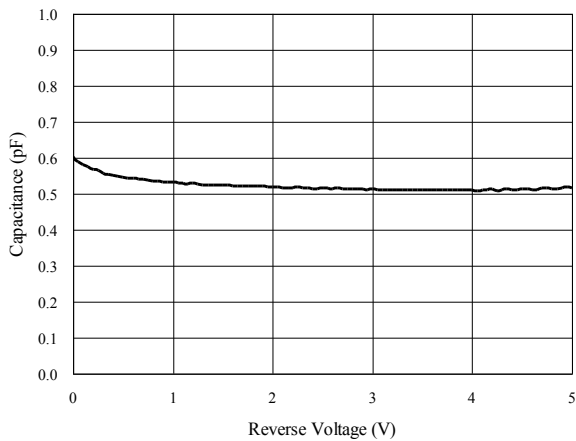
Symbol	Parameter
V_{RWM}	Nominal Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Reverse Breakdown Voltage @ I_T
I_T	Test Current for Reverse Breakdown
V_C	Clamping Voltage @ I_{PP}
I_{PP}	Maximum Peak Pulse Current
C_{ESD}	Parasitic Capacitance
V_R	Reverse Voltage
f	Small Signal Frequency
I_F	Forward Current
V_F	Forward Voltage @ I_F



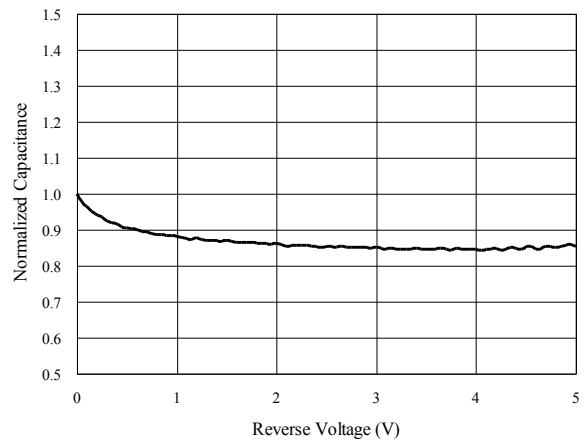
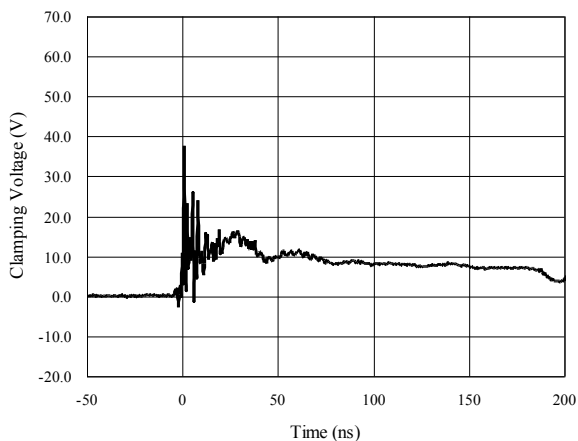
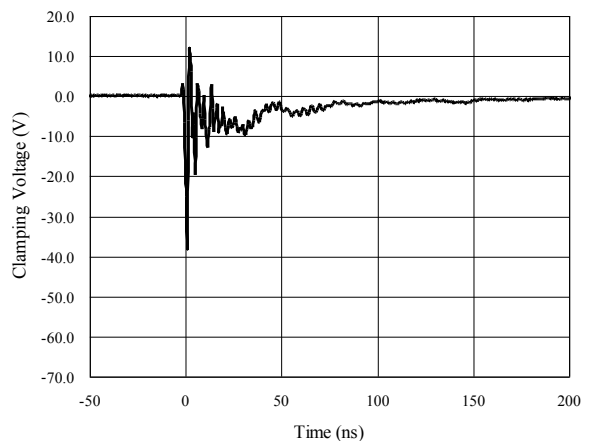
Symbol	Test Condition	Minimum	Typical	Maximum	Units
V_{RWM}				5.0	V
I_R	$V_{RWM} = 5V, T = 25^\circ C$ Between I/O and GND		0.1	1.0	μA
V_{BR}	$I_T = 1mA$ Between I/O and GND	6.0	8.0	9.0	V
V_C	$I_{PP} = 5A, t_p = 8/20\mu s$ Between I/O and GND			11	V
C_{ESD}	$V_R = 0V, f = 1MHz$ Between I/O and GND		0.6	0.85	pF

Voltage Sweeping of I/O to GND

Insertion Loss S21 of I/O to GND

Capacitance vs. Voltage of I/O to GND (f = 1MHz)

Capacitance vs. Reverse Voltage

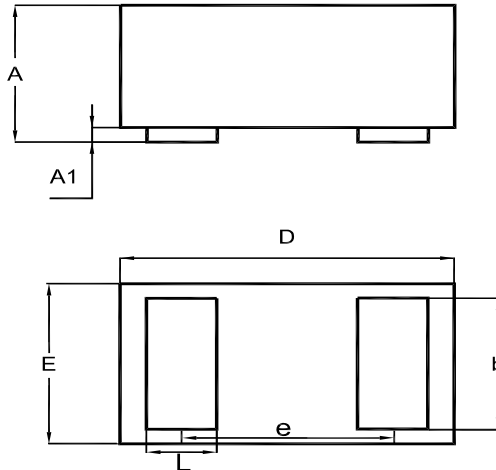


Normalized Capacitance vs. Reverse Voltage

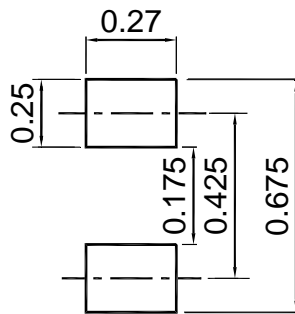

**ESD Clamping of I/O to GND
(+8kV Contact per IEC 61000-4-2)**

**ESD Clamping of I/O to GND
(-8kV Contact per IEC 61000-4-2)**


PACKAGE OUTLINE

Plastic surface mounted package; 2 leads DFN0603



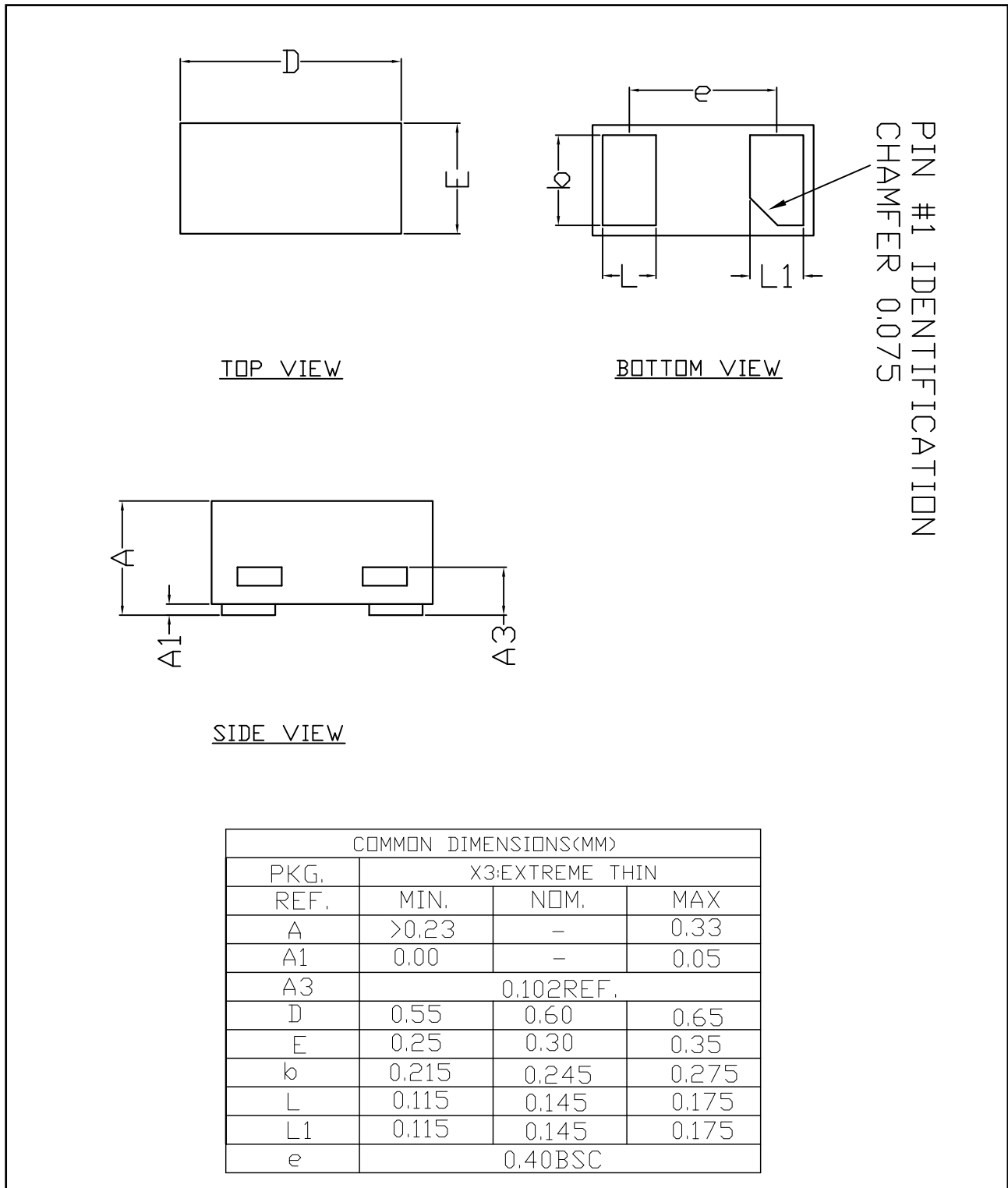
UNIT	A	A1	b	D	E	e	L
mm	0.27	0	0.21	0.57	0.28	0.355	0.14
	0.33	0.025	0.29	0.65	0.35		0.22

Recommended Soldering Footprint

Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN0603	8	4 ± 0.1	0.157 ± 0.004	178	7	10,000

Package Outline

- DFN0603 package
- 2 leads
- MSL-1



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