

1N4001G THRU 1N4007G

Surface Mount General Purpose Silicon Rectifiers

Reverse Voltage - 50 to 1000 V Forward Current - 1 A

FEATURES

- For surface mounted applications
- · Low profile package
- Glass Passivated Chip Junction
- Easy to pick and place
- Lead free in comply with EU RoHS 2011/65/EU directives

MECHANICAL DATA

- Case: SMA
- Terminals: Solderable per MIL-STD-750, Method 2026

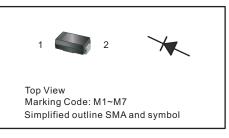
Ratings at 25 °C ambient temperature unless otherwise specified.

• Approx. Weight: 0.055g / 0.002oz

Maximum Ratings and Electrical characteristics

PINNING

PIN	DESCRIPTION			
1	Cathode			
2	Anode			



Parameter	Symbols	1N4001G	1N4002G	1N4003G	1N4004G	1N4005G	1N4006G	1N4007G	Units
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current	I _{F(AV)}	1						А	
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	30						А	
Maximum Instantaneous Forward Voltage at 1 A	V _F	1.1						V	
Maximum DC Reverse Current $T_a = 25 \ ^{\circ}C$ at Rated DC Blocking Voltage $T_a = 125 \ ^{\circ}C$	I _R	5 50						μA	
Typical Junction Capacitance (1)	Cj	15						pF	
Typical Thermal Resistance (2)	R _{eja}	75						°C/W	
Operating and Storage Temperature Range	Tj, T _{stg}	-55 ~ +150						°C	

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C $\,$

(2) P.C.B. mounted with 1.0 X 1.0" (2.54 X 2.54 cm) copper pad areas.

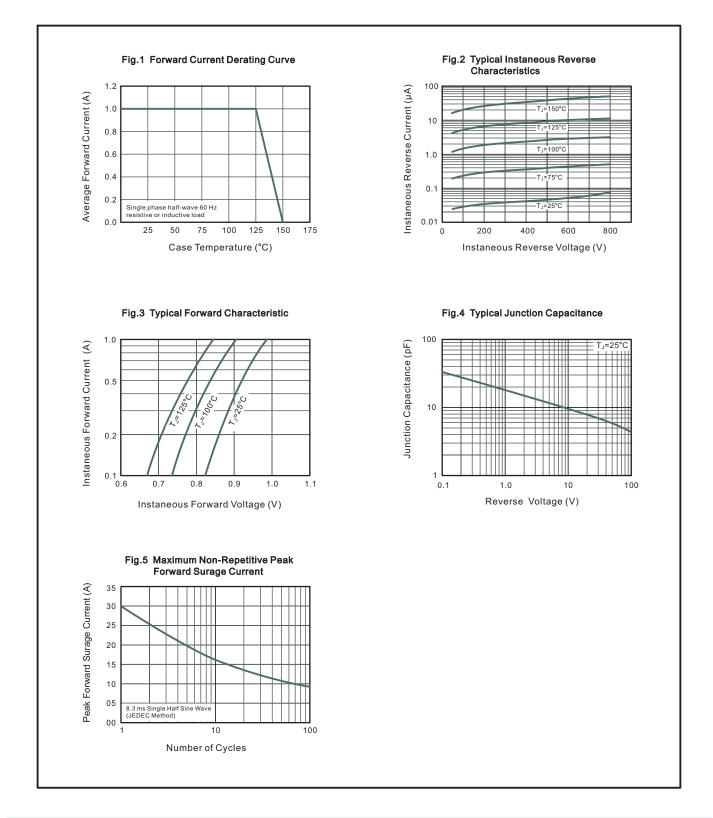
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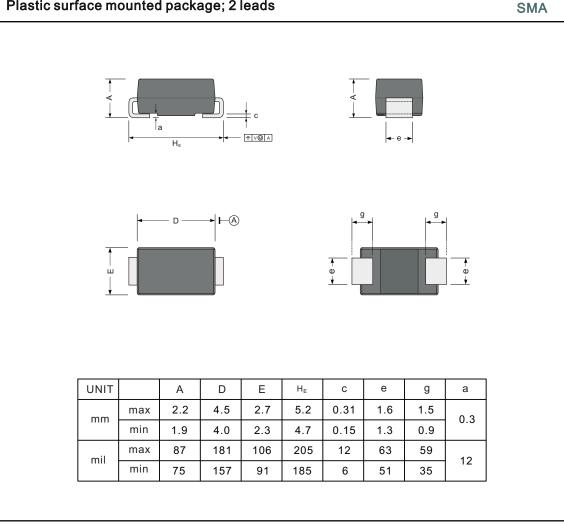
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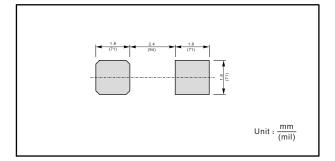


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PACKAGE OUTLINE Plastic surface mounted package; 2 leads



The recommended mounting pad size



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Marking

Type number	Marking code				
1N4001G	M1				
1N4002G	M2				
1N4003G	M3				
1N4004G	M4				
1N4005G	M5				
1N4006G	M6				
1N4007G	M7				



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