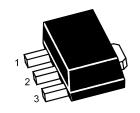




## **NPN Silicon Epitaxial Planar Transistor**

for switching and amplifier applications. Especially suitable for AF-driver stages and low power output stages.



1.Base 2.Collector 3.Emitter SOT-89 Plastic Package

## Absolute Maximum Ratings (T<sub>a</sub> = 25 °C)

Parameter	Symbol	Value	Unit
Collector Base Voltage	V <sub>CBO</sub>	40	V
Collector Emitter Voltage	V <sub>CEO</sub>	25	V
Emitter Base Voltage	$V_{EBO}$	6	V
Collector Current	I <sub>C</sub>	0.8	Α
Power Dissipation	P <sub>tot</sub>	625	mW
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature Range	T <sub>Stg</sub>	- 55 to + 150	°C

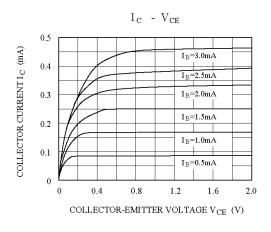
#### Characteristics at T<sub>a</sub> = 25 °C

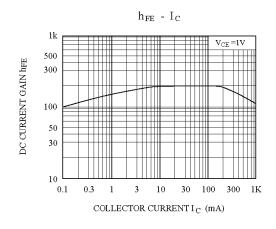
Characteristics at Ta = 25 °C					
Parameter	Symbol	Min.	Тур.	Max.	Unit
DC Current Gain at $V_{CE} = 1 \text{ V}$ , $I_C = 5 \text{ mA}$ at $V_{CE} = 1 \text{ V}$ , $I_C = 100 \text{ mA}$ Current Gain Group L	h <sub>FE</sub> h <sub>FE</sub>	45 120 200	- - -	- 270 400	-
at $V_{CE} = 1 \text{ V}, I_{C} = 800 \text{ mA}$	h <sub>FE</sub>	40	_	-	-
Collector Base Cutoff Current at $V_{CB} = 35 \text{ V}$	I <sub>CBO</sub>	-	-	100	nA
Emitter Base Cutoff Current at $V_{EB} = 6 \text{ V}$	I <sub>EBO</sub>	-	-	100	nA
Collector Base Breakdown Voltage at $I_C = 100 \mu A$	V <sub>(BR)CBO</sub>	40	-	-	V
Collector Emitter Breakdown Voltage at $I_C = 2 \text{ mA}$	$V_{(BR)CEO}$	25	-	-	V
Emitter Base Breakdown Voltage at $I_E = 100 \mu A$	$V_{(BR)EBO}$	6	-	-	V
Collector Emitter Saturation Voltage at $I_C = 800$ mA, $I_B = 80$ mA	V <sub>CE(sat)</sub>	-	-	0.5	V
Base Emitter Saturation Voltage at $I_C = 800$ mA, $I_B = 80$ mA	$V_{BE(sat)}$	-	-	1.2	V
Base Emitter Voltage at $I_C = 10$ mA, $V_{CE} = 1$ V	$V_{BE}$	-	-	1	V
Gain Bandwidth Product at $V_{CE} = 10 \text{ V}$ , $I_C = 50 \text{ mA}$	f⊤	100	-	-	MHz
Collector Base Capacitance at V <sub>CB</sub> = 10 V, f = 1 MHz	СОВ	-	9	-	pF

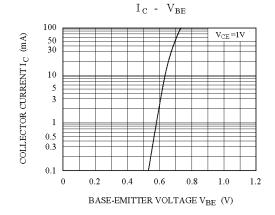
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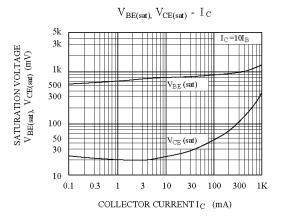


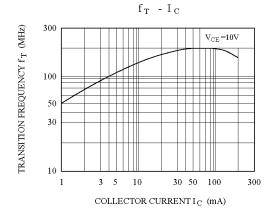


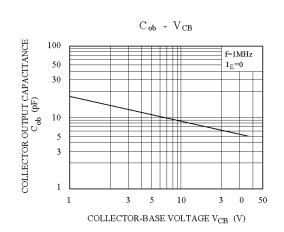






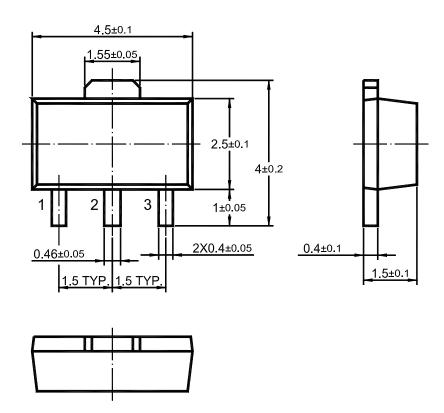








### **SOT-89 PACKAGE OUTLINE**



Dimensions in mm

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