

### PMEG3020ER

# **Surface Mount Schottky Barrier Rectifier Features**

#### Reverse Voltage - 3 0 V Forward Current - 2.0A

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

#### **MECHANICAL DATA**

• Case: SOD-123FL

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight:15mg 0.00048oz

# 1 2

 $Top\ View\quad 1 \subset Cathode\quad 2 \subset Anode$ 

Simplified outline SOD-123FL and symbol

#### **Absolute Maximum Ratings and Electrical characteristics**

Ratings at  $25^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by  $20^{\circ}$ 

Parameter	Symbols		Units
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	30	V
Maximum RMS voltage	V <sub>RMS</sub>	28	٧
Maximum DC Blocking Voltage	V <sub>DG</sub>	40	V
Maximum Average Forward Rectified Current	<sub>F(AV)</sub>	2.0	Α
Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I <sub>FSM</sub>	50	А
Max Instantaneous Forward Voltage at 2 A	V <sub>F</sub>	0.55	٧
Maximum DC Reverse Current $T_a = 25^{\circ}$ C at Rated DC Reverse Voltage $T_a = 100^{\circ}$ C	I <sub>R</sub>	0.5 5	mA
Typical Junction Capacitance <sup>1)</sup>	Cj	220	pF
Typical Thermal Resistance <sup>2)</sup>	R <sub>eJA</sub>	80	°C/W
Operating Junction Temperature Range	Тј	-55 ~ +125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 ~ +150	°C

<sup>1)</sup> Measured at 1MHz and applied reverse voltage of 4 V D.C.

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<sup>2)</sup> P.C.B. mounted with 0.2 X 0.2" (5 X 5 mm) copper pad areas.



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Fig.1 Forward Current Derating Curve

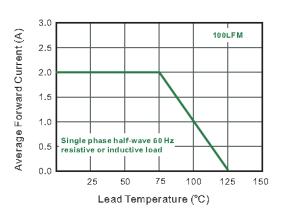


Fig.3 Typical Forward Characteristic

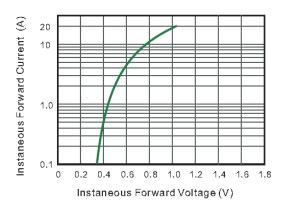


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

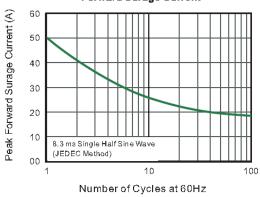


Fig.2 Typical Reverse Characteristics

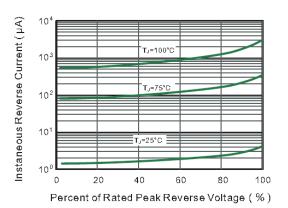


Fig.4 Typical Junction Capacitance

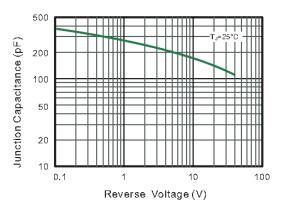
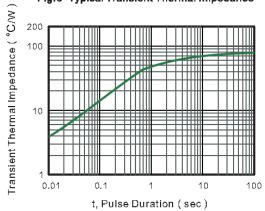


Fig.6- Typical Transient Thermal Impedance

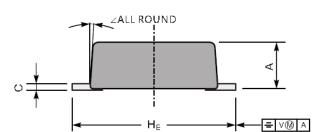




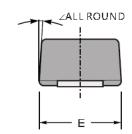
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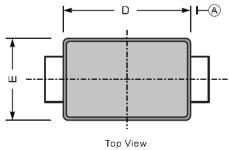
#### **PACKAGE OUTLINE**

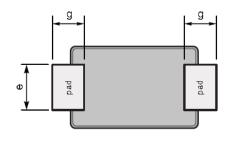
### Plastic surface mounted package; 2 leads







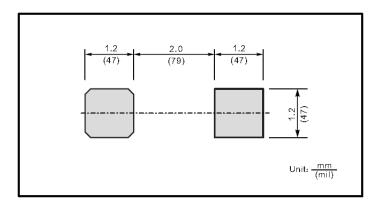




Bottom View

UNIT		Α	С	D	Е	е	g	HE	_	
mm	max	1.1	0.20	2.9	1.9	1.1	0.9	3.8		
	min	0.9	0.12	2.6	1.7	0.8	0.7	3.5	7°	
mil	max	43	7.9	114	75	43	35	150	,	
	min	35	4.7	102	67	31	28	138		

### The recommended mounting pad size



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