

N-Channel Enhancement Mode MOSFET

Feature

30V/4.2A $R_{DS(ON)} = 50m\Omega(\text{MAX})$ @ $V_{GS} = 10V$.

$R_{DS(ON)} = 60m\Omega(\text{MAX})$ @ $V_{GS} = 4.5V$.

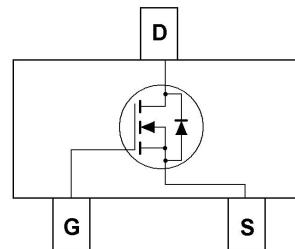
Super High dense cell design for extremely low $R_{DS(ON)}$.

Reliable and Rugged.

SC-59 for Surface Mount Package.



SC-59



Applications

- Power Management

Portable Equipment and Battery Powered Systems.

Absolute Maximum Ratings

$T_A = 25^\circ C$ Unless Otherwise noted

Parameter	Symbol	Limit	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous	I_D	4.2	A

Electrical Characteristics

$T_A = 25^\circ C$ Unless Otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Units
Off Characteristics						
Drain to Source Breakdown Voltage	BVDSS	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
Zero-Gate Voltage Drain Current	IDSS	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
Gate Body Leakage Current, Forward	IGSSF	$V_{GS}=12V, V_{DS}=0V$	-	-	100	nA
Gate Body Leakage Current, Reverse	IGSSR	$V_{GS}=-12V, V_{DS}=0V$	-	-	-100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{GS}=V_{DS}, I_D=250\mu A$	0.6	-	2.5	V
Static Drain-source On-Resistance	RDS(ON)	$V_{GS}=10V, I_D=4A$	-	48	50	$m\Omega$
		$V_{GS}=4.5V, I_D=3A$	-	55	60	$m\Omega$
		$V_{GS}=2.5V, I_D=2A$	-	60	65	$m\Omega$
Drain-Source Diode Characteristics and Maximum Ratings						
Drain-Source Diode Forward Voltage	VSD	$V_{GS}=0V, I_S=1.25A$			1.2	V

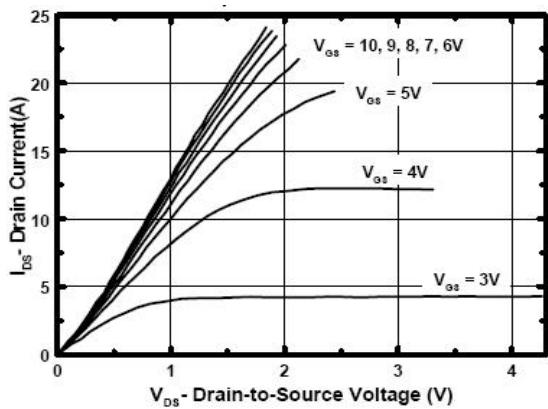
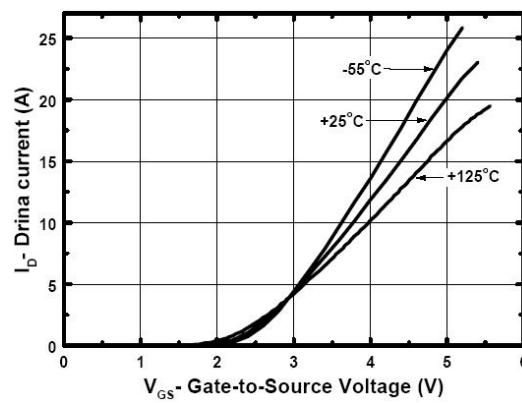
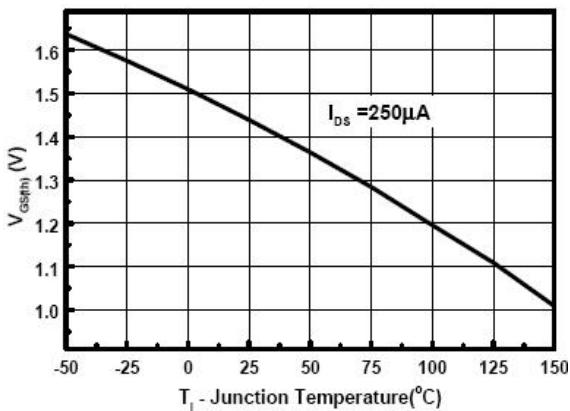
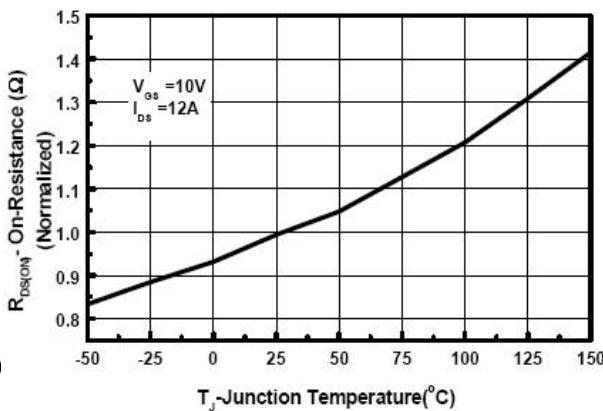
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Dynamic

Q _g	Total Gate Charge	V _{DS} =15V,V _{GS} =10V,I _D =2A	8.5	12	nC
Q _{gs}	Gate-Source Charge		1.1		
Q _{gd}	Gate-Drain Charge		1.8		
t _{on}	Turn-on Time	V _{DD} =15V,I _D =2A,V _{GS} =10V,R _G =6Ω		40	ns
t _{d(on)}	Turn-on Delay time			11	
t _r	Turn-on Rise Time			17	
T _{d(off)}	Turn-off Delay Time			37	
t _f	Turn-off Fall Time			20	
t _{off}	Turn-off Time			60	

Typical Characteristics

Figure 1 . Output Characteristics

Figure 2 . Transfer Characteristics
Typical Characteristics

Figure 3 . Gate Threshold Variation with Temperature

Figure 4 . On-Resistance Variation with Temperature
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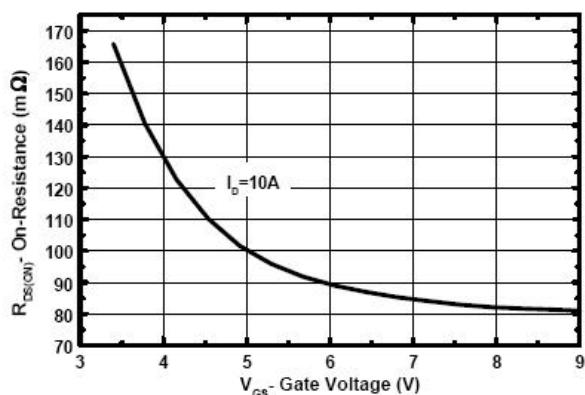


Figure 5 . On-Resistance vs. Gate-to-Source Voltage

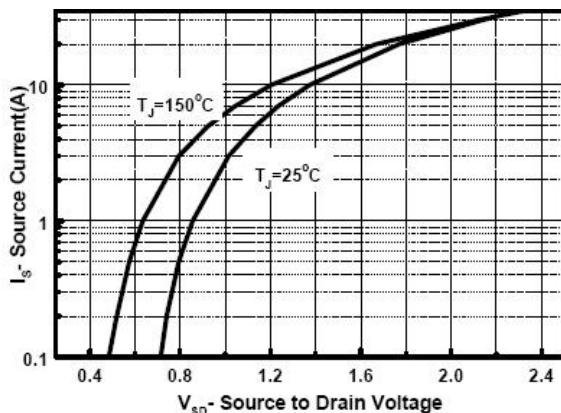
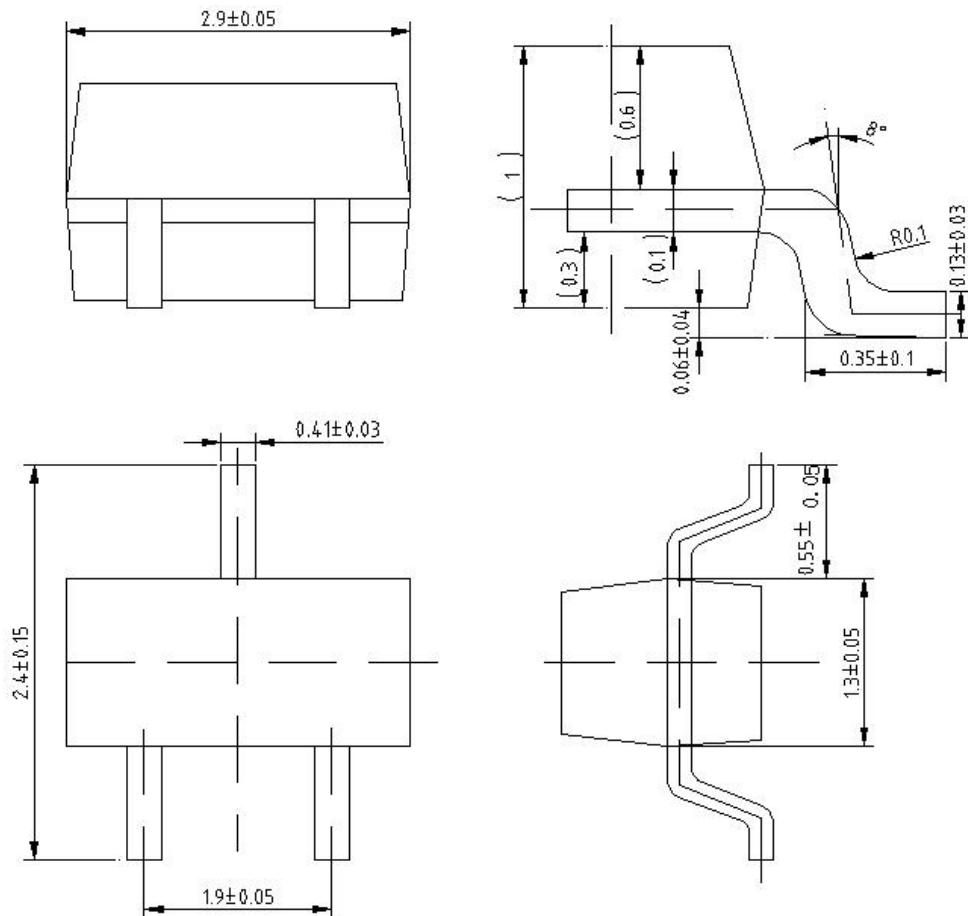


Figure 6 . Source-Drain Diode Forward Voltage



Package Outline Dimensions (UNIT: mm)



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