

P60B6EL

Power MOSFETs 60V, 60A, N-channel

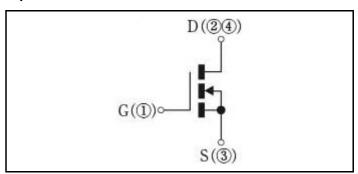
Feature

- N-channel
- SMD
- · Low Ron
- 4.5V Gate Drive
- Low Capacitance
- · Pb free terminal
- RoHS:Yes

OUTLINE



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 150	°C
Channel tempertature	Tch		150	°C
Drain-source voltage	V_{DSS}		60	V
Gate-source voltage	V_{GSS}		±20	V
Continuous drain current(DC)	I _D		60	Α
Continuous drain current(Peak)	I _{DP}	Pulse width 10µs, duty=1/100	240	Α
Total power dissipation	P _T		62.5	W
Single avalanche current	I _{AS}	Starting Tch=25°C Tch≦150°C	35	Α
Single avalanche energy	E _{AS}	Starting Tch=25°C Tch≦150°C	153	mJ

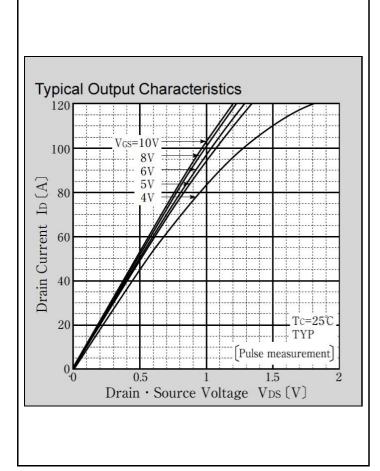
^{* :} See the original Specifications

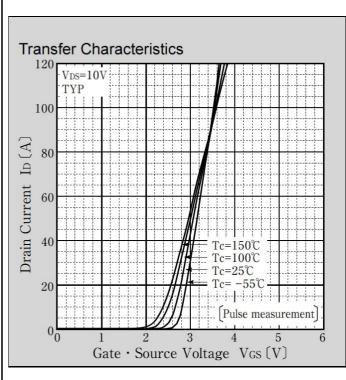
Electrical Characteristics (unless otherwise specified : Tc=25°C)

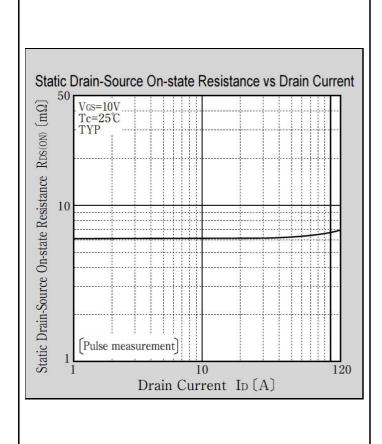
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	Oilit
Drain-Source breakdown voltage	V _{(BR)DSS}	ID=1mA, VGS=0V	60			V
Zero gate voltage drain current	I _{DSS}	VDS=60V, VGS=0V			1	μΑ
Gate-source leakage current	I _{GSS}	VGS=±20V, VDS=0V			±0.1	μΑ
Forward transconductance	9fs	ID=30A, VDS=10V	19	38		S
Static drain-source on-state resistance	R _{DS(ON)}	ID=30A, VGS=10V		0.0061	0.0077	Ω
Static drain-source on-state resistance	R _{DS(ON)}	ID=30A, VGS=4.5V		0.0073	0.0098	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	V_{SD}	IS=60A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case			2	°C/W
Total gate charge	Qg	VDD=48V, VGS=10V, ID=60A		55		nC
Gate to source charge	Qgs	VDD=48V, VGS=10V, ID=60A		10		nC
Gate to drain charge	Qgd	VDD=48V, VGS=10V, ID=60A		18		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		2920		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		190		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		355		pF
Turn-on delay time	td(on)	ID=30A, RL=1 Ω , VDD=30V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		9		ns
Rise time	tr	ID=30A, RL=1 Ω , VDD=30V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		24		ns
Turn-off delay time	td(off)	ID=30A, RL=1 Ω , VDD=30V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		23		ns
Fall time	tf	ID=30A, RL=1 Ω , VDD=30V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		4		ns
Diode reverse recovery time	trr	IF=60A, VGS=0V, di/dt=100A/μs		45		ns
Diode reverse recovery charge	Qrr	IF=60A, VGS=0V, di/dt=100A/μs		65		nC

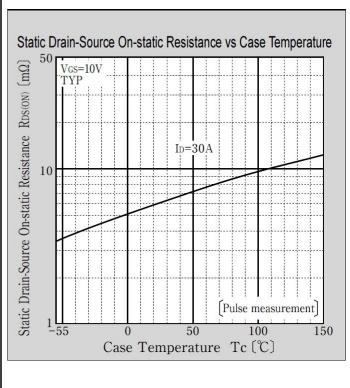
^{*} :See the original Specifications

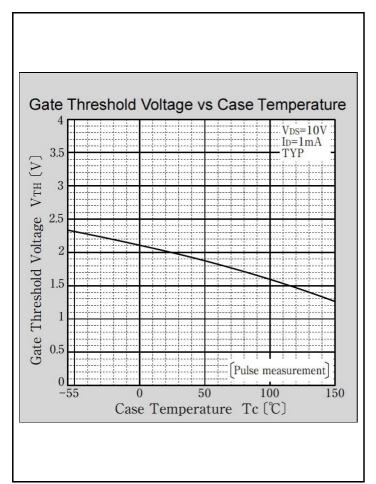
CHARACTERISTIC DIAGRAMS

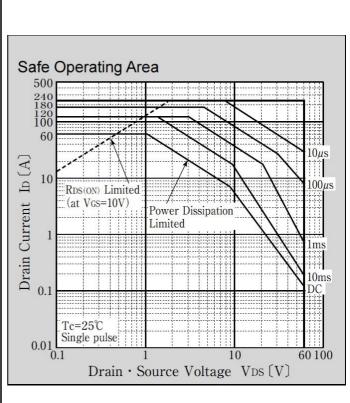


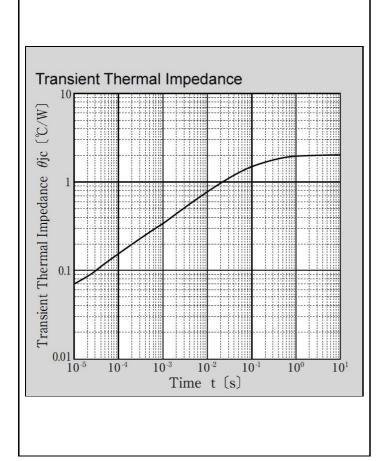


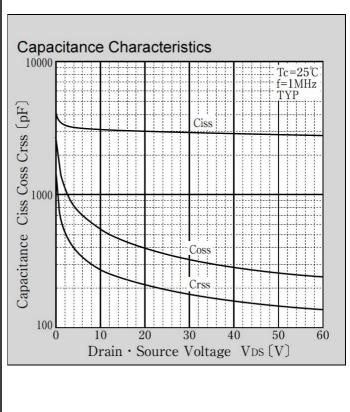


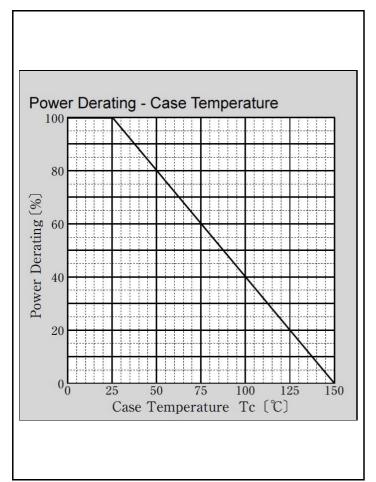


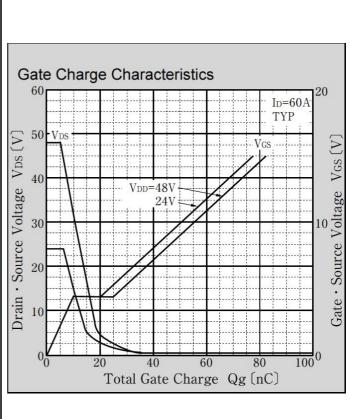


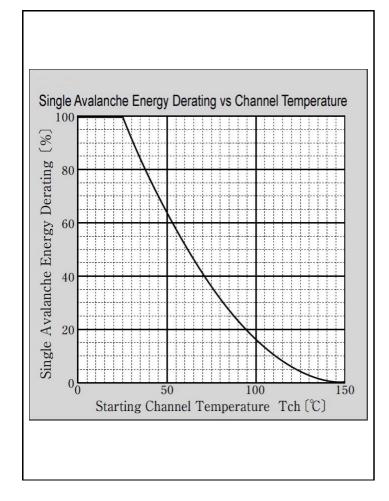






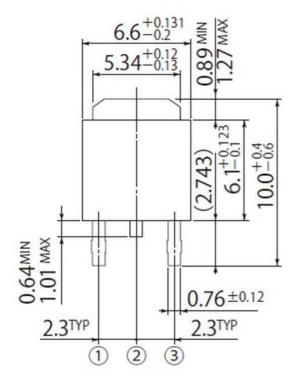


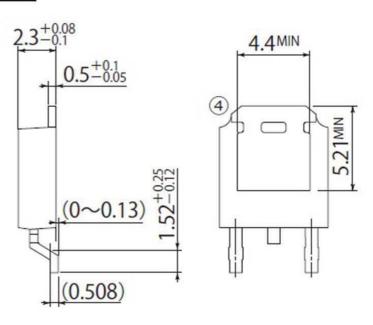


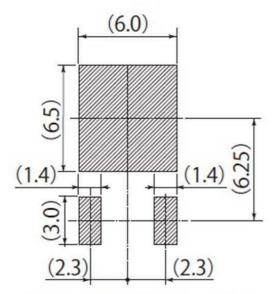


G2

JEDEC Code	TO-252AA		
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Referential Soldering Pad

[·] Optimize soldering pad to the board design and soldering condition.

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