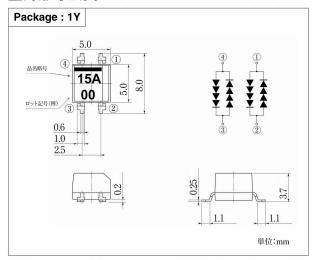
VRYA15

15V

■外形寸法図 OUTLINE DIMENSIONS



外形図については新電元 Web サイト又は〈半導体製品一覧表〉をご参照 下さい。捺印表示については捺印仕様をご確認下さい。

For details of outline dimensions, refer to our web site or the Semiconductor Short Form Catalog. As for the marking, refer to the specification "Marking, Terminal Connection."

■定格表 RATINGS

●絶対最大定格 Absolute Maximum Ratings(指定のない場合 T₁=25℃/unless otherwise specified)

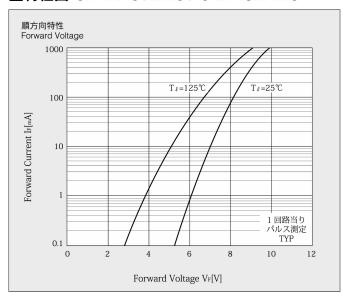
項 目 Item	記号 Symbol	条件 Conditions			規格値 Ratings	単位 Unit
保存温度 Storage Temperature	Tstg				−30~125	${\mathbb C}$
接合部温度 Junction Temperature	Tj				125	$^{\circ}$
出力電流 Average Rectified Forward Current	Io	Ta=40℃ 正弦波 Sine wave 抵抗負荷 R - load 商用周波数 Commercial fre- quency	アルミナ基板実装 On alumina substrate	1 回路通電 1 element operation	140	mArms
				2 回路通電 2 elements operation	90	
			プリント基板実装 On glass ‐ epoxy substrate	1 回路通電 1 element operation	90	
				2 回路通電 2 elements operation	50	
せん頭サージ順電流 Peak Surge Forward Current	Ifsm	50Hz 正弦波 50Hz Sine wave	非繰り返し Non-repetitive	2 回路直列通電 2 elements series operation	6.5	Arms
		10/200 μ s			50	A
		10/1000 μ s			24	

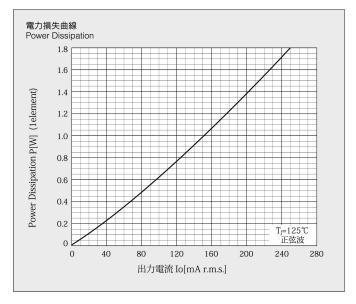
●電気的・熱的特性 TI=25℃ Electrical Characteristics TI=25℃(指定のない場合 T1=25℃/unless otherwise specified)

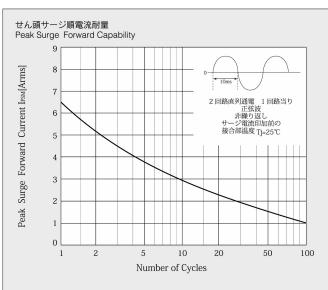
順電圧 Forward Voltage	VF	IF=1mA		1 回路 1 element	5.13~6.37	- V
				2 回路直列 2 elements series	10.25~12.75	
		I _F =10mA		1 回路 1 element	6.25~7.50	
				2 回路直列 2 elements series	12.50~15.0	
		IF=70mA		1 回路 1 element	7.13~8.37	
				2 回路直列 2 elements series	14.25~16.75	
接合容量 Junction Capacitance	Сј	f=100kHz			TYP 5	pF
熱抵抗 Thermal Resistance	θ ја	接合部・周囲間 Junction to ambient	アルミナ基板実装 On alumina substrate	1 回路通電 1 element operation	MAX 90	- °C /W
				2 回路通電 2 elements operation	MAX 150	
			プリント基板実装 On glass - epoxy substrate	1 回路通電 1 element operation	MAX 150	
				2 回路通電 2 elements operation	MAX 250	

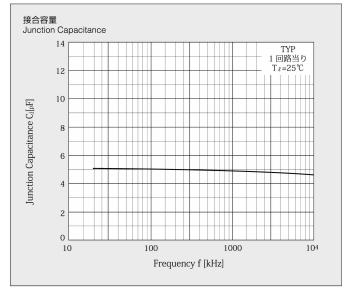
1Y Package VRYA15

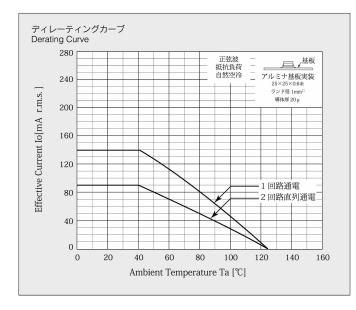
■特性図 CHARACTERISTIC DIAGRAMS

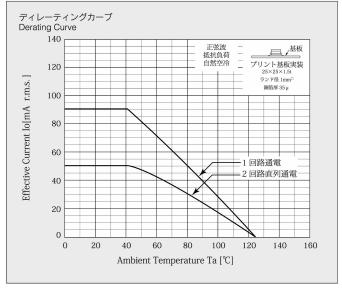












- * Sine wave は 50Hz で測定しています。
- * 50Hz sine wave is used for measurements.

75

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【特定用途】

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