

Features

- Up to 94% Efficiency
- Low voltage start-up: 0.9V
- Shut-down current: < 1μA
- Input voltage: 0.9V~4.4V
- Output voltage: 2.5V~4.3V (Up to 5V with Schottky)
- Low switch on resistance $R_{DS(ON)}$, Internal switch: 0.35 Ω
- 1.4MHz fixed frequency switching
- Automatic PWM/PFM mode switching
- Low profile SOT-23-6L package (lead-free packaging is now available)

Applications

- Digital cameras and MP3
- Palmtop computers / PDAs
- Cellular phones
- Wireless handsets and DSL modems
- PC cards
- Portable media players

Order Information

SD6201 - ① ②

Symbol	Description
①	Denotes Output voltage: A : Adjustable Output
②	Denotes Package Types: F: SOT-23-6L

Description

The SD6201 is high efficiency synchronous, PWM step-up DC/DC converters optimized to provide a high efficient solution to medium power systems. The devices work under the input voltage between 0.9V and 4.4V with a 1.4MHz fixed frequency switching. These features minimize overall solution footprint by allowing the use of tiny, low profile inductors and ceramic capacitors. Automatic PWM/PFM mode switching at light load saves power and improves efficiency.

The SD6201 is capable of supplying an output voltage between 2.5V and 4.3V, the internal synchronous switch is desired to provide high efficiency without Schottky.

The SD6201 regulators are available in the industry standard SOT-23-6L power packages (or upon request).

Typical Applications

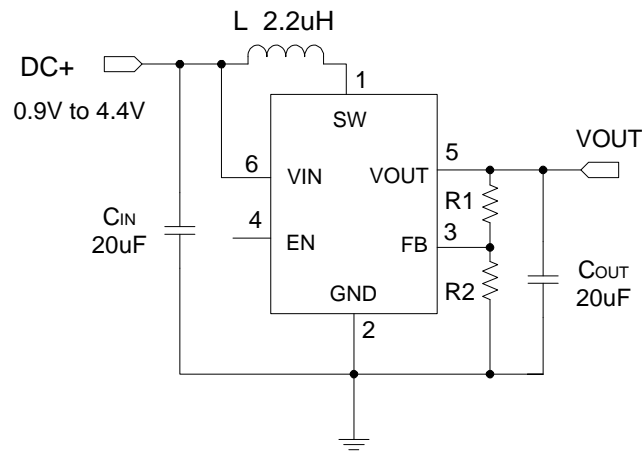


Figure 1: Typical Application Circuit

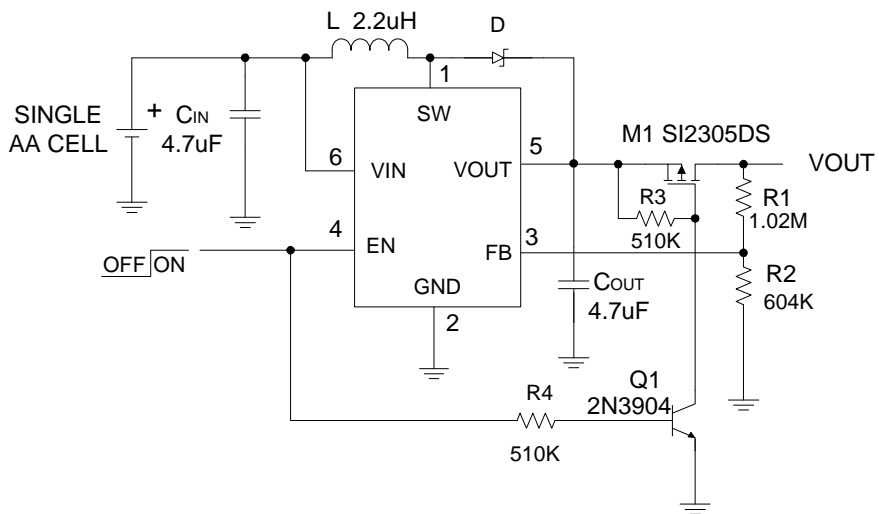


Figure 2: Single Cell to 3.3V Synchronous Boost Converter with Load Disconnect in Shutdown.

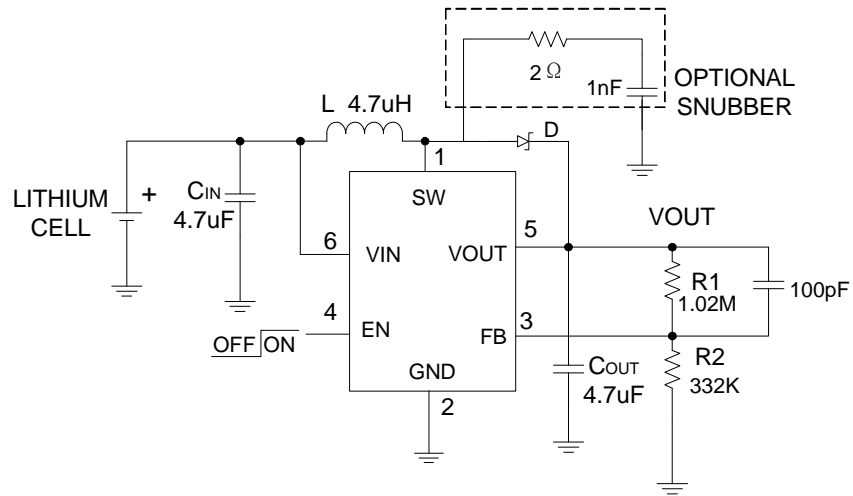


Figure 3: Single Lithium Cell to 5V with Load 250mA

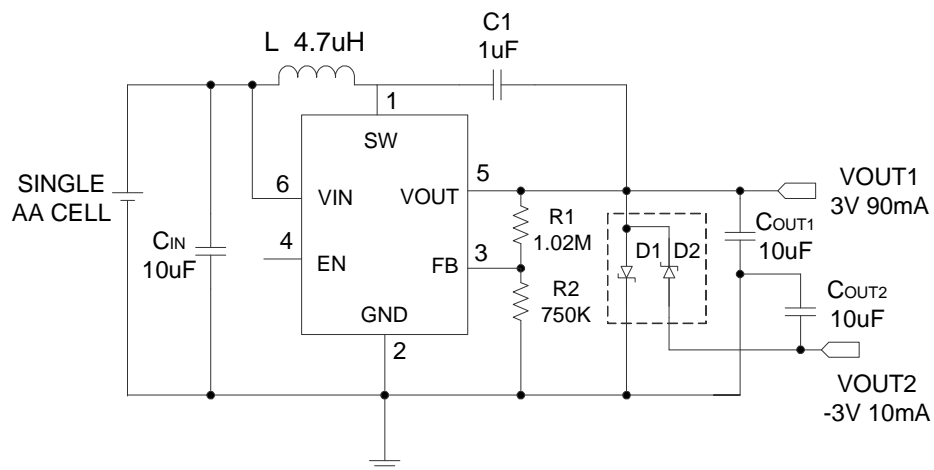
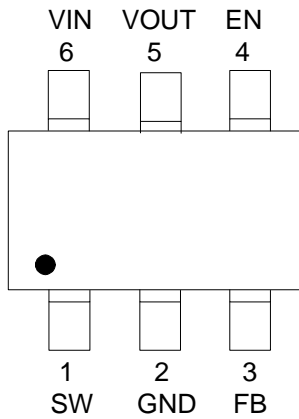


Figure 4: Single Cell AA Cell to $\pm 3V$ Synchronous Boost Converter

Pin Assignment

TOP VIEW



SOT-23-6L

* Marking:HxN-Xh

PIN Number SOT-23- 6L	PIN Name	Function
1	SW	Switch Output
2	GND	Ground
3	FB	Feedback
4	EN	ON/OFF Control (High Enable)
5	VOUT	Output
6	VIN	Input

Absolute Maximum Ratings (Note 1)

- V_{IN} - 0.3 V ~ + 6.6V
- V_{OUT} - 0.3 V ~ + 6.6V
- V_{SW} - 0.3 V ~ + 6.6 V
- V_{EN} - 0.3 V ~ + 6.6 V
- Operating Temperature Range (Note 2)- 30°C ~ + 85°C
- Lead Temperature (Soldering 10 sec.)+ 300°C
- Storage Temperature Range- 65°C ~ + 125°C

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: The SD6201 are guaranteed to meet performance specifications from 0°C to 70°C. Specifications over the – 30°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

Electrical Characteristics

Operating Conditions: $T_A=25^{\circ}\text{C}$, $V_{\text{IN}}=1.2\text{V}$, $V_{\text{OUT}}=3.3\text{V}$ unless otherwise specified.

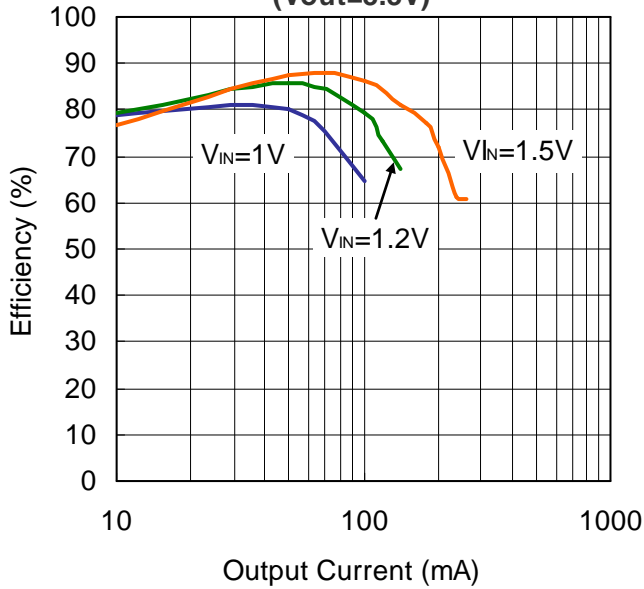
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Output Voltage Range (Adj.)		2.5		5	V
Minimum Start-Up Voltage (Note 3)	$I_{\text{LOAD}} = 1\text{mA}$		0.9	1.1	V
Minimum Operating Voltage	$\text{EN} = V_{\text{IN}}$		0.6	0.75	V
Switching Frequency		1.1	1.4	1.7	MHz
Max Duty Cycle	$V_{\text{FB}} = 1.15\text{V}$	80	87		%
Feedback Voltage		1.165	1.212	1.241	V
Feedback Input Current	$V_{\text{FB}} = 1.22\text{V}$ (Note 4)		1		nA
NMOS Switch Leakage	$V_{\text{SW}} = 5\text{V}$		0.1	5	μA
PMOS Switch Leakage	$V_{\text{SW}} = 0\text{V}$		0.1	5	μA
NMOS Switch On Resistance	$V_{\text{OUT}} = 3.3\text{V}$		0.35		Ω
PMOS Switch On Resistance	$V_{\text{OUT}} = 3.3\text{V}$		0.45		Ω
NMOS Current Limit		700	850		mA
Quiescent Current	$I_{\text{LOAD}} = 0\text{mA}$		260		μA
Shutdown Current	$V_{\text{EN}}=0\text{V}$, Including Switch Leakage		0.1	1	μA
En Input High		1			V
En Input Low				0.5	V
En Input Current	$V_{\text{EN}} = 5.5\text{V}$		0.01	1	μA

Note 3: Minimum V_{IN} operation after start-up is only limited by the battery's ability to provide the necessary power as it enters a deeply discharged state.

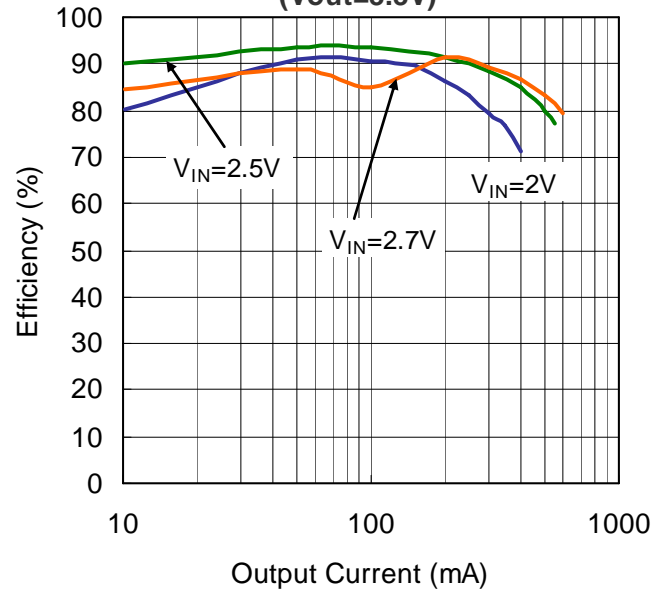
Note 4: Specification is guaranteed by design and not 100% tested in production.

Typical Performance Characteristics

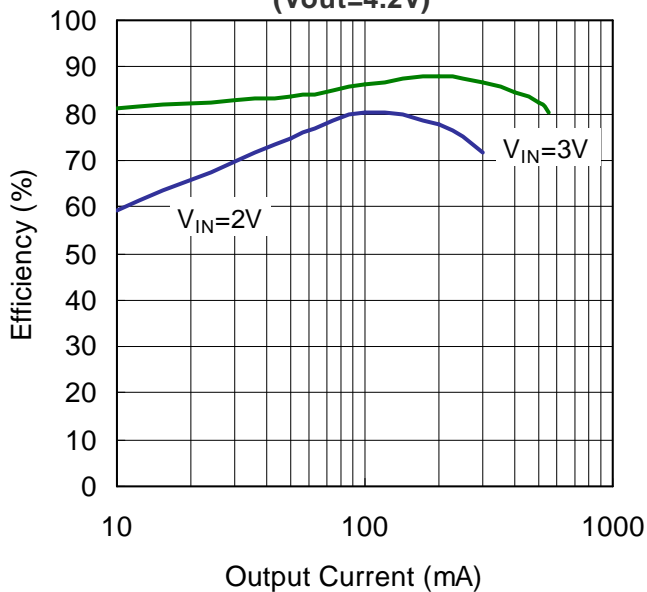
Efficiency vs. Output Current
($V_{out}=3.3V$)



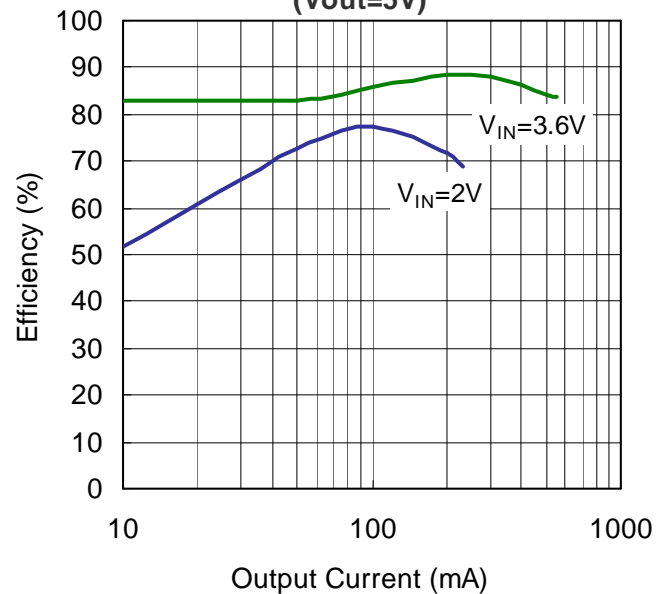
Efficiency vs. Output Current
($V_{out}=3.3V$)



Efficiency vs. Output Current
($V_{out}=4.2V$)



Efficiency vs. Output Current
($V_{out}=5V$)



Pin Description

SW (Pin1): Switch Pin. Connect inductor between SW and V_{IN} . Keep these PCB trace lengths as short and wide as possible to reduce EMI and voltage overshoot.

GND (Pin 2): Signal and Power Ground. Provide a short direct PCB path between GND and the (-) side of the output capacitor(s).

FB (Pin 3): Feedback Input to the g_m Error Amplifier. Connect resistor divider tap to this pin. The output voltage can be adjusted from 2.5V to 5V by: $V_{OUT} = 1.212V \cdot [1 + (R1/R2)]$

EN (Pin 4): Logic Controlled Shutdown Input. EN = High: Normal free running operation, 1.4MHz typical operating frequency. EN = Low: Shutdown, quiescent current $<1\mu A$. Output capacitor can be completely discharged through the load or feedback resistors.

VOUT (Pin 5): Output Voltage Sense Input and Drain of the Internal Synchronous Rectifier MOSFET. Bias is derived from V_{OUT} . PCB trace length from V_{OUT} to the output filter capacitor(s) should be as short and wide as possible.

VIN (Pin 6): Battery Input Voltage. The device gets its start-up bias from V_{IN} . Once V_{OUT} exceeds V_{IN} , bias comes from V_{OUT} . Thus, once started, operation is completely independent from V_{IN} . Operation is only limited by the output power level and the battery's internal series resistance.

Applications Information

Inductor Selection

The SD6201 can utilize small surface mount and chip inductors due to their fast 1.4MHz switching frequency. A minimum inductance value of 3.3uH is necessary for 3.6V and lower voltage applications and 4.7uH for output voltages greater than 3.6V. Larger values by reducing the inductor ripple current. Increasing the inductance above 10uH will increase size while providing little improvement in output current capability.

The inductor current ripple is typically set for 20% to 40% of the maximum inductor current (I_P). High frequency ferrite core inductor materials reduce frequency dependent power losses compared to cheaper powdered iron types, improving efficiency. The inductor should have low ESR (series resistance of the windings) to reduce the I^2R power losses, and must be able to handle the peak inductor current without saturating. Molded chokes and some chip inductors usually do not have enough core to support the peak inductor currents of 850mA seen on the SD6201. To minimize radiated noise, use a toroid, pot core or shielded bobbin inductor. See Table 1 for some suggested components and suppliers.

Output and Input Capacitor Selection

Low ESR (equivalent series resistance) capacitors should be used to minimize the output voltage ripple. Multilayer ceramic capacitors are an excellent choice as they have extremely low ESR and are available in small footprints. A 4.7 μ F to 15 μ F output capacitor is sufficient for most applications. Larger values up to 22 μ F may be used to obtain extremely low output voltage ripple and improve transient response. An additional phase lead capacitor may be required with output capacitors larger than 10 μ F to maintain acceptable phase margin. X5R and X7R dielectric materials are preferred for their ability to maintain capacitance over wide voltage and temperature ranges.

Low ESR input capacitors reduce input switching noise and reduce the peak current drawn from the battery. It follows that ceramic capacitors are also a good choice for input decoupling and should be located as close as possible to the device. A 10 μ F input capacitor is sufficient for virtually any application. Larger values may be used without limitations.

Table 1: Suggested Inductors

Vendor	Part	Inductance (uH)	MAM DCR (mΩ)	Height (mm)
Sumida	CDRH5D18-4R1	4.1	57	2.0
	CDRH5D18-100	10	124	2.0
	CDRH3D16-4R7	4.7	105	1.8
	CDRH3D16-6R8	6.8	170	1.8
	CR43-4R7	4.7	109	3.5
	CR43-100	10	182	3.5
	CMD4D06-4R7MC	4.7	216	0.8
	CMD4D06-3R3MC	3.3	174	0.8
Coilcraft	DS1608-472	4.7	60	2.9
	DS1608-103	10	75	2.9
	DS1608C-472	4.7	90	2.9
Toko	D52LC-4R7M	4.7	84	2.0
	D52LC-100M	10	137	2.0
Murata	LQH3C4R7M24	4.7	195	2.2

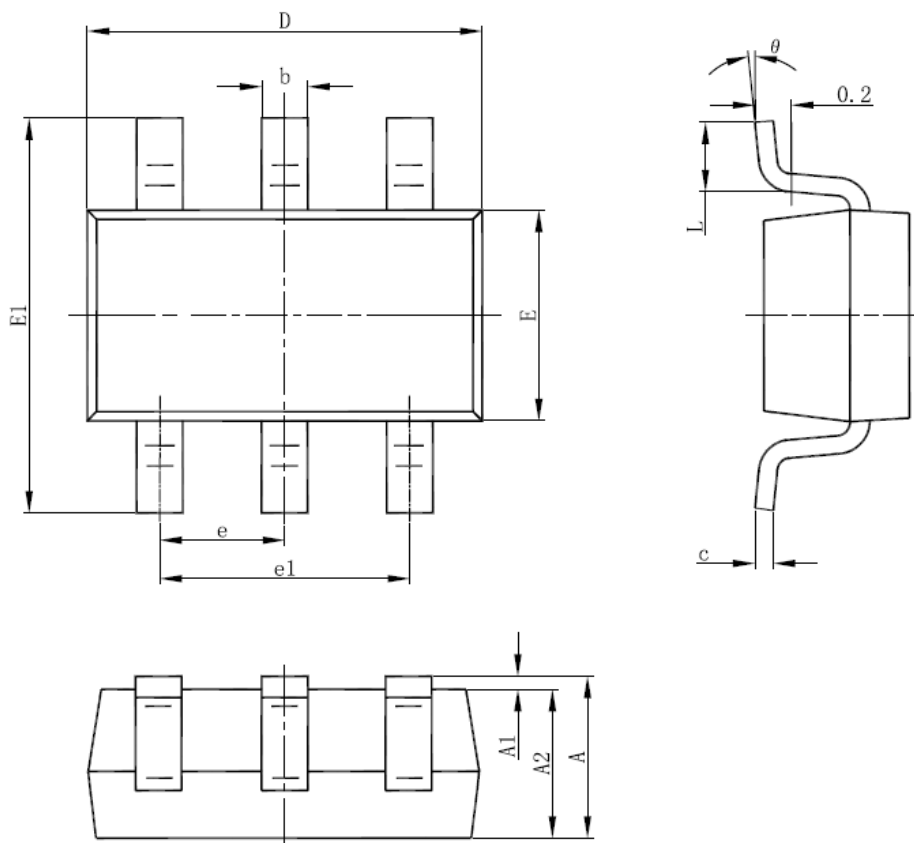
PCB Layout Guidelines

The high speed operation of the SD6201 demands careful attention to board layout. You will not get advertised performance with careless layout. A large ground pin copper area will help to lower the chip temperature. A multilayer board with a separate ground plane is ideal, but not absolutely necessary.

Recommended component placement: traces carrying high current are direct. Trace area at FB pin is small. Lead length to battery is short.

Packaging Information

SOT-23-6L Surface Mount Package



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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