600mA, 1.5MHz Synchronous Step-Down Converter

The SD8906A is a constant frequency, current mode PWM step-down converter.The device integrates a main switch and a synchronous rectifier for high efficiency without an external Schottky diode. It is ideal for powering portable equipment that runs from a single cell Lithium-Ion (Li+) battery. The output voltage can be regulated as low as 0.6 V . The SD8906A can also run at $100 \%$ duty cycle for low dropout operation, extending battery life in portable system. This device offers two operation modes, PWM control and PFM Mode switching control, which allows a high efficiency over the wider range of the load.

The SD8906A is offered in a low profile (1mm)5-pin, thin SOT package, and is available in an adjustable version and fixed output voltage of $1.2 \mathrm{~V}, 1.5 \mathrm{~V}, 1.8 \mathrm{~V}, 2.5 \mathrm{~V}$ and 3.3 V .

## FEATURES

- High Efficiency: Up to $96 \%$
- 1.5 MHz Constant Frequency Operation
- 600mA Output Current
- No Schottky Diode Required
- 2.3 V to 5.5 V Input Voltage Range
- Short Circuit Protection
- $\quad<1 \mu \mathrm{~A}$ Shutdown Current
- Tiny SOT23-5 Package


## APPLICATIONS

- Digital Still and Video Cameras
- Wireless and DSL Modems
- PDAs
- MP3 Players
- Cellular and Smart Phones


## Typical Application



Figure 1. Basic Application Circuit

- Output Voltage as Low as 0.6 V
- 100\% Duty Cycle in Dropout
- Low Quiescent Current: $35 \mu \mathrm{~A}$
- Slope Compensated Current Mode Control for Excellent Line and Load Transient Response
- Thermal Fault Protection
- Inrush Current Limit and Soft Start

Efficiency us. Output Current


SD8906A
Absolute Maximum Ratings ${ }^{\text {(Note 1) }}$


Package/Order Information


| Part Number | OUTPUT <br> VOLTAGE (V) | OUTPUT <br> CURRENT (A) | SWICHING <br> FREQUENCY | Temp Range |
| :---: | :---: | :---: | :---: | :---: |
| SD8906A | ADJ | 0.6 | 1.5 MHz | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |

## Pin Description

| PIN | NAME | FUNCTION |
| :---: | :---: | :--- |
| 1 | RUN | Chip Enable Pin. Drive RUN above 1.5V to turn on the part. Drive <br> RUN below 0.3V to turn it off. Do not leave RUN floating. |
| 2 | GND | Ground Pin |
| 3 | SW | Power Switch Output. It is the switch node connection to Inductor. <br> This pin connects to the drains of the internal P-ch and N-ch MOSFET <br> switches. |
| 4 | VIN | Power Supply Input. Must be closely decoupled to GND, Pin 2, with a <br> $4.7 \mu F ~ o r ~ g r e a t e r ~ c e r a m i c ~ c a p a c i t o r . ~$ |
| 5 | VOUT | Output Voltage Feedback Pin. An internal resistive divider divides the <br> output voltage down for comparison to the internal reference voltage. |

Electrical Characteristics ${ }^{\text {Nowes }}$
$\left(\mathrm{V}_{\text {IN }}=\mathrm{V}_{\mathrm{RUN}}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=1.8 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$

| Parameter | Conditions | MIN | TYP | MAX | unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage Range |  | 2.7 |  | 6 | V |
| UVLO Threshold |  | 2.2 | 2.4 | 2.5 | V |
| Input DC Supply Current <br> PWM Mode <br> PFM Mode <br> Shutdown Mode | $\begin{aligned} & (\text { Note } 4) \\ & \text { Vout }=90 \%, \text { lload }=0 \mathrm{~mA} \\ & \text { Vout }=105 \%, \text { lload }=0 \mathrm{~mA} \\ & \mathrm{~V}_{\text {RUN }}=0 \mathrm{~V}, \mathrm{~V}_{\text {IN }}=4.2 \mathrm{~V} \\ & \hline \end{aligned}$ |  | $\begin{array}{r} 200 \\ 20 \\ 0.1 \end{array}$ | $\begin{array}{r} 400 \\ 35 \\ 1.0 \end{array}$ | $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ |
| Regulated Feedback Voltage | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | 0.588 | 0.600 | 0.612 | V |
|  | $\mathrm{T}_{\mathrm{A}}=0^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq 85^{\circ} \mathrm{C}$ | 0.586 | 0.600 | 0.613 | V |
|  | $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq 85^{\circ} \mathrm{C}$ | 0.585 | 0.600 | 0.615 | V |
| Reference Voltage Line Regulation | Vin=2.7V to 5.5 V |  | 0.04 | 0.40 | \%/V |
| Output Voltage Line Regulation | $\mathrm{V}_{\mathrm{IN}}=2.7 \mathrm{~V}$ to 5.5 V |  | 0.04 | 0.4 | \% |
| Output Voltage Load Regulation |  |  | 0.5 |  | \% |
| Oscillation Frequency | $\begin{aligned} & \text { Vout=100\% } \\ & \text { Vout=0V } \end{aligned}$ | 1.3 | 1.5 | 1.8 | MHz |
|  |  |  | 300 |  | KHz |
| On Resistance of PMOS | $\mathrm{l}_{\mathrm{sw}}=100 \mathrm{~mA}$ |  | 0.4 | 0.45 | $\Omega$ |
| ON Resistance of NMOS | $\mathrm{l}_{\mathrm{sw}}=-100 \mathrm{~mA}$ |  | 0.35 | 0.45 | $\Omega$ |
| Peak Current Limit | $\mathrm{V}_{\text {IN }}=3 \mathrm{~V}$, Vout $=90 \%$ |  | 1.5 |  | A |
| RUN Threshold |  | 0.30 | 1.0 | 1.50 | V |
| RUN Leakage Current |  |  | $\pm 0.01$ | $\pm 1.0$ | $\mu \mathrm{A}$ |
| SW Leakage Current | $\mathrm{V}_{\mathrm{RUN}}=0 \mathrm{~V}, \mathrm{~V}_{\text {IN }}=\mathrm{Vsw}=5 \mathrm{~V}$ |  | $\pm 0.01$ | $\pm 1.0$ | $\mu \mathrm{A}$ |

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.
Note 2: $T_{J}$ is calculated from the ambient temperature $T_{A}$ and power dissipation $P_{D}$ according to the following formula: $\quad T_{J}=T A+(P D) \times\left(250^{\circ} \mathrm{C} / \mathrm{W}\right)$.
Note3: $100 \%$ production test at $+25^{\circ} \mathrm{C}$. Specifications over the temperature range are guaranteed by design and characterization.

Note 4: Dynamic supply current is higher due to the gate charge being delivered at the switching frequency

## Typical Performance Characteristics




## Functional Block Diagram



Figure 2. SD8906A Block Diagram

## Functional Description

The SD8906A is a high performance 600 mA 1.5 MHz monolithic step-down converter. The SD8906A requires only three external power components (Cin, Cout and L). The adjustable version can be programmed with external feedback to any voltage, ranging from 0.6 V to the input voltage.
At dropout, the converter duty cycle increases to $100 \%$ and the output voltage tracks the input voltage minus the Rdson drop of the high-side MOSFET.
The internal error amplifier and compensation provides excellent transient response, load, and line regulation. Soft start function prevents input inrush current and output overshoot during start up.

## APPLICATIONS INFORMATION

## Inductor Selection

For most designs, SD8906A operates with inductors of $1 \mu \mathrm{H}$ to $4.7 \mu \mathrm{H}$. Low inductance values are physically smaller but require faster switching, which results in some efficiency loss. The inductor value can be derived from the following equation:

$$
L=\frac{V_{\text {OUT }} \times\left(V_{\text {IN }}-V_{\text {OUT }}\right)}{V_{\text {IN }} \times \Delta I_{L} \times f_{\text {OSC }}}
$$

Where $\Delta I_{L}$ is inductor Ripple Current.
Large value inductors result in lower ripple current and small value inductors result in high ripple current. For optimum voltage-positioning load transients, choose an inductor with $D C$ series resistance in the $50 \mathrm{~m} \Omega$ to $150 \mathrm{~m} \Omega$ range.

## Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency should be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. A $4.7 \mu \mathrm{~F}$ ceramic capacitor for most applications is sufficient. A large value may be used for improved input voltage filtering.

## Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current ratings. The output ripple $V_{\text {OUT }}$ is determined by:

$$
\Delta V_{\text {OUT }} \leq \frac{V_{\text {OUT }} \times\left(V_{\text {IN }}-V_{\text {OUT }}\right)}{V_{I N} \times f_{\text {OSC }} \times L} \times\left(E S R+\frac{1}{8 \times f_{\text {OSC }} \times C 3}\right)
$$

A $10 \mu \mathrm{~F}$ ceramic can satisfy most applications.

## PC Board Layout Checklist

When laying out the printed circuit board, the following checking should be used to ensure proper operation of SD8906A. Check the following in your layout:

1. The power traces, consisting of the GND trace, the SW trace and the VIN trace should be kept short, direct and wide.
2. Does the (+) plates of Cin connect to Vin as closely as possible? This capacitor provides the AC current to the internal power MOSFETs.
3. Keep the switching node, SW, away from the sensitive VOUT node.
4. Keep the (-) plates of Cin and Cout as close as possible

## Package Description


5LD SOT-23 PACKAGE OUTLINE DIMENSIONS ALL DIMENSIONS $\operatorname{IN}$ MM.

| Dimension | Min. | Max. |
| :---: | :---: | :---: |
| A | 0.9 | 1.10 |
| A1 | 0.01 | 0.13 |
| B | 0.3 | 0.5 |
| c | 0.09 | 0.2 |
| 0 | 2.8 | 3.0 |
| H | 2.5 | 3.1 |
| E | 1.5 | 1.7 |
| e | 0.95 PEEF. |  |
| e1 | 1.90 ReF. |  |
| 11 | 0.2 | 0.55 |
| 1 | 0.35 | 0.8 |
| 0 | 0 | $10^{\circ}$ |



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