

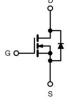
Sinai Power Technologies

SPA24N50G www.sinai-power.com

N-channel Power MOSFET

PRODUCT SUMMARY				
V _{DS} (V) at TJ max.	500			
R _{DS(on)} max. at 25 ^o C (Ω)	V _{GS} =10V	0.2		
Q _g max. (nC)	160			
Q _{gs} (nC)	30			
Q _{gd} (nC)	53			
Configuration	single			





TO-247

Schematic diagram

Features

- ID=24A(Vgs=10V)
- Ultra Low Gate Charge
- Improved dv/dt Capability
- 100% Avalanche Tested
- R_oHS compliant

Applications

- Switching Mode Power Supplies (SMPS)
- **PWM Motor Controls**
- DC to DC Converters
- LED Lighting
- **Bridge Circuits**

ORDERING INFORMATION			
Device	SPA24N50G		
Device Package	TO-247		
Marking	24N50G		

ABSOLUTE MAXIMUM RATINGS (T _C = 25°C, unless otherwise noted)					
Parameter	Cymhol	Limit	Unit		
Parameter	Symbol	SPA24N50G			
Drain to Source Voltage	V _{DSS}	500	V		
Continuous Drain Current (@Tc=25°C)		24 (1)	A		
Continuous Drain Current (@Tc=100°C)	I _D	15 ⁽¹⁾	A		
Drain current pulsed ⁽²⁾	I _{DM}	96 (1)	A		
Gate to Source Voltage	V _{GS}	±30	V		
Single pulsed Avalanche Energy ⁽³⁾	E _{AS}	2016	mJ		
Peak diode Recovery dv/dt ⁽⁴⁾	dv/dt	6	V/ns		
Total power dissipation (@T _C =25°C)	P _D	280	W		
Derating Factor above 25°C	- 'D	2.2	W/ºC		
Operating Junction Temperature & Storage Temperature	T _{STG} , T _J	-55 to + 150	°C		
Maximum lead temperature for soldering purpose	TL	260	°C		

Notes

1. Drain current is limited by maximum junction temperature.

- 2. Repetitive rating : pulse width limited by junction temperature.
- 3. L = 7mH, I_{AS} = 24A, V_{DD} = 50V, R_G =25 Ω , Starting at T_J = 25°C 4. $I_{SD} \le ID$, di/dt = 100A/us, $V_{DD} \le BV_{DSS}$, Starting at T_J =25°C



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THERMAL CHARACTERISTICS				
Parameter	Symbol	Value	Unit	
Thermal resistance, Junction to case	R _{thjc}	0.44	°C/W	
Thermal resistance, Junction to ambient	R _{thja}	38	°C/W	

ELECTRICAL CHARACTERISTICS (To Parameter		Test conditions	Min.	Typ	Max.	Unit
	Symbol	I est conditions	MIN.	Тур.	wax.	Unit
Off Characteristics					-	1
Drain to source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	500			V
Breakdown voltage temperature coefficient	ΔBV _{DSS} / ΔTJ	I _D =250uA, referenced to 25°C		0.49		V/°C
Drain to source leakage current	I _{DSS}	V _{DS} =500V, V _{GS} =0V			1	uA
		V _{DS} =500V, T _C =125°C			50	uA
Gate to source leakage current, forward	I _{GSS}	V _{GS} =30V, V _{DS} =0V			100	nA
Gate to source leakage current, reverse	IGSS	V _{GS} =-30V, V _{DS} =0V			-100	nA
On Characteristics		· · · · · · · · · · · · · · · · · · ·				
Gate threshold voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250uA	2		4	V
Drain to source on state resistance	R _{DS(ON)}	V _{GS} =10V, I _D =12A		0.15	0.2	Ω
Forward Transconductance	Gfs	V _{DS} = 30 V, I _D = 12A		22		S
Dynamic Characteristics						
Input capacitance	C _{iss}	V _{GS} =0V, V _{DS} =25V, f=1MHz		5240		
Output capacitance	Coss			472		pF
Reverse transfer capacitance	Crss			15		
Turn on delay time	t _{d(on)}			76		
Rising time	tr	V _{DS} =250V, I _D =24A ,		82		
Turn off delay time	t _{d(off)}	R _G =25Ω		198		ns
Fall time	t _f			78		1
Total gate charge	Qg	V _{DS} =400V, V _{GS} =10V, I _D =24A		118	160	
Gate-source charge	Q _{gs}			30		nC
Gate-drain charge	Q _{gd}			53		

SOURCE TO DRAIN DIODE RATINGS CHARACTERISTICS						
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Continuous source current	Is	Integral reverse p-n Junction . diode in the MOSFET			24	А
Pulsed source current	I _{SM}				96	А
Diode forward voltage drop.	V _{SD}	I _S =24A, V _{GS} =0V			1.4	V
Reverse recovery time	Trr	I _S =24A, V _{GS} =0V, dI _F /dt=100A/us		520		ns
Reverse recovery Charge	Qrr			10		uC



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Fig1. Output characteristics

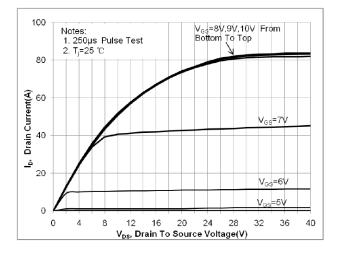


Fig3. Gate charge characteristics

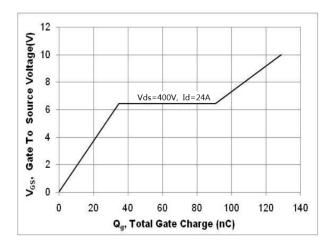


Fig 5. RDS(ON) vs junction temperature

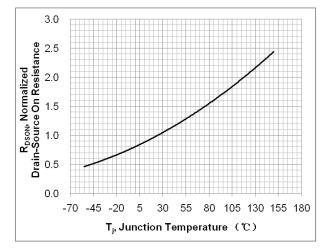


Fig2. Drain-source on-state resistance

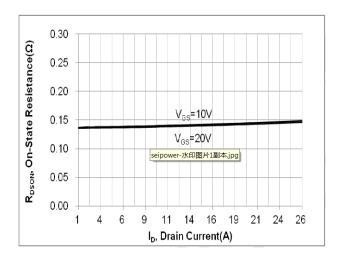


Fig 4. Capacitance Characteristics

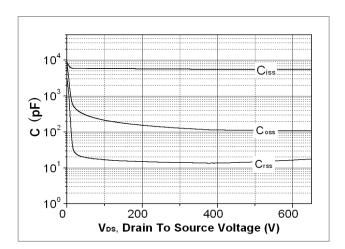
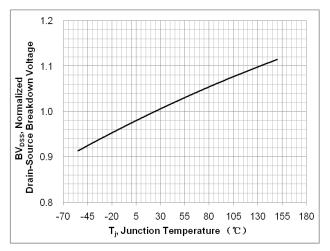


Fig 6. BVDss vs junction temperature



17-1211-Rev 00

3 For technical questions, contact: <u>Tech@Sinai-power.com</u>.

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Fig 7. Safe operating area

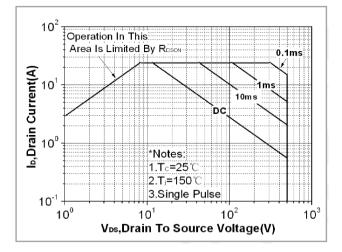


Fig 8. Transient thermal impedance

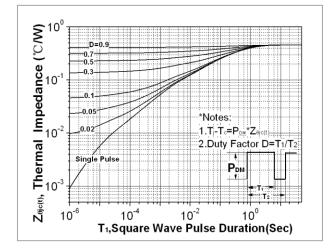


Fig 9. Forward characteristics of reverse diode

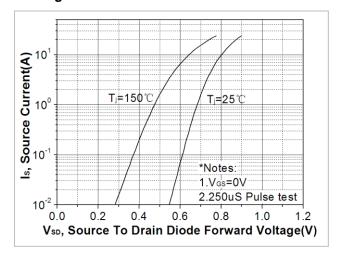
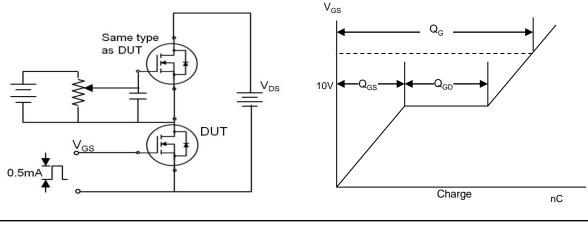


Fig 10. Gate charge test circuit & waveform



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Fig 11. Switching time test circuit & waveform

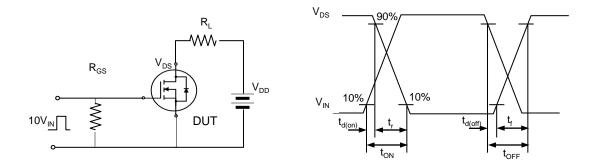


Fig 12. Unclamped Inductive switching test circuit & waveform

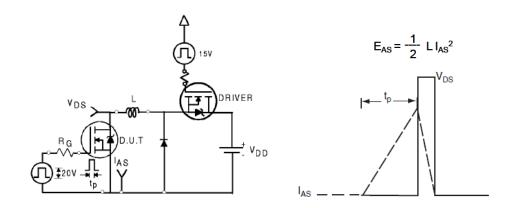
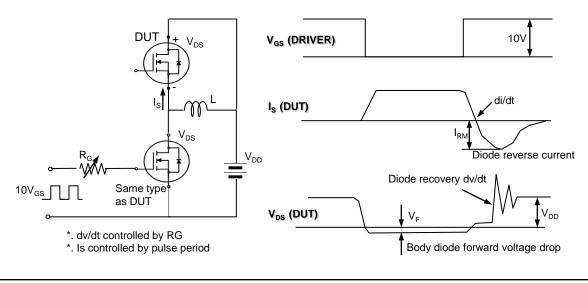


Fig 13. Peak diode recovery dv/dt test circuit & waveform



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