

Sinai Power Technologies

<u>SPC18N50G</u>

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N-channel Power MOSFET

PRODUCT SUMMARY				
V _{DS} (V) at TJ max.	550			
R _{DS(on)} max. at 25°C (Ω)	V _{GS} =10V	0.30		
Q _g max. (nC)	8	8		
Q _{gs} (nC)	2	1		
Q _{gd} (nC)	2	8		
Configuration	single			





TO-220F

Schematic diagram

Features

- $I_D=18A(Vgs=10V)$
- Ultra Low Gate Charge
- Improved dv/dt Capability
- 100% Avalanche Tested
- ROHS compliant

Applications

- Switching Mode Power Supplies (SMPS)
- PWM Motor Controls
- DC to DC Converters
- LED Lighting
- Bridge Circuits

ORDERING INFORMATION

Device	SPC18N50G
Device Package	TO-220F
Marking	18N50G

ABSOLUTE MAXIMUM RATINGS ($T_c = 25^{\circ}C$, unless otherwise noted)				
Parameter	Symbol	Value	Unit	
Drain to Source Voltage	V _{DSS}	500	V	
Continuous Drain Current (@T _c =25°C)		18 ⁽¹⁾	A	
Continuous Drain Current (@T _c =100°C)		13 ⁽¹⁾	A	
Drain current pulsed ⁽²⁾	I _{DM}	72 ⁽¹⁾	A	
Gate to Source Voltage	V _{GS}	±30	V	
Single pulsed Avalanche Energy ⁽³⁾	E _{AS}	1200	mJ	
MOSFET dv/dt ruggedness (@V _{DS} =0~400V)	dv/dt	25	V/ns	
Peak diode Recovery dv/dt ⁽⁴⁾	dv/dt	15	V/ns	
Total power dissipation (@T _C =25°C)		40	W	
Derating Factor above 25°C	- P _D	0.3	W/ºC	
Operating Junction Temperature & Storage Temperature	T_{STG}, T_J	-55 to + 150	°C	
Maximum lead temperature for soldering purpose	TL	260	°C	
Mounting torque ⁽⁵⁾		0.4~0.6	N.m	

Notes

- 1. Drain current is limited by maximum junction temperature.
- 2. Repetitive rating : pulse width limited by junction temperature.
- 3. L = 6mH, I_{AS} = 18A, V_{DD} = 50V, R_G =25 Ω , Starting at T_J = 25°C
- 4. $I_{SD} \leq I_D$, di/dt = 100A/us, $V_{DD} \leq BV_{DSS}$, Starting at $T_J = 25^{\circ}C$

5. Mounting consideration for TO220 Fullpack:

M3 screw plus flat washer is suggested, free of burr between devices and contact area,

the devices are to be mounted to a hole not larger than 3.6mm in contact diameter (chamfer included).



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THERMAL CHARACTERISTICS				
Parameter	Symbol	Value		
	Symbol	SPC18N50G		
Thermal resistance, Junction to case	R _{thjc}	3.0	°C/W	
Thermal resistance, Junction to ambient	R _{thja}	46	°C/W	

ELECTRICAL CHARACTERISTICS ($T_c = 25^{\circ}C$ unless otherwise specified)						
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Off Characteristics						
Drain to source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	500			V
Breakdown voltage temperature coefficient	ΔBV _{DSS} / ΔTJ	I _D =250uA, referenced to 25°C		0.51		V/°C
Drain to source leakage current	I _{DSS}	V _{DS} =500V, V _{GS} =0V			1	uA
		V _{DS} =400V, T _C =125°C			50	uA
Gate to source leakage current, forward	1	V _{GS} =30V, V _{DS} =0V			100	nA
Gate to source leakage current, reverse	IGSS	V _{GS} =-30V, V _{DS} =0V			-100	nA
On Characteristics						
Gate threshold voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250uA	3		5	V
Drain to source on state resistance	R _{DS(ON)}	V _{GS} =10V, I _D =9 A		0.25	0.30	Ω
Forward Transconductance	Gfs	V _{DS} = 30 V, I _D = 9 A		18.5		S
Dynamic Characteristics		<u> </u>				
Input capacitance	Ciss	$V_{DS} = 30 \text{ V}, \text{ I}_{D} = 9 \text{ A}$		4670		pF
Output capacitance	Coss			315		
Reverse transfer capacitance	Crss			18.5		
Turn on delay time	t _{d(on)}			49		
Rising time	tr	V _{DS} =250V, I _D =18 ,		65		- ns
Turn off delay time	t _{d(off)}	R _G =25Ω		232		
Fall time	t _f			81		
Total gate charge	Qg	V _{DS} =400V, V _{GS} =10V, I _D =18A		88		
Gate-source charge	Q _{gs}			22		nC
Gate-drain charge	Q_gd			29		
Gate Resistance	Rg	V _{DS} =0V, Scan F mode		1		Ω

SOURCE TO DRAIN DIODE RATINGS CHARACTERISTICS						
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit
Continuous source current	Is	Integral reverse p-n Junction diode in the MOSFET			18	А
Pulsed source current	I _{SM}				72	А
Diode forward voltage drop.	V _{SD}	I _S =18A, V _{GS} =0V			1.3	V
Reverse recovery time	Trr	I _S =18A, V _{GS} =0V, dI⊧/dt=100A/us		461		ns
Reverse recovery Charge	Qrr			5.5		uC

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